









I\_High\_Side\_NMOS\_Driver\_\_ Source\_Voltage\_In Gate\_Voltage\_Out

GVO is a fn of SVI with a offset of Ygs (10v) of the NMOS

| Notes: | 1. Driving frequency shouldn't increase more than 100kHz

<ol> <li>Back EMF Protection. √</li> <li>2x Channel.</li> <li>Able to handle 10A per channel with 30A peak, and 5-30V with 50V peak.</li> <li>PWM Input should be isolated (No common ground with MCU)</li> <li>LEDs indicating the direction of the Motor.</li> <li>Push button for testing the motor in both the directions.</li> <li>XT60 Terminals. √</li> <li>Should not jitter at low speeds.</li> <li>Should have a 100k resistor at output to provide a discharge path for Back EMF. √</li> <li>Should have a voltage feedback from the output terminals.</li> <li>Reverse Voltage Protection from the battery. √</li> <li>Over Current Protection. √</li> </ol>	Channel.  let to handle 10A per channel with 30A peak, i 5-30V with 50V peak.  WM Input should be isolated (No common ground with MCU). The should be isolated in the Motor. In the should not for testing the motor in both the directions. in the should not jitter at low speeds. In the should not jitter at low speeds. In the should have a 100k resistor at output to provide a discharge the for Back EMF. In the should have a voltage feedback from the output terminals. It were so voltage Protection from the battery.				
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