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This is the simulation of Carry Look Ahead adder with the bahavioural one. Here, the operation is similar to that of the carry look ahead adder.
The difference is that we have designed it using partial full adders and look aheads. Here, we have not used any of them. Instead of them, we directly assigned the addition.
In this one, we need to convert each of them into unsigned and then we nned to do the addition which requires some extra circuit. This is not a sythesizable circuit.

Cursor = 50ns
Baseline = 0
Cursor-Baseline = 50ns

Baseline = 0

TimeA = 50ns

	0	10ns	20ns	30ns	40ns
a	256487	11458796	587964	20000	
b	99999	15487	1012563	10000	
cin	0				
cout	0				
sum	356486	11474283	1600528	30001	30000

'd20000
'd10000
0
0
'd30000