Carry_Look_Ahead_delay.Pillalamarri.U56606203

This is the simulation of the Carry Look Ahead Adder with delay. Here, A, B, Cin are 32-bit inputs and Cout_32, S are the 32-bit outputs. G3_32,P3_32 are a bit outputs.
Here, we have created this carry look ahead adder from the 4 bit carry look ahead adder which is built with the Partial Full Adders and look ahead. Here, by using look aheads the circuit generates carry itself.
That is why the adding operation in the circuit is faster than the other adders. As we have inserted delay in the gates, we can observe there is delay in generating of the output. Here, there is a delay of 476ps.