

Carry_Look_Ahead_no_delay .U56606203

This is the simulation of the Carry Look Ahead Adder without delay. Here, A, B, Cin are the 32-bit inputs and Cout_32, S are the 32-bit outputs. G3_32,P3_32 are a bit outputs. Here, we have created this carry look ahead adder from the 4 bit carry look ahead adder which is built with the partial full adders and look ahead. Here, by using look aheads the carry itself. That is why the carry look ahead adders are faster than other adders. Here, as we have not taken any delay, we can not see the delay in the circuit. But in the real world there will be delay. This circuit is synthesizable.

