

```

/* ctrl word
 *
 * 4bit port
 *
 * offset  signal  NOP  ACTIVE  WRITE  READ  BURST  TERM  REFRESH  PRECHARGE  LOAD
 *                                     MODEREG
 *      0   CS      0    0        0      0    0      0      0      0      0
 *      1   WE      1    1        0      1    0      1      0      0      0
 *      2   CAS     1    1        0      0    1      0      1      0      0
 *      3   RAS     1    0        1      1    1      0      0      0      0
 *
 *                                     0xE  0x6  0x8  0xA  0xC  0x2  0x4  0x0
 */
#define CTRL_NOP          0xE
#define CTRL_ACTIVE      0x6
#define CTRL_WRITE       0x8
#define CTRL_READ        0xA
#define CTRL_BURST_TERMINATE 0xC
#define CTRL_REFRESH     0x2
#define CTRL_PRECHARGE   0x4
#define CTRL_LOAD_MODEREG 0x0

#endif

```