# **Display Controller Component**

**REV A** 

Publication Date: 2013/9/19 XMOS © 2013, All Rights Reserved.



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#### 1 Overview

#### IN THIS CHAPTER

- ▶ Features
- Memory requirements
- ▶ Resource requirements
- ▶ Performance

The display controller module is used to drive a single graphics LCD screen up to 800 \* 600 pixels incorporating a managed double buffer.

#### 1.1 Features

- Non-blocking SDRAM management.
- Real time servicing of the LCD.
- ► Touch interactive display
- Image memory manager to simplify handling of images.
- No real time constraints on the application.

### 1.2 Memory requirements

Resource	Usage	
Stack	6198 bytes	
Program	11306 bytes	

# 1.3 Resource requirements

Resource	Usage
Channels	3
Timers	0
Clocks	0
Threads	1



# 1.4 Performance

The achievable effective bandwidth varies according to the avaliable XCore MIPS. The maximum pixel clock supported is 25MHz.



# 2 Hardware Requirements

IN THIS CHAPTER

- ► Recommended Hardware
- ▶ Demonstration Applications

#### 2.1 Recommended Hardware

#### 2.1.1 Slicekit

This module may be evaluated using the Slicekit Modular Development Platform, available from digikey. Required board SKUs are:

- XP-SKC-L2 (Slicekit L2 Core Board)
- ► XA-SK-SCR480 plus XA-SK-XTAG2 (Slicekit XTAG adaptor)

### 2.2 Demonstration Applications

#### 2.2.1 Display Controller Application

- Package: sw\_display\_controller
- ► Application: app\_display\_controller

This combination demo employs the module\_lcd along with the module\_sdram, module\_touch\_controller\_lib, module\_i2c\_master and the module\_display\_controller framebuffer framework component to implement a 480x272 display controller.

Required board SKUs for this demo are:

- ➤ XP-SKC-L2 (Slicekit L2 Core Board) plus XA-SK-XTAG2 (Slicekit XTAG adaptor)
- XA-SK-SDRAM
- ➤ XA-SK-SCR480 (which includes a 480x272 color touch screen)



#### IN THIS CHAPTER

- ► Configuration Defines
- ΔP

- component: sc\_sdram\_burst which handles the SDRAM
- component: sc\_lcd which handles the LCD

The below section details the APIs in the application. For details about the LCD and SDRAM APIs please refer to the respective repositories.

### 3.1 Configuration Defines

The module\_display\_controller can be configured via the header display\_controller\_conf.h. The module requires nothing to be additionally defined however any of the defines can be overridden by adding the header display\_controller\_conf.h to the application project and adding the define that needs overridding. The possible defines are:

#### DISPLAY\_CONTROLLER\_MAX\_IMAGES

This defines the storage space allocated to the display controller for it to store image metadata. When an image is registered with the display controller its dimensions and location in SDRAM address space are stored in a table. The define specifies how many entries are allowed in that table. Note, there is no overflow checking by default.

#### **DISPLAY CONTROLLER VERBOSE**

This define switches on the error checking for memory overflows and causes verbose error warnings to be emitted in the event of an error.

#### 3.2 API

- display\_controller\_client.xc
- ▶ display\_controller\_internal.h
- ▶ display\_controller.xc
- ▶ display\_controller.h
- ▶ transitions.h
- ▶ transitions.xc



The display controller handles the double buffering of the image data to the LCD as a real time service and manages the I/O to the SDRAM as a non-real time service.

The display controller API is as follows: doxygenfunction:: play controller .. doxygenfunction:: display\_controller\_image\_read\_line display\_controller\_image\_read\_line\_p doxygenfunction:: doxvaenfunction:: display controller image write line doxvgenfunction:: display\_controller\_image\_write\_line\_p doxygendisplay\_controller\_image\_read\_partial line function:: doxygenfunction:: display\_controller\_image\_read\_partial\_line\_p doxvaenfunction:: display controller register image doxvgenfunction:: display\_controller\_wait\_until\_idle ... doxygenfuncdisplay\_controller\_wait\_until\_idle\_p tion:: doxygenfunction:: display\_controller\_frame\_buffer\_commit .. doxygenfunction:: play\_controller\_frame\_buffer\_init

The transition API is as follows: .. doxygenfunction:: transition\_wipe .. doxygenfunction:: transition\_slide .. doxygenfunction:: transition\_roll .. doxygenfunction:: transition\_dither .. doxygenfunction:: transition\_alpha\_blend

The transitions use the display controller API.



# 4 Programming Guide

#### IN THIS CHAPTER

- ► Shared Memory Interface
- ▶ Source code structure
- ► Executing The Project
- ► Software Requirements

# 4.1 Shared Memory Interface

The display controller uses a shared memory interface to move the large amount of data around from tile to tile efficiently. This means that the display\_controller, sdram\_server and lcd\_server must be one the same tile.

### 4.2 Source code structure

	Project	File	Description
	module_display_controller	display_controller.h	Header file containing the APIs for the display controller component.
		display_controller.xc	File containing the implementation of the display controller component.
		display_controller_client.xc	File containing the implementation of the display controller client functions.
		display_controller_internal.h	Header file containing the user configurable defines for the display controller component.
		transitions.h	Header file containing the APIs for the display controller transitions.
Figure 1: Project structure		transitions.xc	File containing the implementation of the display controller transitions.



## 4.3 Executing The Project

The module by itself cannot be built or executed separately - it must be linked in to an application. Once the module is linked to the application, the application can be built and tested for driving a LCD screen.

- 1. module\_display\_controller
- 2. module\_lcd
- 3. module\_sdram
- 1. module\_touch\_controller\_lib or module\_touch\_controller\_server
- module\_i2c\_master

should be added to the list of MODULES.

### 4.4 Software Requirements

The module is built on XDE Tool version 12.0 The module can be used in version 12.0 or any higher version of xTIMEcomposer.



# 5 Example Applications

IN THIS CHAPTER

- ▶ app\_display\_controller\_demo
- ► Application Notes

This tutorial describes a demo application that uses the display controller module. §2.1 describes the required hardware setup to run the demos.

### 5.1 app\_display\_controller\_demo

This application demonstrates how the lcd\_module is used to write image data to the LCD screen whilst imposing no real time constraints on the application. The purpose of this demonstration is to show how data is passed to the display\_controller. This application also demonstrates an interactive display using touch\_controller\_lib module.

### 5.2 Application Notes

### 5.2.1 Getting Started

- 1. Plug the XA-SK-LCD Slice Card into the 'TRIANGLE' slot of the Slicekit Core Board
- 2. Plug the XA-SK-SDRAM Slice Card into the 'STAR' slot of the Slicekit Core Board
- 3. Open app\_display\_controller\_demo.xc and build the project.
- 4. run the program ensuring that it is run from the project directory where the tga images are.

The output produced should look like a series of images transitioning on the LCD when the screen is touched.





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