

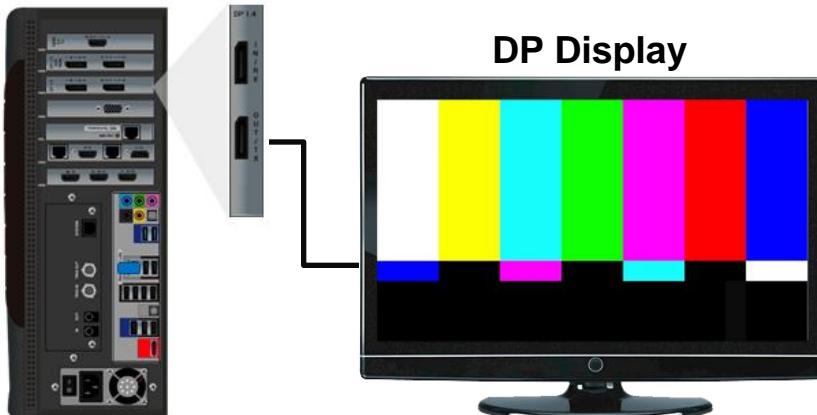
quantum**data**™ 980 Series Test Systems Overview of Applications

DisplayPort 1.4 Video Generator /
Analyzer module

980 DisplayPort 1.4 Video Generator / Analyzer – Video Format Test

- DisplayPort Video Test of an DP monitor, display or sink device.
 - Select between extensive library of VESA and CEA video timing formats.
 - Run video tests with DP 1.4 formats at link rates up to HBR3 (8.1Gb/s) on four (4) lanes.
 - Run video tests with DP 1.2 formats at link rates up to HBR2 (5.4Gb/s) on four (4) lanes.

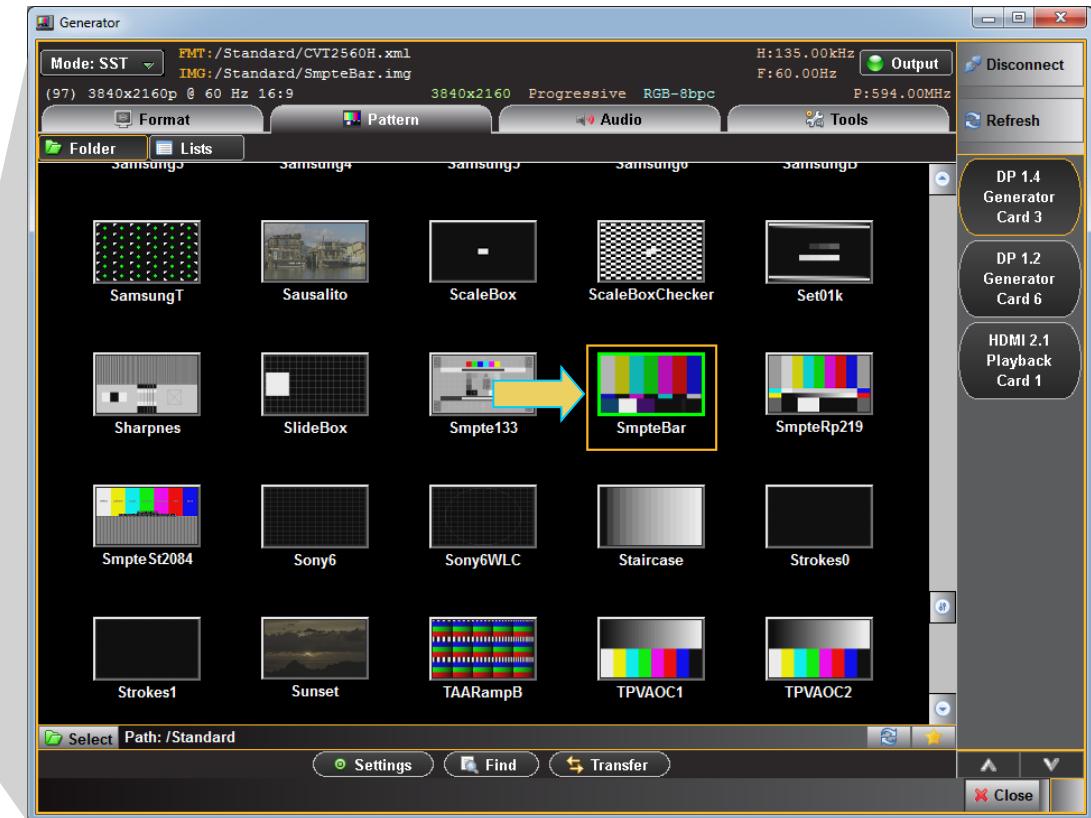
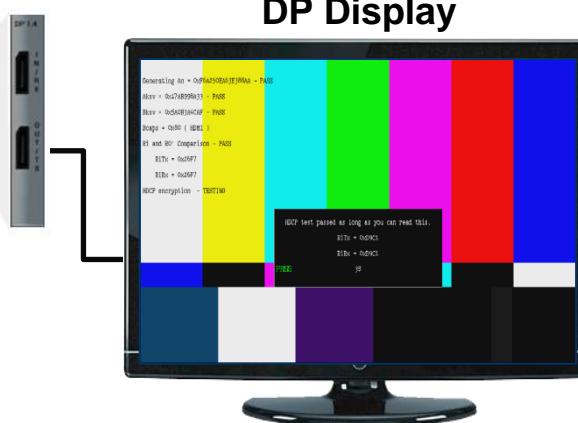
980B w/ DP 1.4
Video Generator



980 DisplayPort 1.4 Video Generator / Analyzer – Video Pattern Testing

- Pattern Testing of a DisplayPort monitor, display or sink device
 - Verify proper rendering on a display.
 - Use extensive test pattern library.

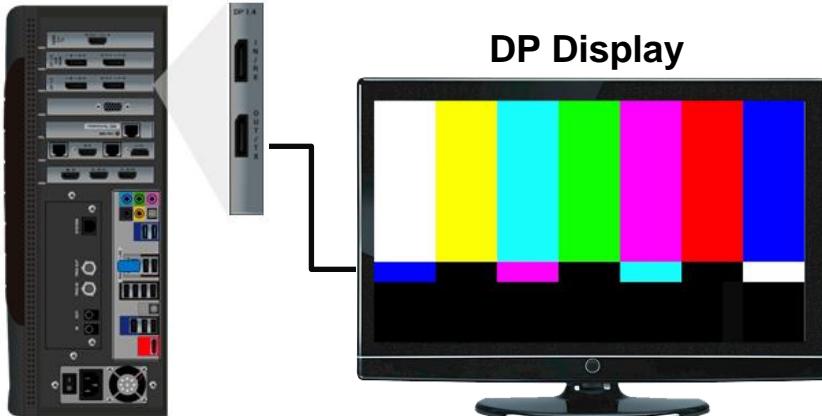
980B w/ DP 1.4
Video Generator



980 DisplayPort 1.4 Video Generator / Analyzer – Audio Test

- DisplayPort Video Test of a DP audio rendering device
- Run LPCM uncompressed formats with programmable audio parameters.

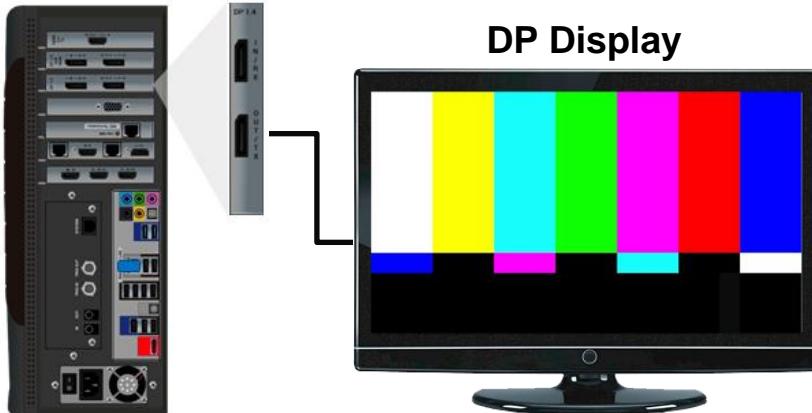
980B w/ DP 1.4
Video Generator



980 DisplayPort 1.4 Video Generator / Analyzer – Link Train Test

- DisplayPort Link Training test of an DP monitor, display or sink device
 - Provides current status of link training on top panel.
 - Control settings on lower panel.

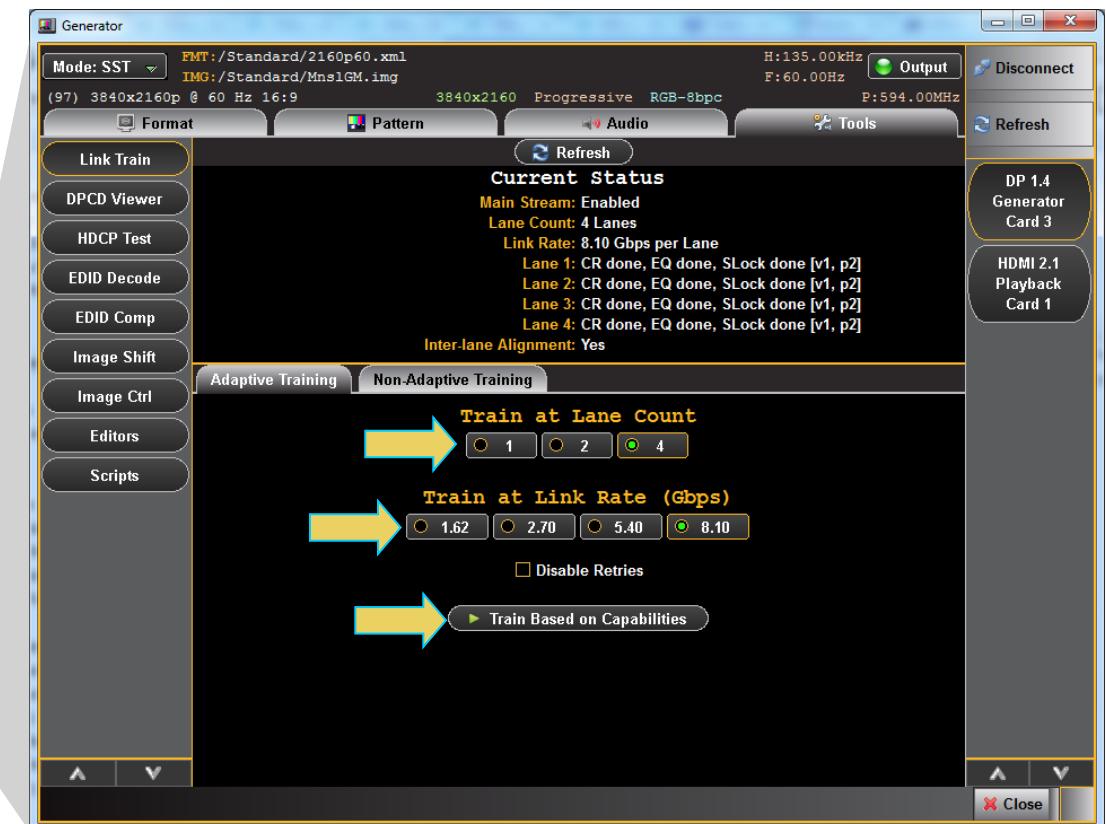
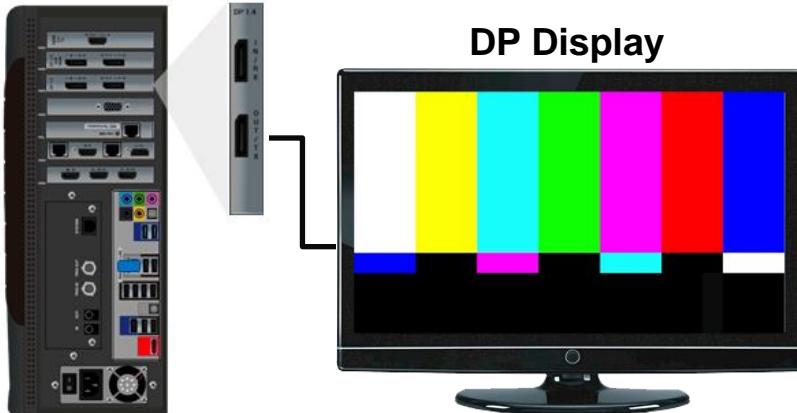
980B w/ DP 1.4
Video Generator



980 DisplayPort 1.4 Video Generator / Analyzer – Link Train Test

- DisplayPort Link Training test of an DP monitor, display or sink device
 - Select Adaptive (automatic) link training.
 - Set limits on source lane count and link rate capabilities (e.g. 4 lanes at 8.1Gbps).

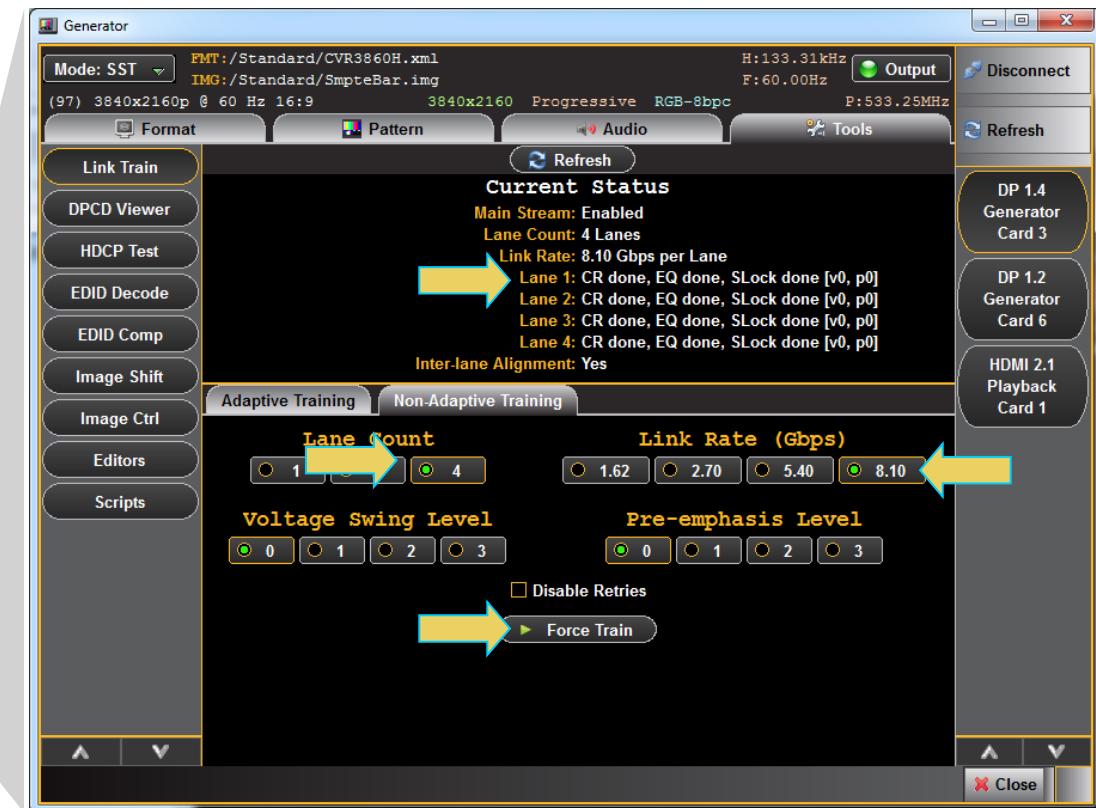
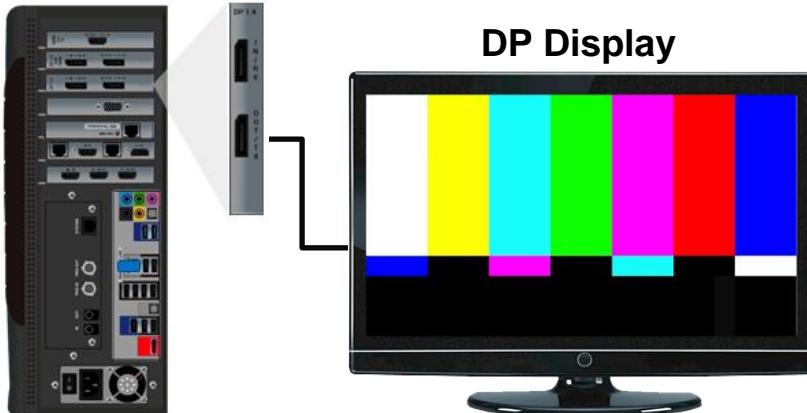
980B w/ DP 1.4
Video Generator



980 DisplayPort 1.4 Video Generator / Analyzer – Link Train Test

- DisplayPort Link Training test of an DP monitor, display or sink device
 - Select Non-Adaptive (user control).
 - Enables precise control over lane count and link rate. Force link training at 8.1Gbps for example.

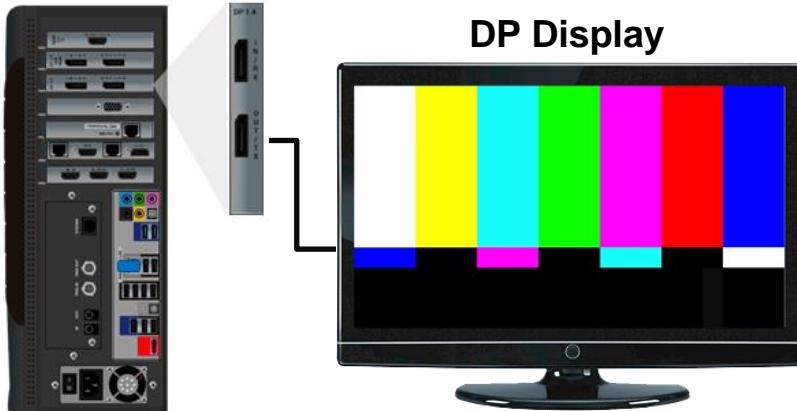
980B w/ DP 1.4
Video Generator



980 DisplayPort 1.4 Video Generator / Analyzer – Link Train Test

- Monitor and view DisplayPort Link Training transactions in real time with Auxiliary Channel Analyzer (ACA).
- View timestamps of each transaction.
- View details of each transaction.
- Disseminate traces to subject matter experts.

980B w/ DP 1.4
Video Generator



[DP_LT_4K_4L_81LR_VS1_PE1] Events: 56 (112)			
7	DPLT	DP-T30	> R:202 LANE0_1_STATUS: L=3
8	DPLT	DP-T30	< ACK 02 00 80
9	DNAT	DP-T30	> R:0 DCD_C_REV L=12
10	DNAT	DP-T30	< ACK 14 1E C4 81 01 00 01 80...
11	DNAT	DP-T30	> W:10A eDP_CONFIGURATION_SET L=1 00
12	DNAT	DP-T30	< ACK 00
13	DNAT	DP-T30	> R:201 DEVICE_SERVICE_IRQ_VECTOR L=1
14	DNAT	DP-T30	< ACK 00
15	DNAT	DP-T30	> R:E TRAINING_AUX_RD_INTERVAL L=1
16	DNAT	DP-T30	< ACK 01
17	DPLT	DP-T30	> W:100 LINK_BW_SET L=1 1E
18	DPLT	DP-T30	< ACK
19	DPLT	DP-T30	> W:101 LANE_COUNT_SET L=1 84
20	DPLT	DP-T30	< ACK
21	DPLT	DP-T30	> W:107 DOWNSPREAD_CTRL L=1 00
22	DPLT	DP-T30	< ACK
23	DNAT	DP-T30	> R:E TRAINING_AUX_RD_INTERVAL L=1
24	DNAT	DP-T30	< ACK 01
25	DPLT	DP-T30	> W:102 TRAINING_PATTERN_SET: L=1 21
26	DPLT	DP-T30	< ACK
27	DPLT	DP-T30	> W:103 TRAINING_LANE0_SET L=4 09 09 09 09
28	DPLT	DP-T30	< ACK
29	DPLT	DP-T30	> R:202 LANE0_1_STATUS: L=2
30	DPLT	DP-T30	< ACK 11 11
31	DPLT	DP-T30	> W:102 TRAINING_PATTERN_SET: L=1 07
32	DPLT	DP-T30	< ACK
33	DPLT	DP-T30	> W:103 TRAINING_LANE0_SET L=4 09 09 09 09
34	DPLT	DP-T30	< ACK
35	DPLT	DP-T30	> R:202 LANE0_1_STATUS: L=3
36	DPLT	DP-T30	< ACK 77 77 81
37	DPLT	DP-T30	> R:204 LANE_ALIGN_STATUS_UPDATED L=2
38	DPLT	DP-T30	< ACK 01 00
39	DPLT	DP-T30	> W:102 TRAINING_PATTERN_SET: L=1 00
40	DPLT	DP-T30	< ACK
41	DPLT	DP-T30	> R:202 LANE0_1_STATUS: L=3
42	DPLT	DP-T30	< ACK 77 77 01
43	DPLT	DP-T30	> R:100 LINK_BW_SET L=8
44	DPLT	DP-T30	< ACK 1E 84 00 09 09 09 09 09

Start Time: +00:58:37.062259
Type: Native
Direction: Reply
Command: ACK

Reply to Read Request.

00202: LANE0_1_STATUS:

Bit	Name	Value	Description
0	LANE0_CR_DONE	Y(1)	
1	LANE0_CHANNEL_EQ_DONE	Y(1)	
2	LANE0_SYMBOL_LOCKED	Y(1)	
3		0	Reserved
4	LANE1_CR_DONE	Y(1)	
5	LANE1_CHANNEL_EQ_DONE	Y(1)	
6	LANE1_SYMBOL_LOCKED	Y(1)	
7		0	Reserved

00203: LANE2_3_STATUS

Bit	Name	Value	Description
0	LANE2_CR_DONE	Y(1)	
1	LANE2_CHANNEL_EQ_DONE	Y(1)	
2	LANE2_SYMBOL_LOCKED	Y(1)	
3		0	Reserved
4	LANE3_CR_DONE	Y(1)	
5	LANE3_CHANNEL_EQ_DONE	Y(1)	
6	LANE3_SYMBOL_LOCKED	Y(1)	
7		0	Reserved

00204: LANE_ALIGN_STATUS_UPDATED

Bit	Name	Value	Description
0	INTERLANE_ALIGN_DONE	Y(1)	
1	POST_LT_ADJ_REQ_IN_PROGRESS	N(0)	
2		0	Reserved
3		0	Reserved
4		0	Reserved
5		0	Reserved
6	DOWNSPREAD_PORT_STATUS_CHANGED	N(0)	
7	LINK_STATUS_UPDATED	Y(1)	

Raw Data:

```
[0000][00 77 77 81 -- -- -- --] [.ww. ]
```

36: < ACK 77 77 81

Option

Data

Filter

Find

Clear

Open

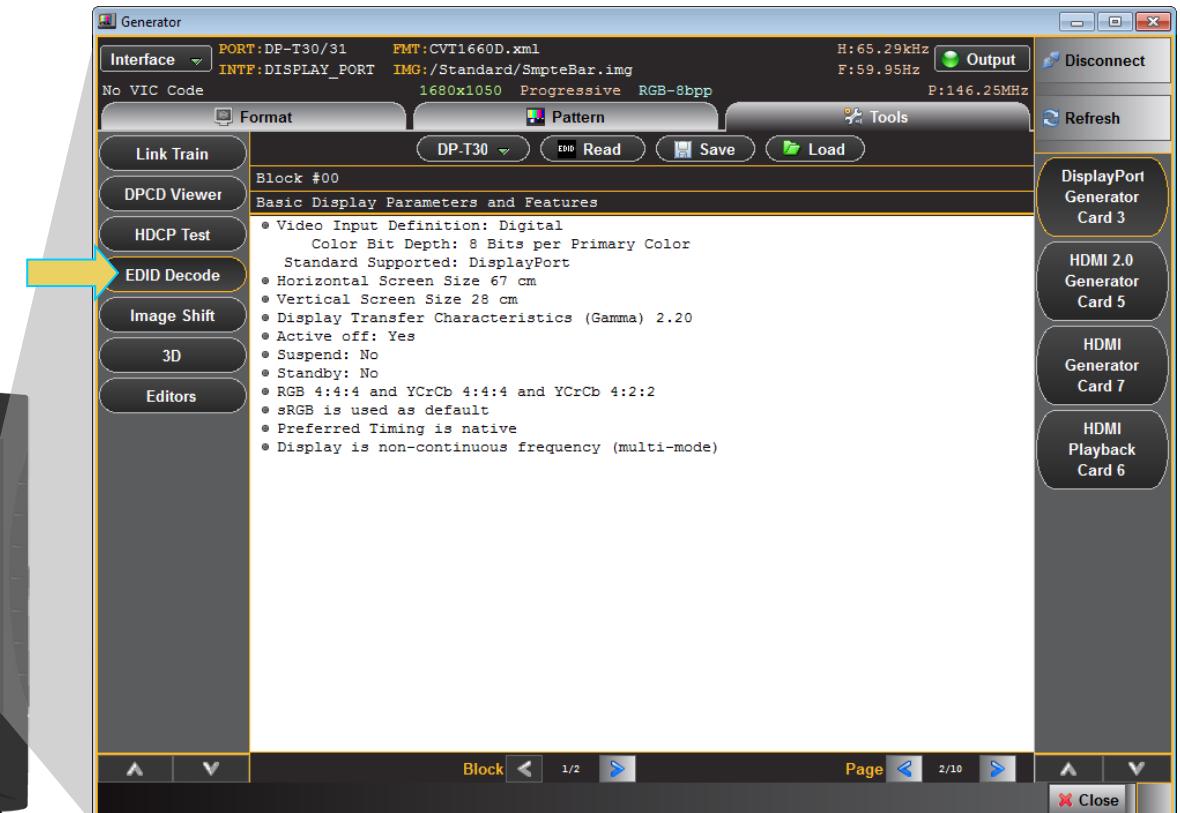
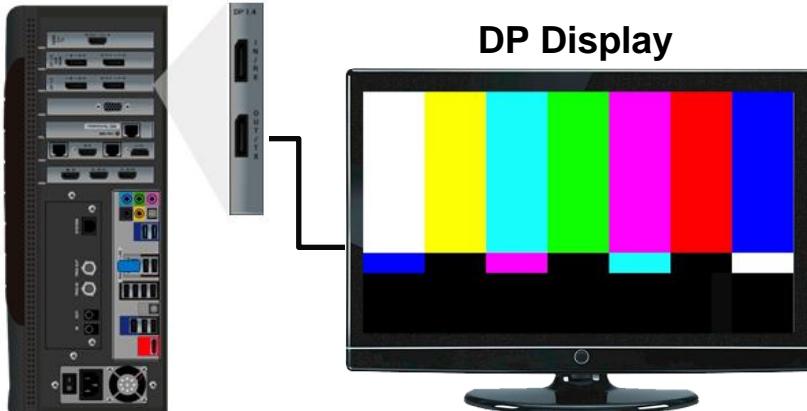
Export

Hide

980 DisplayPort 1.4 Video Generator / Analyzer – EDID Test

- DisplayPort EDID verification test of a DP monitor, display or sink device
 - View the entire EDID in human readable text.
 - Check for checksum and header errors.

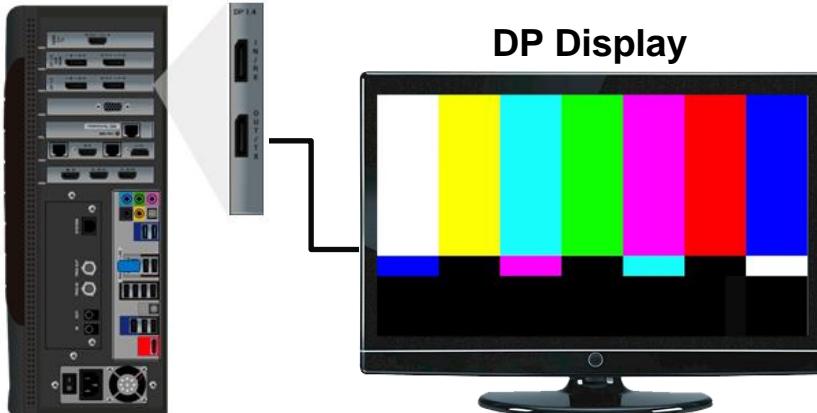
980B w/ DP 1.4
Video Generator



980 DisplayPort 1.4 Video Generator / Analyzer – DPCD Test

- DisplayPort DPCD verification test of an DP monitor, display or sink device
 - View display's DPCD registers in human readable text.
 - Example shows Receiver Capabilities indicating 8.1Gbps link rate support.

980B w/ DP 1.4
Video Generator

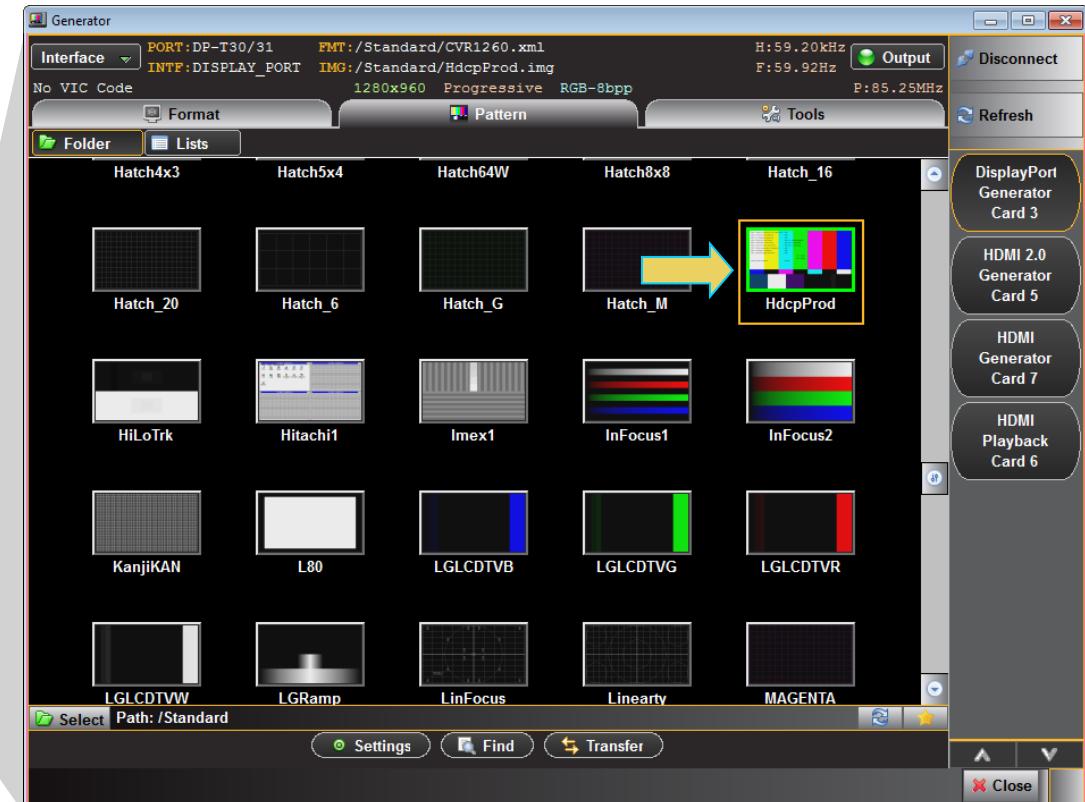
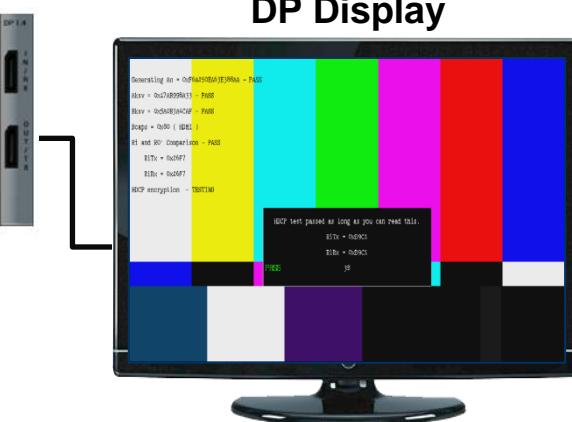


This screenshot shows the DPCD Viewer tab of the 980 software, focusing on the 00100: LINK_BW_SET register. The register is set to 1Eh, which corresponds to 8.1 Gbps per lane. The software provides a detailed view of each bit's value and description. Other registers shown include 00101: LANE_COUNT_SET (4 lanes), 00102: TRAINING_PATTERN_SET, and 00103: TRAINING_LANE0_SET. The right side of the interface shows various test modes and card configurations.

980 DisplayPort 1.4 Video Generator / Analyzer – HDCP Test

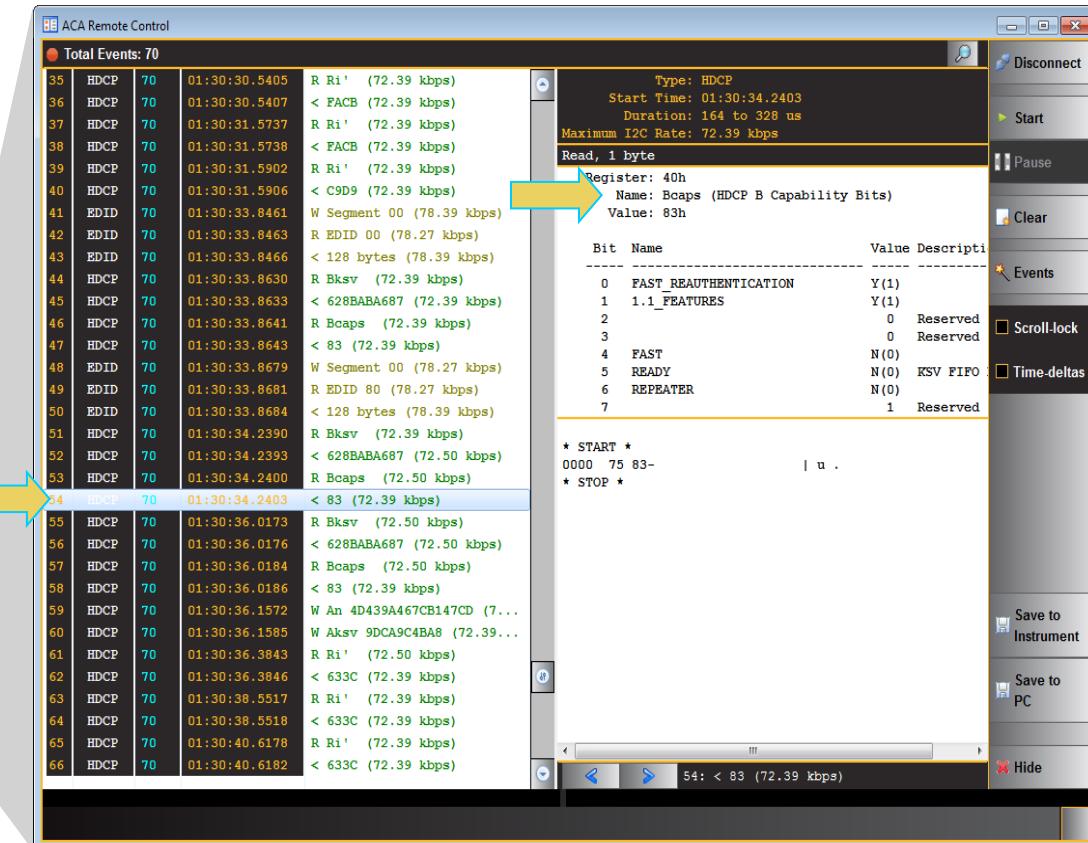
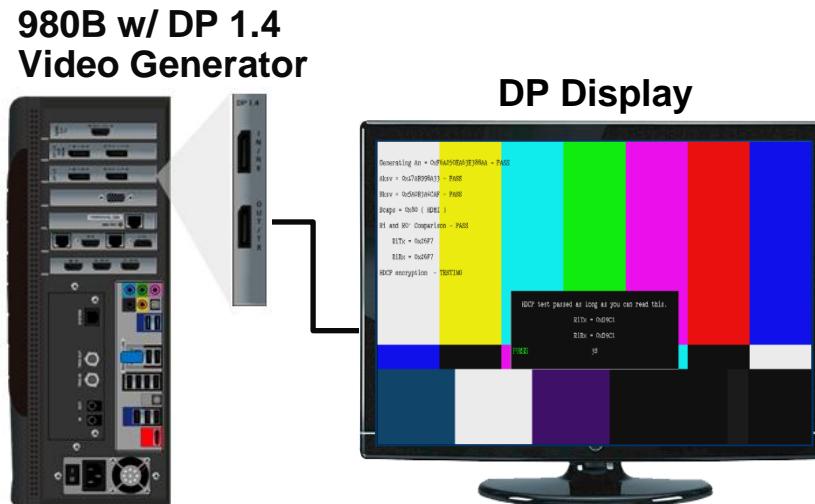
- HDCP verification test of a DisplayPort monitor, display or sink device
 - Verify HDCP authentication.
 - View results on connected display.

980B w/ DP 1.4
Video Generator



980 DisplayPort 1.4 Video Generator / Analyzer – HDCP Test

- HDCP verification with a DisplayPort monitor, display or sink device
 - Verify HDCP versions 1.3 and 2.2 authentication transactions on Auxiliary Channel Analyzer (ACA) utility.
 - Identify cause of HDCP interoperability problems.
 - Save results and disseminate to colleagues at other locations.



980 DisplayPort 1.4 Generator / Analyzer – HDCP 2.2 Source Compliance

- Run DP-HDCP 2.2 Compliance tests on next-gen DP sources
- Pre-test or self-test your DisplayPort HDCP 2.2 source device to reduce costs and meet product time to market requirements.
- View details of test results and identify root cause of HDCP failure.
- Link to aux channel traces to view failures from test results screen.
- Disseminate results to subject matter experts.

980B w/ DP 1.4 Video Generator / Analyzer



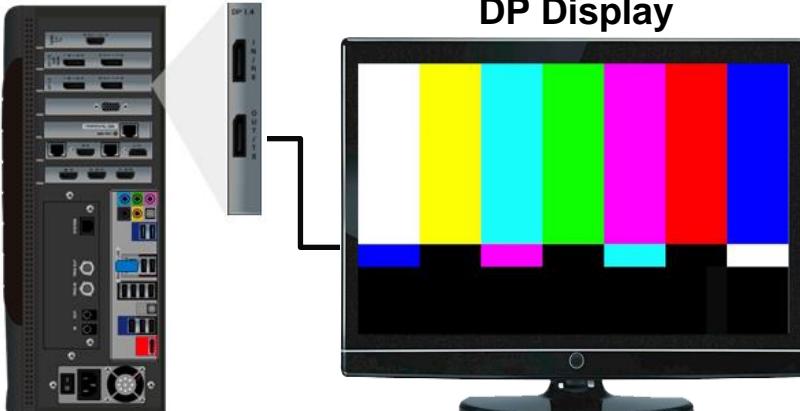
The screenshot displays two windows of the DP HDCP 2.2 TX CT 1.0 software. The top window shows a list of test cases under the 'Test Selection' tab, with several items checked (e.g., 1A-01 through 1A-11). The bottom window is the 'Compliance Test Results Viewer' showing detailed results for a specific test run. The results table includes columns for Test Name, Details, and Status (Pass or Incomplete). A yellow arrow points from the test selection window to the results viewer, indicating the flow from configuration to execution and analysis.

Test Name	Details	Status
1A-01: Regular Procedure: With previously connected Receiver (With stored Km)	TX AUTH:MSG:HDP_DIS ts:7561890365.44 us TX UNAUTH :ENTER	Pass
1A-02: Regular Procedure: With newly connected Receiver (Without stored Km)	TX UNAUTH:MSG RD:HDP_DIS ts:0.00 us TX UNAUTH:MSG RD:INVALID_VER ts:32966939792173888.00 us RX UNAUTH :ENTER Repn: DevCnt:0 Dep:0 RX UNAUTH:NO VIDEO Present	Pass
1A-03: Regular Procedure: Receiver disconnect after AKE Init		Incomplete
1A-04: Regular Procedure: Verify the Source DUT restarts auth after the exchange of Km.	TX AUTH:MSG:HDP_DIS ts:7565891379.20 us TX UNAUTH:MSG RD:HDP_DIS ts:7565891379.20 us RX UNAUTH:RCVD:AKE_Init ts:7565891317.76 us RX UNAUTH:**Test Cond.** hpd	Pass
1A-05: Regular Procedure: Verify the Source DUT restarts auth after locality check is initiated	TX UNAUTH:MSG RD:VALID_VER ts:7563890841.60 us TX UNAUTH:MSG RD:HDP_DIS ts:7565890877.44 us TX UNAUTH:AKE_INIT ts:7565891379.20 us TX UNAUTH:MSG RD:AKE_Init ts:7565891379.20 us RX UNAUTH:RCVD:AKE_Init ts:7565891317.76 us RX UNAUTH:**Test Cond.** hpd	Pass
1A-06: Regular Procedure: Verify the Source DUT restarts auth after the exchange of Ks.	TX UNAUTH:MSG RD:VALID_VER ts:7563890841.60 us TX UNAUTH:MSG RD:HDP_DIS ts:7565890877.44 us TX UNAUTH:AKE_INIT ts:7565891379.20 us TX UNAUTH:MSG RD:AKE_Init ts:7565891379.20 us RX UNAUTH:RCVD:AKE_Init ts:7565891317.76 us RX UNAUTH:**Test Cond.** hpd	Pass
1A-07: Regular Procedure: Verify the Source DUT restarts auth after the exchange of Ks.	TX UNAUTH:MSG RD:VALID_VER ts:7563890841.60 us TX UNAUTH:MSG RD:HDP_DIS ts:7565890877.44 us TX UNAUTH:AKE_INIT ts:7565891379.20 us TX UNAUTH:MSG RD:AKE_Init ts:7565891379.20 us RX UNAUTH:RCVD:AKE_Init ts:7565891317.76 us RX UNAUTH:**Test Cond.** hpd	Pass
1A-08: Irregular Procedure: Verify the Source DUT considers it a Receiver certificate fails.	TX UNAUTH:MSG RD:VALID_VER ts:7563890841.60 us TX UNAUTH:MSG RD:HDP_DIS ts:7565890877.44 us TX UNAUTH:AKE_INIT ts:7565891379.20 us TX UNAUTH:MSG RD:AKE_Init ts:7565891379.20 us RX UNAUTH:RCVD:AKE_Init ts:7565891317.76 us RX UNAUTH:**Test Cond.** ake_init	Pass
1A-09: Irregular Procedure: Verify the Source DUT considers it a revocation list.	TX UNAUTH:MSG RD:VALID_VER ts:7563890841.60 us TX UNAUTH:MSG RD:HDP_DIS ts:7565890877.44 us TX UNAUTH:AKE_INIT ts:7565891379.20 us TX UNAUTH:MSG RD:AKE_Init ts:7565891379.20 us RX UNAUTH:RCVD:AKE_Init ts:7565891317.76 us RX UNAUTH:**Test Cond.** ake_init	Pass
1A-10: Irregular Procedure: Verify the Source DUT considers it a value for H' that does not match H.	TX UNAUTH:MSG RD:VALID_VER ts:7563890841.60 us TX UNAUTH:MSG RD:HDP_DIS ts:7565890877.44 us TX UNAUTH:AKE_INIT ts:7565891379.20 us TX UNAUTH:MSG RD:AKE_Init ts:7565891379.20 us RX UNAUTH:RCVD:AKE_Init ts:7565891317.76 us RX UNAUTH:**Test Cond.** ake_init	Pass
1A-11: Irregular Procedure: Verify the Source DUT considers it a revocation list.	TX UNAUTH:MSG RD:VALID_VER ts:7563890841.60 us TX UNAUTH:MSG RD:HDP_DIS ts:7565890877.44 us TX UNAUTH:AKE_INIT ts:7565891379.20 us TX UNAUTH:MSG RD:AKE_Init ts:7565891379.20 us RX UNAUTH:RCVD:AKE_Init ts:7565891317.76 us RX UNAUTH:**Test Cond.** ake_init	Pass
1A-12: Irregular Procedure: Verify the Source DUT considers it a locality failure.	TX UNAUTH:MSG RD:VALID_VER ts:7563890841.60 us TX UNAUTH:MSG RD:HDP_DIS ts:7565890877.44 us TX UNAUTH:AKE_INIT ts:7565891379.20 us TX UNAUTH:MSG RD:AKE_Init ts:7565891379.20 us RX UNAUTH:RCVD:AKE_Init ts:7565891317.76 us RX UNAUTH:**Test Cond.** locality	Pass
1A-13: Regular Procedure - Encryption Disable Bootstrapping		Incomplete

980 DisplayPort 1.4 Generator / Analyzer – HDCP 2.2 Sink Compliance

- Run DisplayPort HDCP 2.2 Compliance tests on next gen displays
- Pre-test or self-test your DP-HDCP 2.2 sink device to reduce costs and meet product time to market requirements.
- View details of test results and identify root cause of HDCP failure.
- Link to aux channel traces to view failures from test results screen.
- Disseminate results to subject matter experts.

980B w/ DP 1.4
Video Generator



DP HDCP 2.2 Receiver CT 1.0

CDF Entry Test Selection Test Options / Preview

Open Save Select All Tests Deselect All Tests

Upstream with Transmitter

Select All on Page Clear All on Page

2C-01: Regular Procedure - With transmitter
Verify the Receiver DUT works with an attached source under nominal circumstances.

2C-02: Irregular Procedure - New Authentication after AKE_Init
Verify the Receiver DUT restarts authentication when a new AKE_Init and r_tx is transmitted right after the transmission.

2C-03: Irregular Procedure - New Authentication during Locality Check
Verify the Receiver DUT restarts authentication when a new AKE_Init and r_tx is transmitted right after the transmission.

2C-04: Irregular Procedure - New Authentication after SKE Send Eks
Verify the Receiver DUT restarts authentication when a new SKE Send Eks and r_tx is transmitted right after the transmission.

2C-05: Irregular Procedure - New Authentication during Link Sync
Verify the Receiver DUT restarts authentication when a new AKE_Init and r_tx is transmitted right after the transmission.

2C-06: Regular Procedure - With transmitter
Verify the Receiver DUT works with an attached source under nominal circumstances.

DP HDCP 2.2 Receiver (1.0) Compliance Test Results

Test Name / Details	Status	Status
2C-01: Regular Procedure - With transmitter	Pass	Pass
2C-02: Irregular Procedure - New Authentication after AKE_Init	Pass	Pass
2C-03: Irregular Procedure - New Authentication during Locality Check	Pass	Pass
2C-04: Irregular Procedure - New Authentication after SKE Send Eks	Pass	Pass
2C-05: Irregular Procedure - New Authentication during Link Sync	Pass	Pass
2C-06: Regular Procedure - With transmitter	Pass	Pass

Results Name: Acme_XYZ_HDCP_22_DP_Sink1
Date Tested: January 24, 2017 11:51 AM
Overall Status: CTS 1.0 - Incomplete

Manufacturer: Acme
Model Name: XYZ
Port Tested: 1

Test Results

Iter 01:

- TX:HD_D:ENTER
- TX:AKE:**Test Cond.** ake_init
- TX:UNAUTH:ENTER
- RX:MSG RD:ENC_DIS ts:92078933053.44 us
- Encryption Disabled
- TX:UNAUTH:AKE_INIT ts:92080432230.40 us
- TX:UNAUTH:MSG RD:AKE_Init ts:92080432230.40 us
- RX:AUTH:MSR:RCVD:AKE_Init ts:92080432179.20 us
- HDCP2RX:UNAUTH:Repno:DevCntr:31 Dep:4
- MSG:RCVD:AKE_Init ts:92080432179.20 us
- RX:MSG:WROTE TO DPCD:AKE_Send_Cert:534 ts:92080432414.72 us
- TX:UNAUTH:MSG:RCVD:AKE_Send_Cert ts:92080491888.64 us
- TX:UNAUTH:Rrx ff,ca,f8,b,25,72,82,f2
- TX:UNAUTH:RrxCaps 2 0 2
- RX:AKE:MSG SND:AKE_Send_Cert ts:92080505088.00 us
- MSG:RCVD:AKE_Stored_km ts:92080506019.84 us
- TX:AKE:Snd_Stored_RM ts:92080506081.28 us
- RX:MSG:WROTE TO DPCD:AKE_Send_H_prime:33 ts:92080506091.52 us
- TX:AKE:MSG:AKE_Stored_km ts:92080506081.28 us
- RX:AKE:MSG:RCVD:AKE_Send_H_prime ts:92080510832.64 us
- TX:LC:Snd:LC_Init ts:92080511948.80 us
- TX:LC:MSG:LC_Init ts:92080511948.80 us
- MSG:RCVD:LC_Init ts:92080511887.36 us
- RX:MSG:WROTE TO DPCD:LC_Send_L_prime:33 ts:92080512256.00 us
- RX:SKE:MSG SND:AKE_Send_H_prime ts:92080511887.36 us

2C-01: Regular Procedure - With transmitter

Instrument: L980B [10.30.196.145]

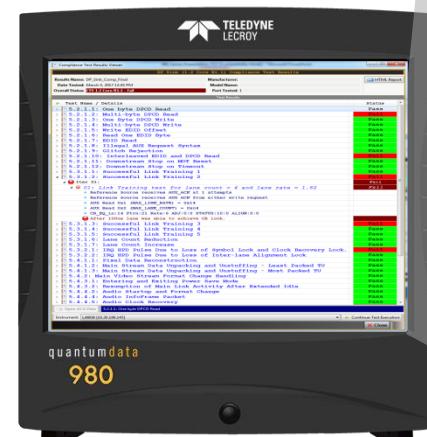
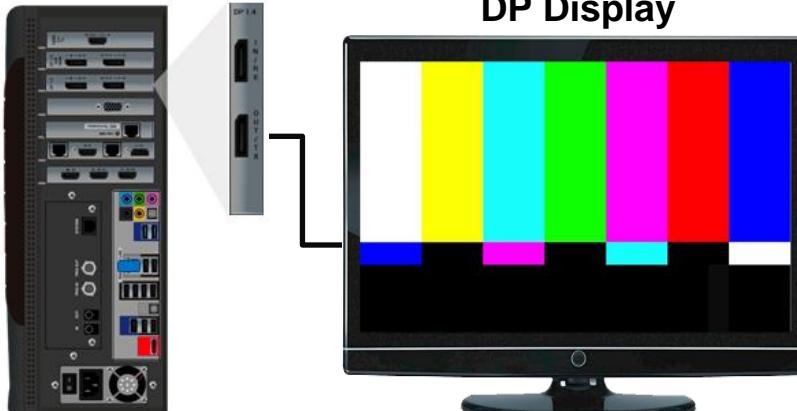
Continue Test Execution

X Close

980 DisplayPort 1.4 Generator / Analyzer – Link Layer 1.2 Sink Compliance

- Run DP 1.2 Link Layer Compliance tests on next gen sinks (displays)
- Pre-test or self-test your DP 2.2 sink device to reduce costs and meet product time to market requirements.
- View details of test results and identify root cause of link layer failure.
- Link to aux channel traces to view failures from test results screen.
- Disseminate results to subject matter experts.

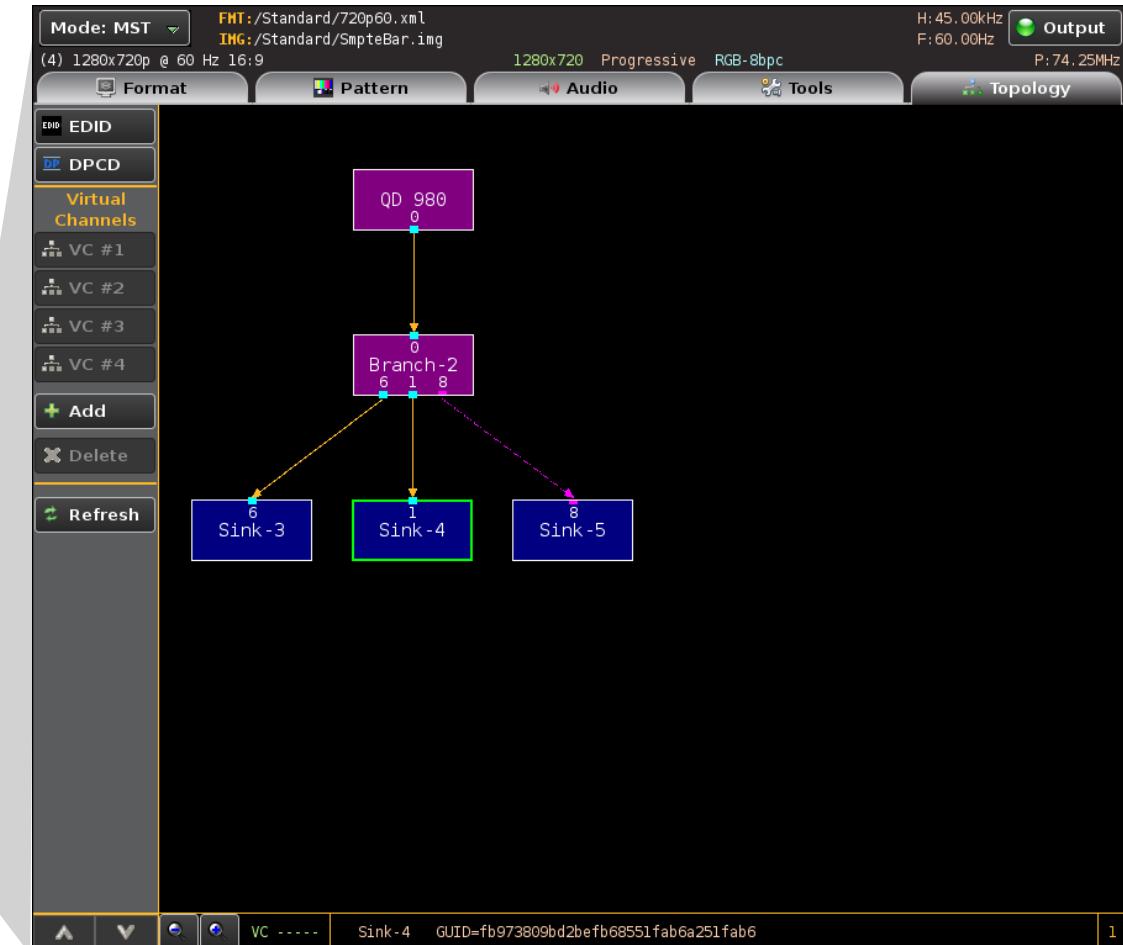
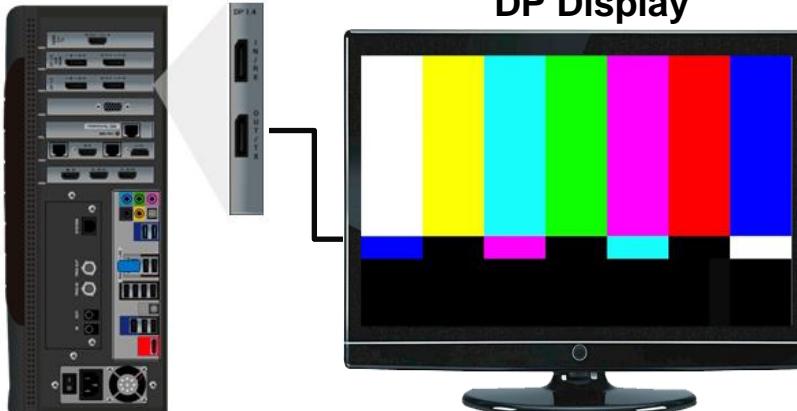
980B w/ DP 1.4 Video Generator



980 DisplayPort 1.4 Video Generator / Analyzer – Multi-Stream Transport

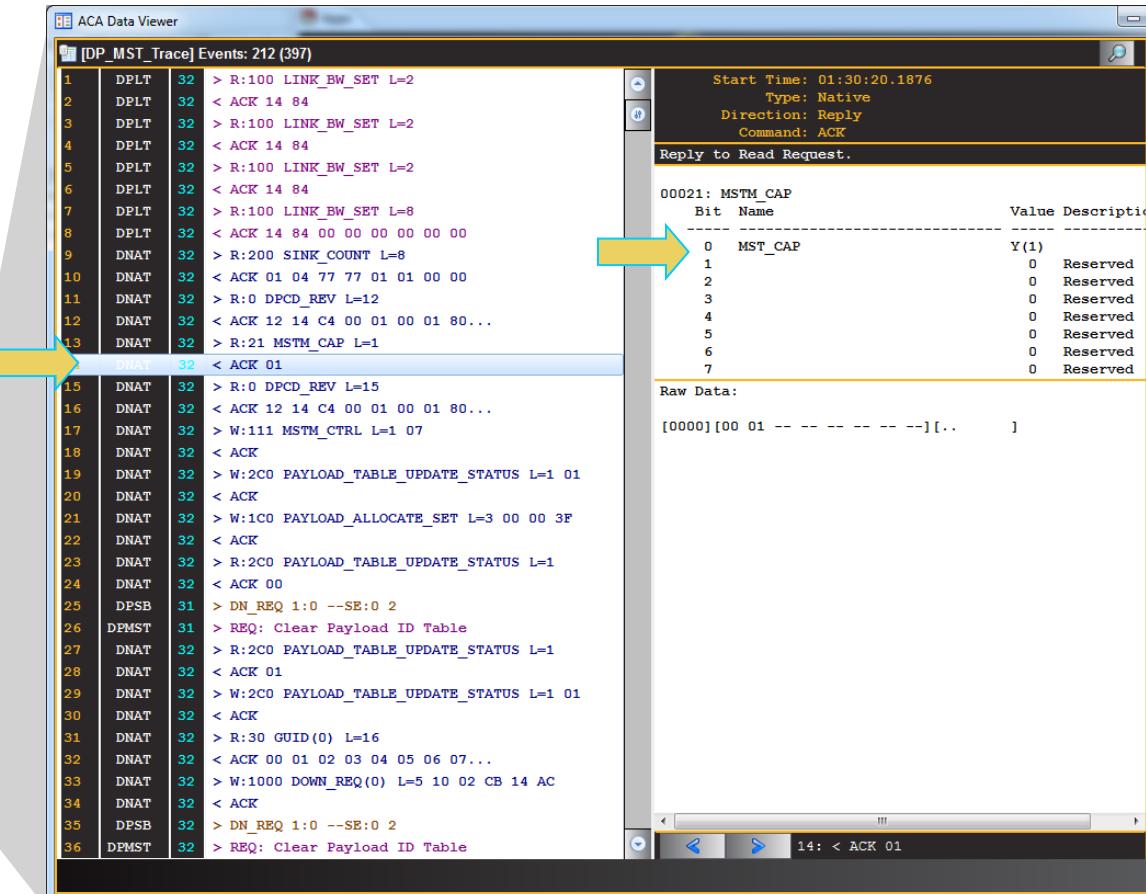
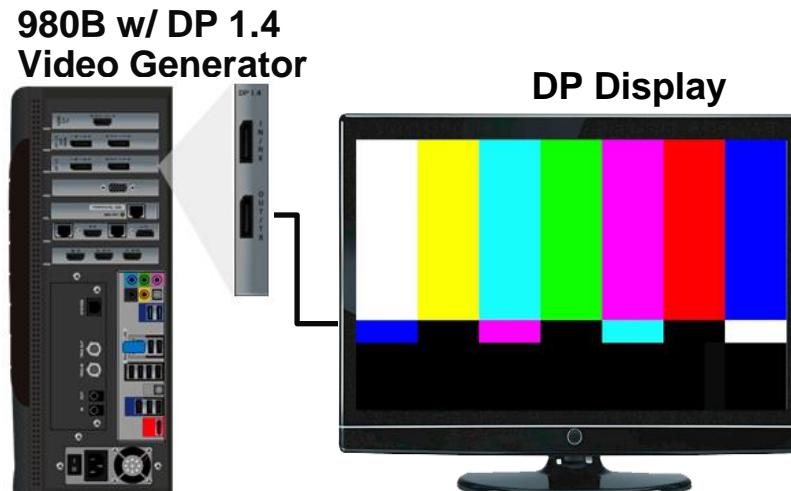
- Emulate a DisplayPort Multi-Stream Transport (MST) source to test an MST display or branch device.
- Configure up to 3 downstream MST nodes.

980B w/ DP 1.4
Video Generator



980 DisplayPort 1.4 Video Generator / Analyzer – Multi-Stream Transport

- Emulate a DisplayPort Multi-Stream Transport (MST) source to test an MST display or branch device.
 - Use Auxiliary Channel Analyzer (ACA) to verify MST negotiation with an MST-capable sink.
 - Save results and disseminate to colleagues at other locations.



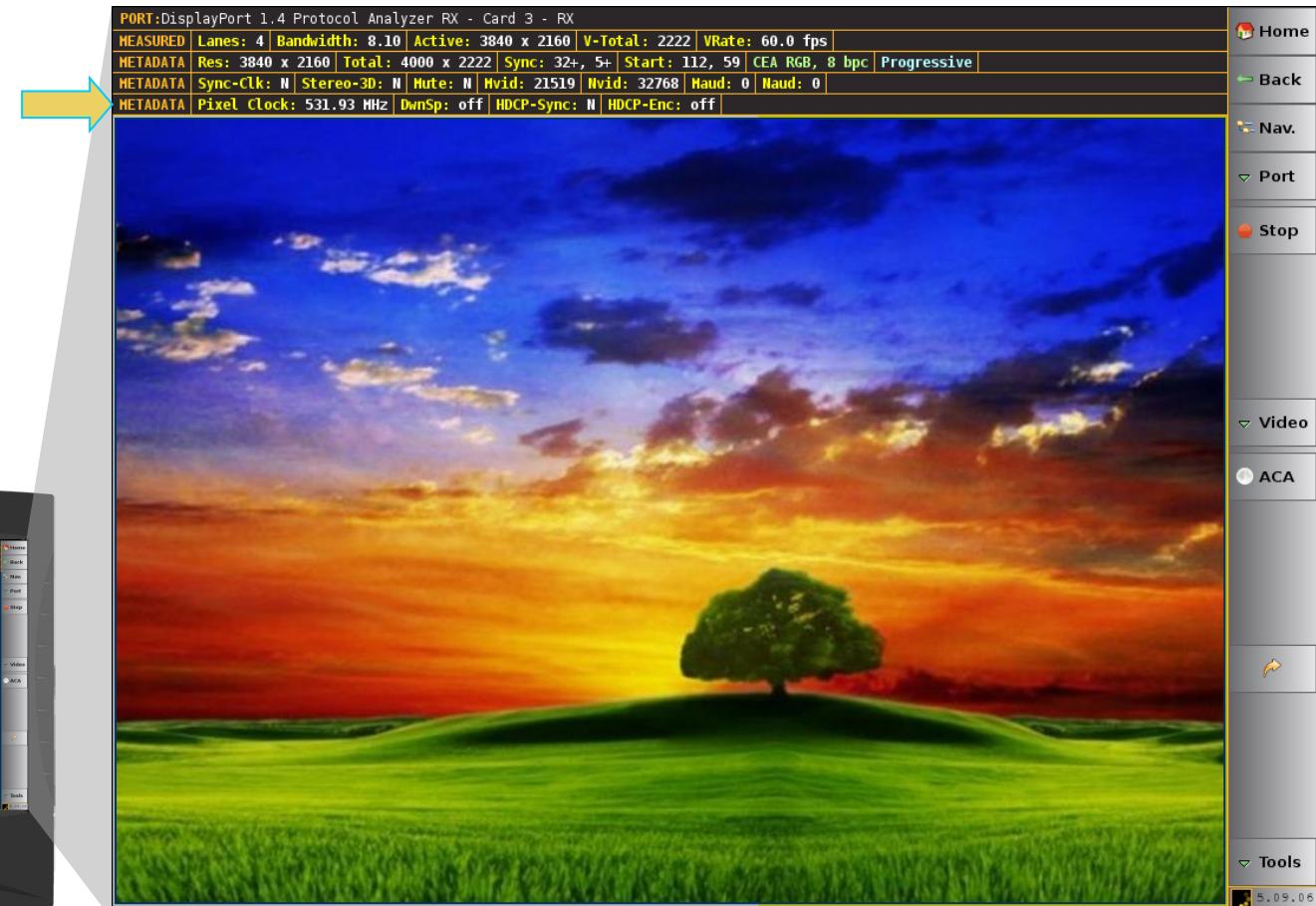
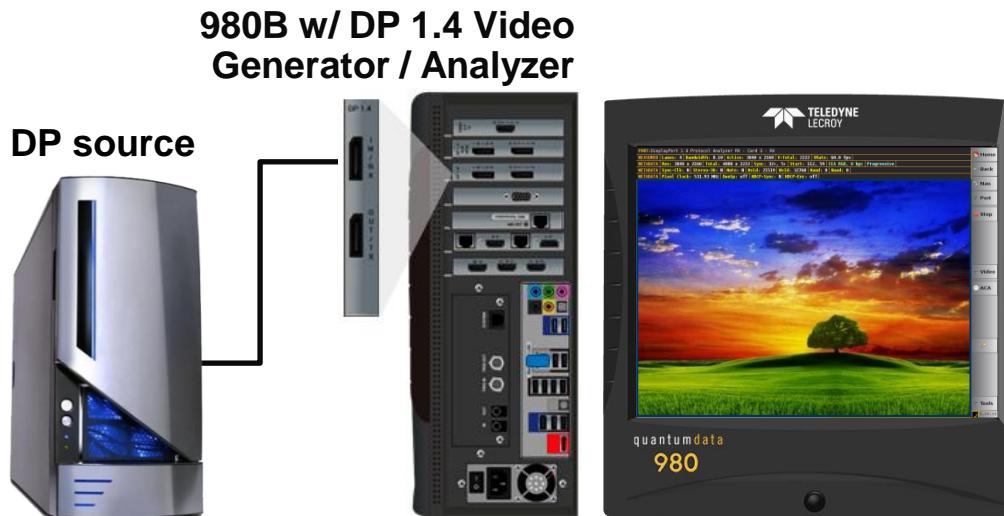
980 DisplayPort 1.4 Video Generator / Analyzer – Sink Emulation

- View summary of incoming video from a DisplayPort 1.4 source running at 8.1Gbps link rate.
 - Emulate any EDID to test source response.
 - Emulate DPCD and edit DPCD registers.
 - Emulate Multi-Stream Transport (MST) branch or display device.



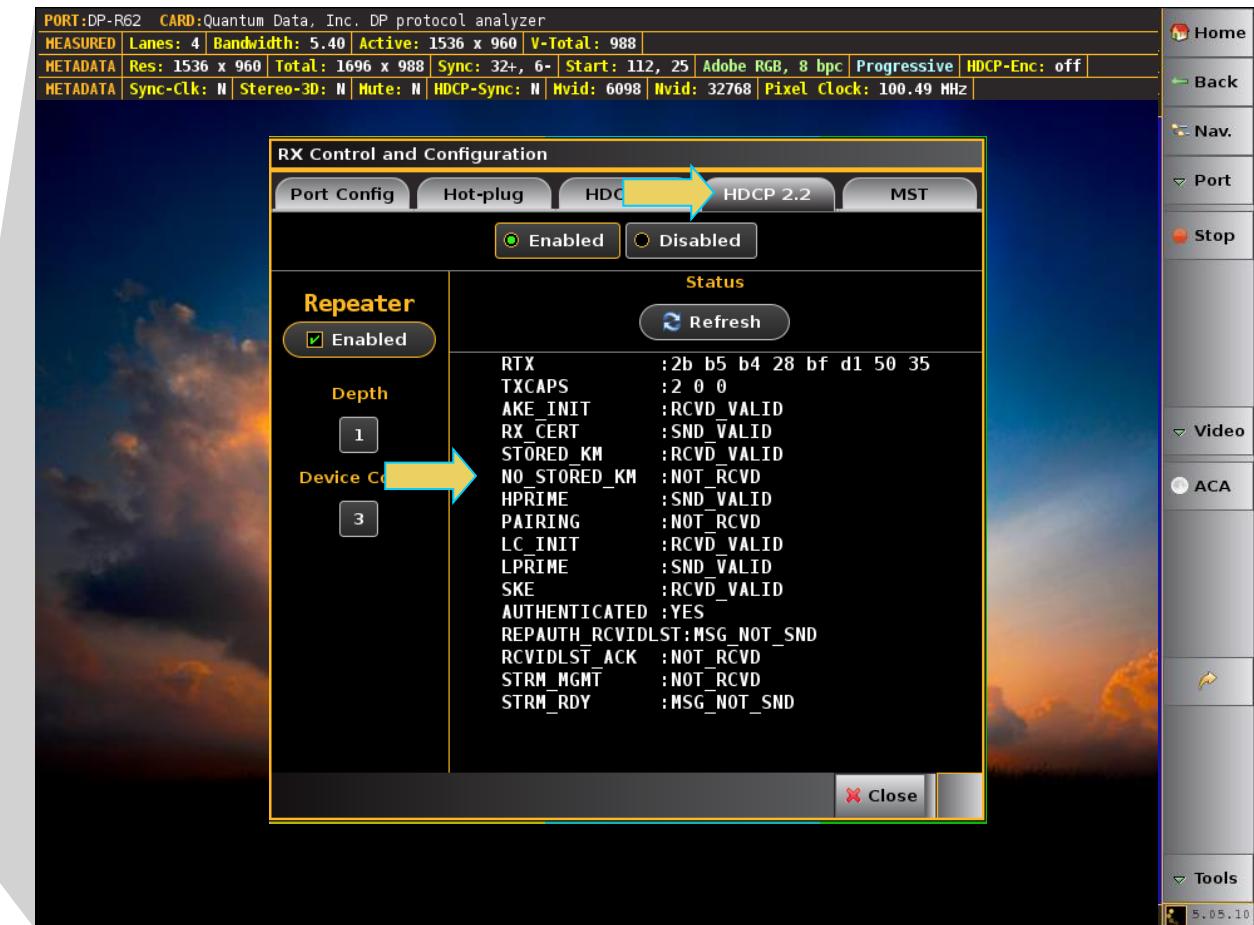
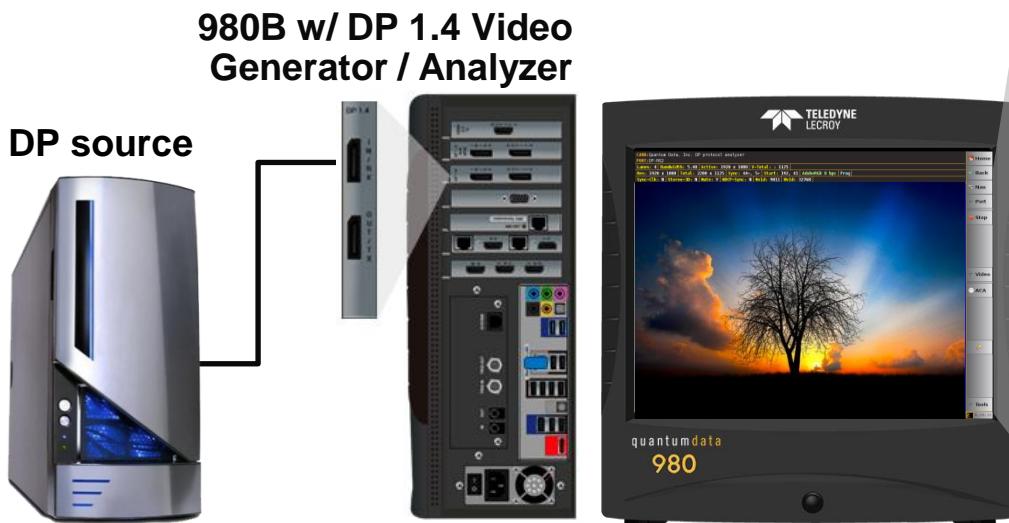
980 DisplayPort 1.4 Video Generator / Analyzer – Source Real Time View

- View summary of incoming video from a DisplayPort 1.4 source running at 8.1Gbps link rate.
- View video image and video, audio and metadata on upper status bar.
- View content of HDCP encrypted video.



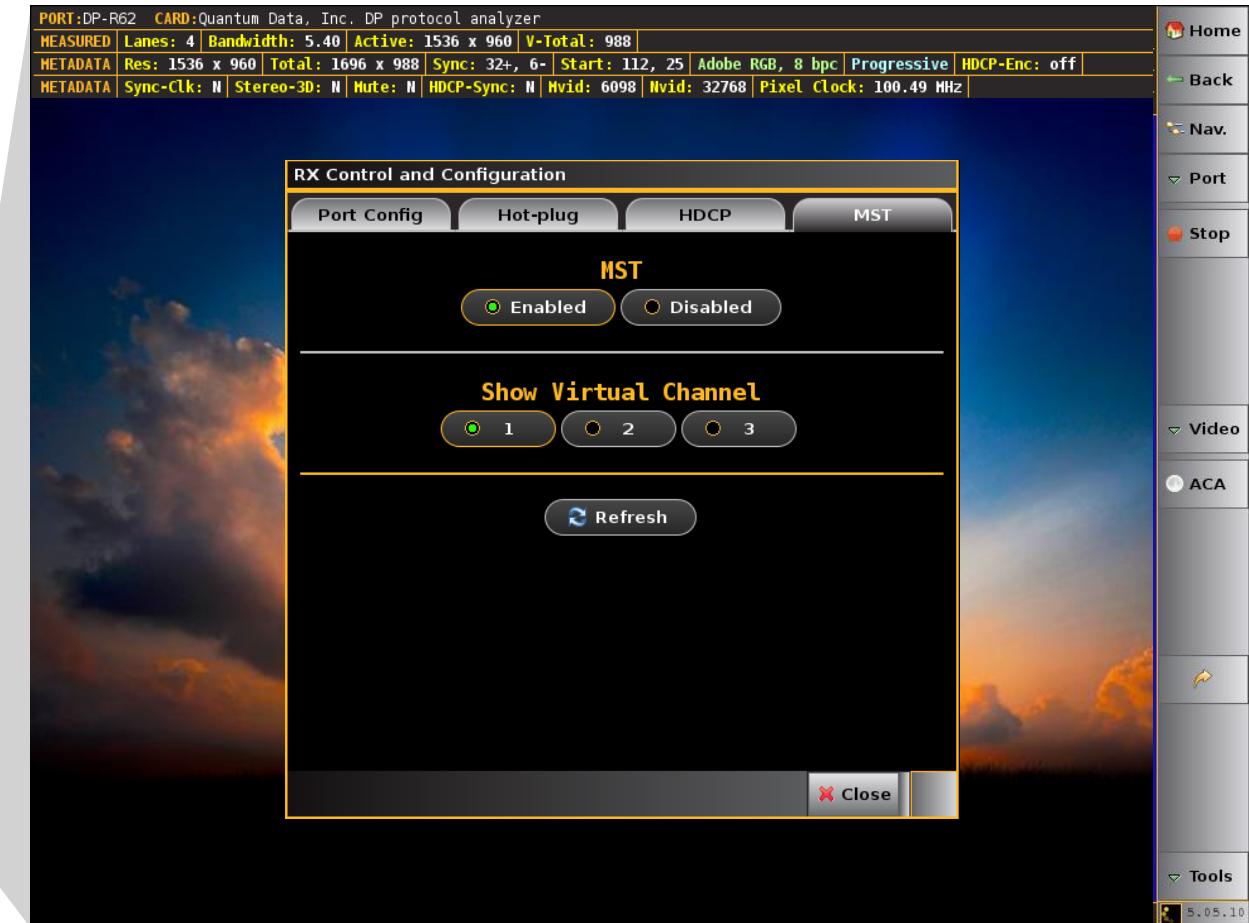
980 DisplayPort 1.4 Video Generator / Analyzer – Source HDCP Test

- View summary of link training transactions from a DisplayPort source
- Configure analyzer for HDCP version 1.3 or HDCP 2.2 emulation.
- View summary of HDCP transactions.



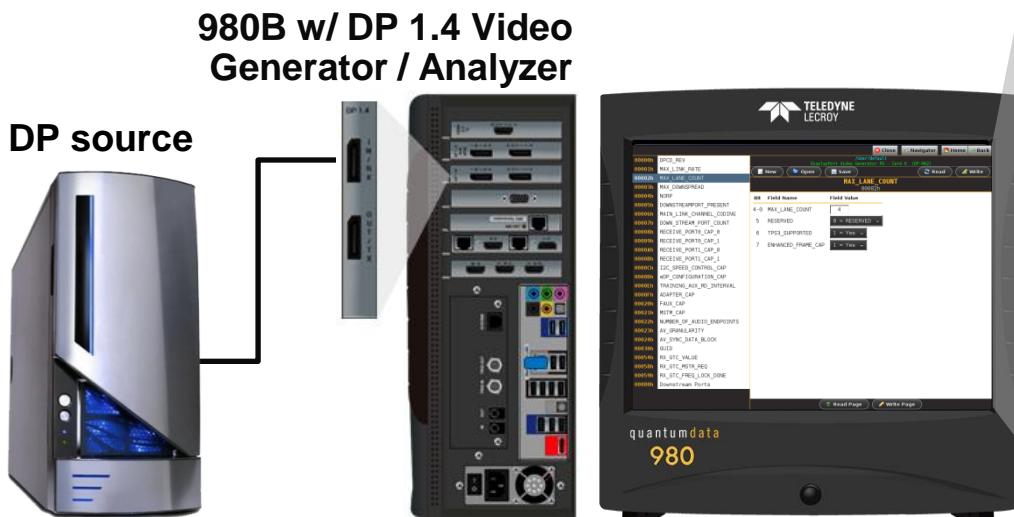
980 DisplayPort 1.4 Video Generator / Analyzer – Source MST Test

- Emulate an MST branch device to test an MST-capable DisplayPort source.
 - Configure number of downstream display nodes.
 - Emulate the downstream nodes as well.



980 DisplayPort 1.4 Video Generator / Analyzer – DPCD Emulation

- Emulate the EDID and DPCD of DisplayPort monitor or sink operating up to 8.1Gbps link rates (HBR3).
 - Use the EDID Editor and the DPCD Editor (shown) to emulate DisplayPort display devices to test a source's response.
 - Save EDID and DPCD configurations for reuse and retesting.

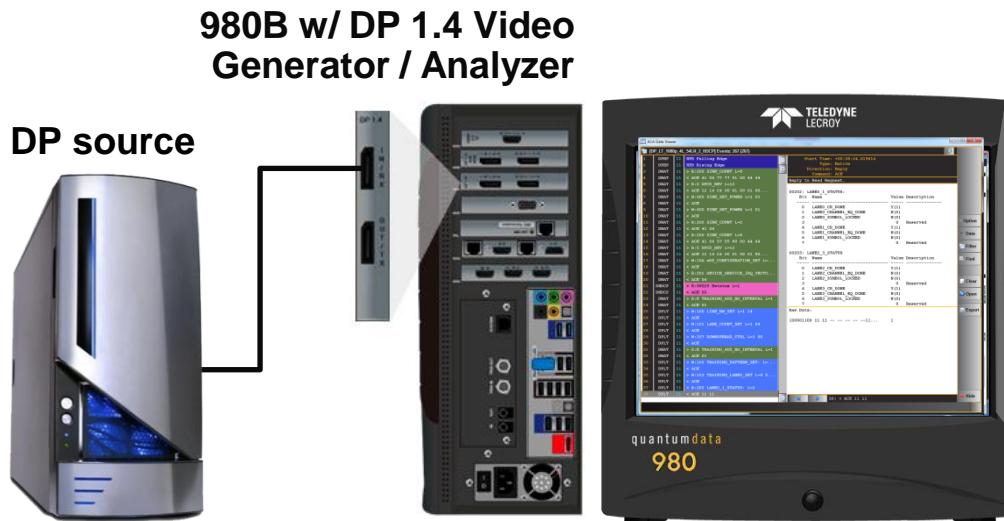


The screenshot shows the Teledyne Lecroy software interface for DPCD emulation. The main window displays a list of DPCD fields with their memory addresses and descriptions. A specific field, "MAX_LANE_COUNT", is selected and highlighted in blue. The right side of the screen shows a detailed configuration dialog for this field, with the current value set to "000002h". The dialog includes fields for "Field Name" (MAX_LANE_COUNT), "Field Value" (4), and "Field Description" (0 = RESERVED). Other fields shown in the list include DPCD_REV, MAX_LINK_RATE, and various receive and transmit port capabilities.

Bit	Field Name	Field Value
4-0	MAX_LANE_COUNT	4
5	RESERVED	0 = RESERVED
6	TPS3_SUPPORTED	1 = Yes
7	ENHANCED_FRAME_CAP	1 = Yes

980 DisplayPort 1.4 Video Generator / Analyzer – Source Aux Channel Test

- Monitor aux channel transactions with a DisplayPort source
 - Monitor link training transactions.
 - Monitor EDID exchanges and HDCP transactions.
 - Save results and disseminate to colleagues at other locations.



The screenshot displays the ACA Data Viewer interface. On the left, a list of transactions is shown with their hex values and descriptions. Some entries are highlighted in pink or green. On the right, detailed status information for LANE0 and LANE2 is provided, including bit names, values, and descriptions. A "Raw Data" section at the bottom shows binary data. The interface includes various toolbars and a sidebar with options like "Data", "Filter", and "Find".

ACA Data Viewer

[DP_LT_1080p_4L_54LR_2_HDCP] Events: 267 (267)

Start Time: +00:58:24.619614
Type: Native
Direction: Reply
Command: ACK

Reply to Read Request.

00202: LANE0_1_STATUS:

Bit Name	Value Description
0 LANE0_CR_DONE	Y(1)
1 LANE0_CHANNEL_EQ_DONE	N(0)
2 LANE0_SYMBOL_LOCKED	N(0)
3	0 Reserved
4 LANE1_CR_DONE	Y(1)
5 LANE1_CHANNEL_EQ_DONE	N(0)
6 LANE1_SYMBOL_LOCKED	N(0)
7	0 Reserved

00203: LANE2_3_STATUS

Bit Name	Value Description
0 LANE2_CR_DONE	Y(1)
1 LANE2_CHANNEL_EQ_DONE	N(0)
2 LANE2_SYMBOL_LOCKED	N(0)
3	0 Reserved
4 LANE3_CR_DONE	Y(1)
5 LANE3_CHANNEL_EQ_DONE	N(0)
6 LANE3_SYMBOL_LOCKED	N(0)
7	0 Reserved

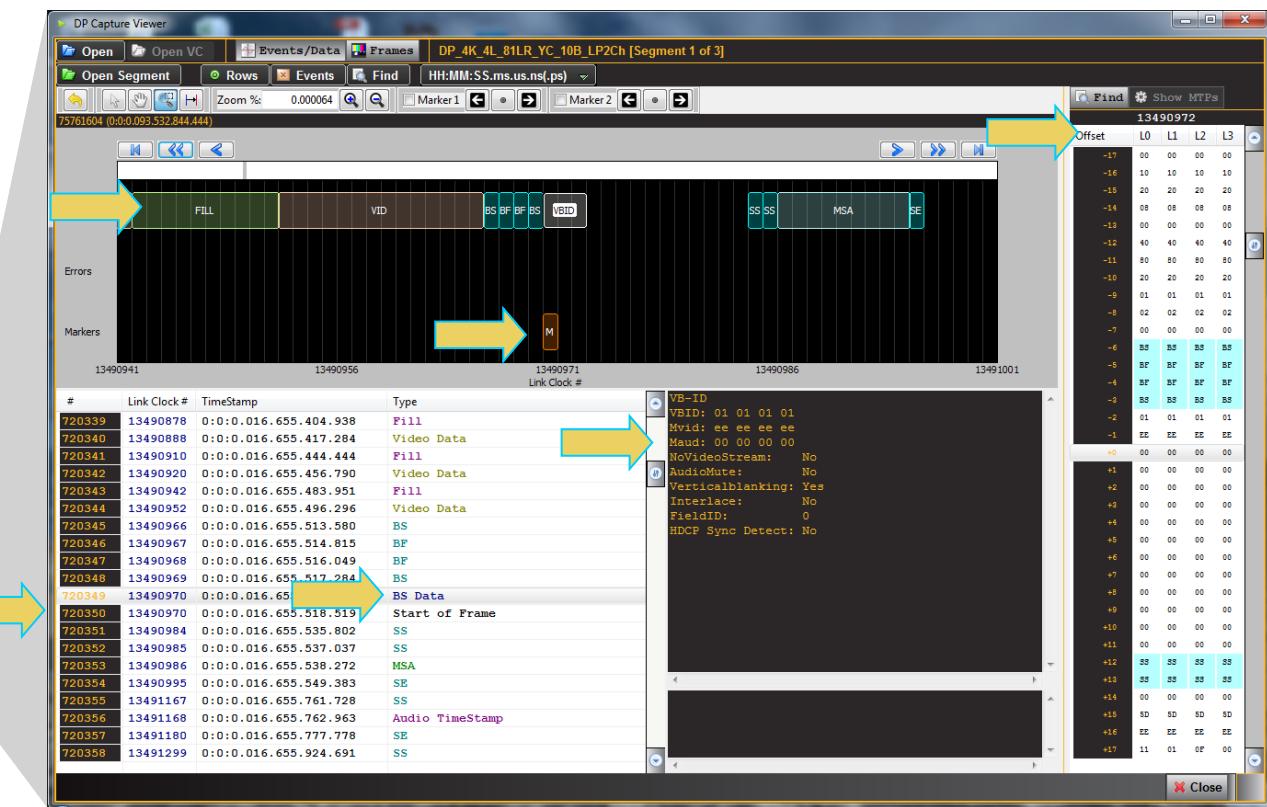
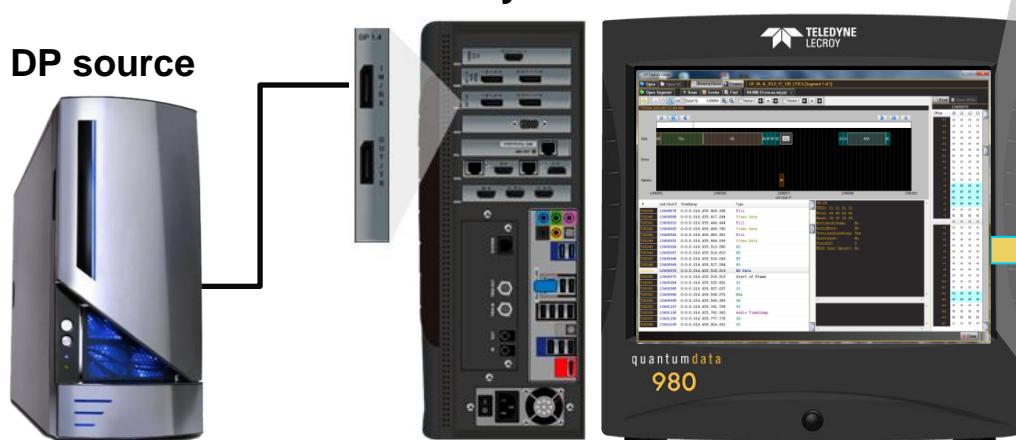
Raw Data:

[0000] [00 11 11 -- -- -- --] [...]

980 DisplayPort 1.4 Video Generator / Analyzer – Capture Data Analysis

- Capture and store DisplayPort mainstream and secondary data.
 - View main stream data on graphical view (Event Plot) and table view (Data Decode).
 - View each link symbol event.
 - View protocol symbol elements, mainstream attributes, secondary data stream.
 - Protocol errors are indicated.

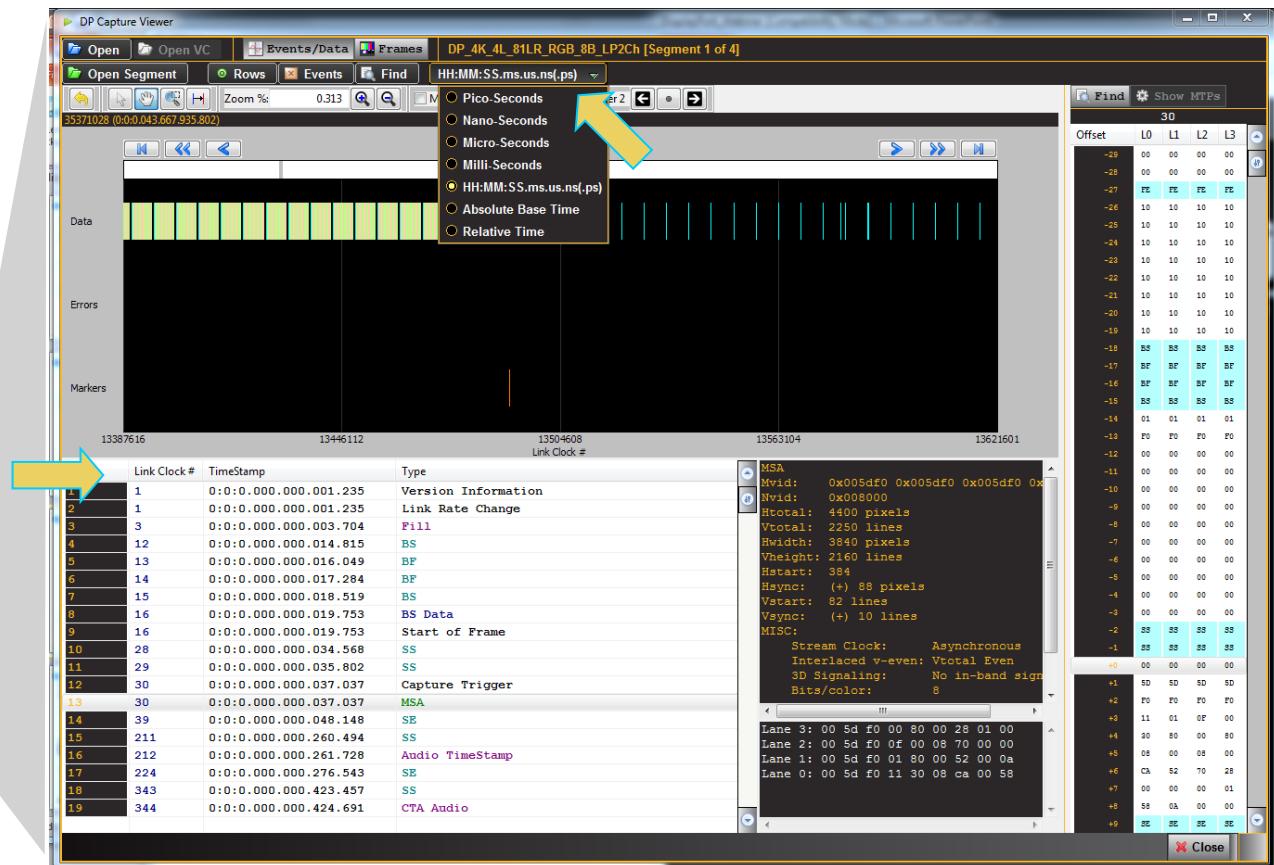
980B w/ DP 1.4 Video Generator / Analyzer



980 DP 1.4 Video Generator / Analyzer – Capture Data Analysis

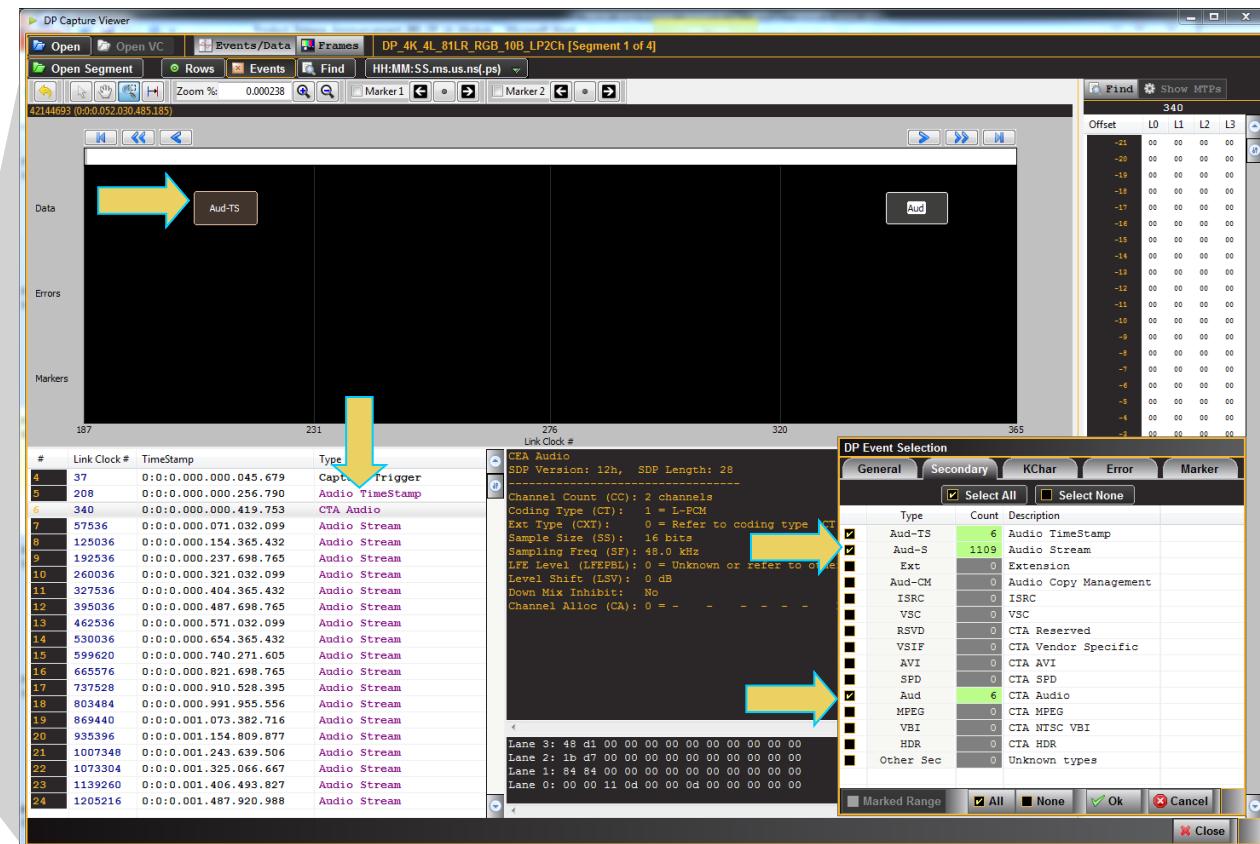
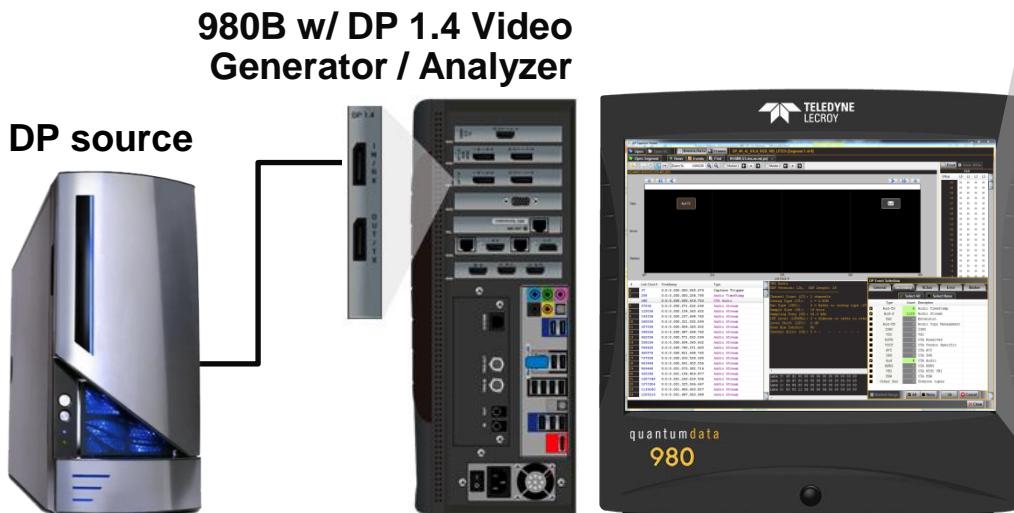
- Capture and store mainstream and secondary data.
- View data elements in tabular form.
- View details of any data element in raw hex or human readable text.
- Select the timestamp format to view each record.
- Disseminate captures to subject matter experts for further analysis.

980B w/ DP 1.4 Video Generator / Analyzer



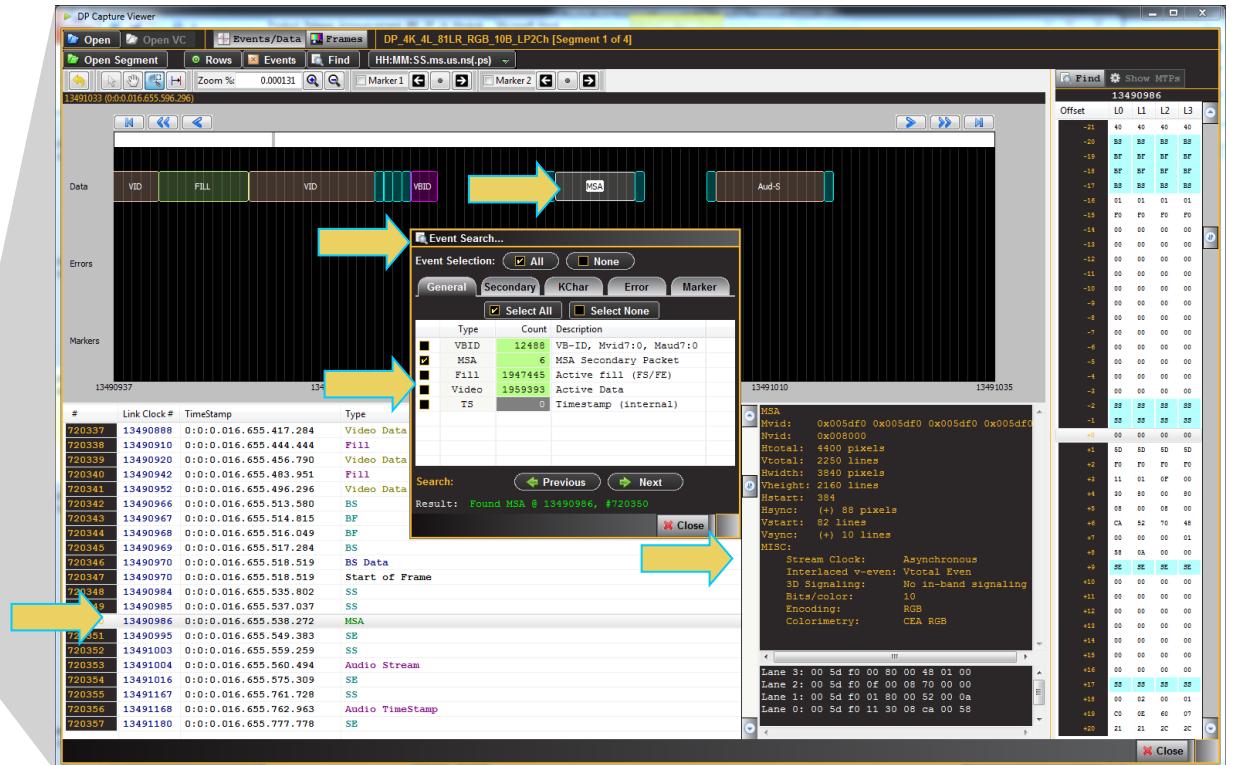
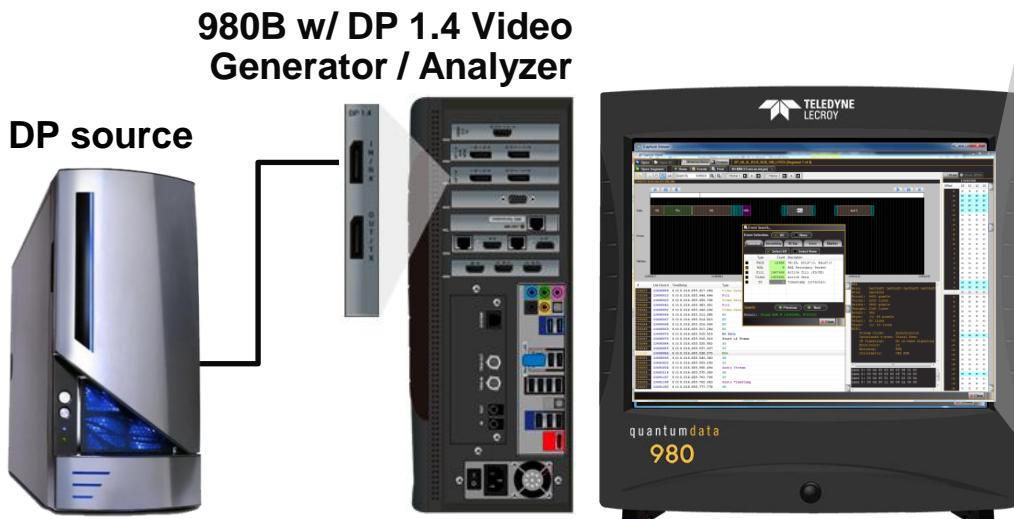
980 DisplayPort 1.4 Video Generator / Analyzer – Capture Data Analysis

- Capture and store DisplayPort mainstream and secondary data.
- Filter view to show only data elements of interest (example view only secondary data packets such as audio-related packets).



980 DisplayPort 1.4 Video Generator / Analyzer – Capture Data Analysis

- Capture and store DisplayPort mainstream and secondary data.
- Search for specific data types such as MSA packet data.



980 DisplayPort 1.4 Video Generator / Analyzer – Capture Data Analysis

- Capture and store DisplayPort mainstream and secondary data.
 - View details of data element alignment.
 - Measure timing between elements or groups of data elements.

