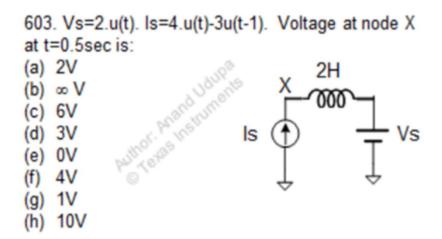
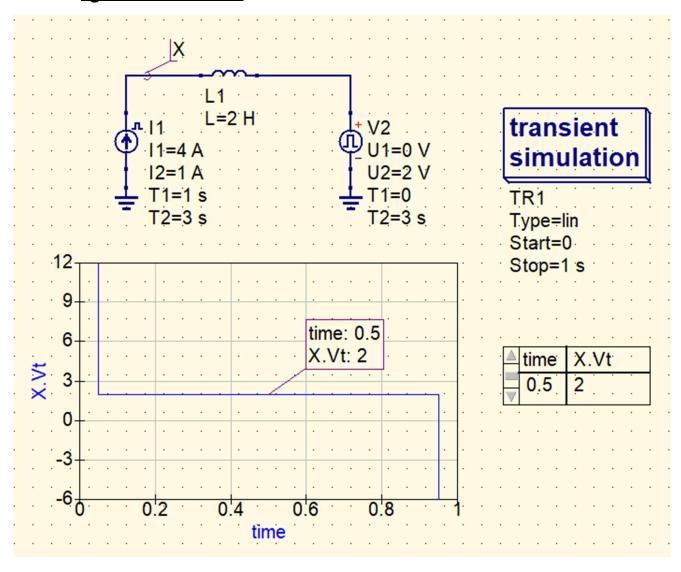
TI BYTE Simulation Exercise

Week 3: RL Circuits

• Question 1:



> QUCS Circuit:



- X is used to label the node and find the voltage at that node.
- Both the inductors have zero initial current through them.

> **QUCS Result:**

Therefore, from the simulation, we get our answer as:

$$V_x = 2V$$

Answer: (a)

Conclusion:

- The final voltage at X can be found out using the Superposition Theorem.
- Upon considering the current source I1 only,

$$I_1 = 4. u(t) - 3. u(t - 1)$$

$$I_1 = \begin{cases} 4, & 0 \le t \le 1 \\ 1, & t > 1 \end{cases}$$

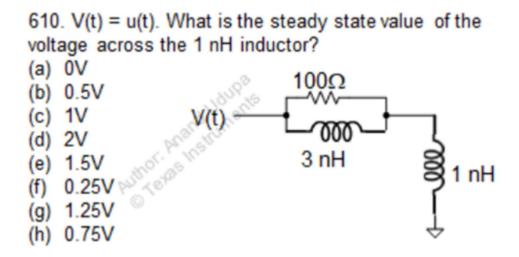
- Thus, voltage across the inductor due to the current source only,

$$V_L = Lrac{dI}{dt}$$
 $I_1 = egin{cases} \delta(t), & t=0 \ -\delta(t), & t=1 \ 0, & elsewhere \end{cases}$

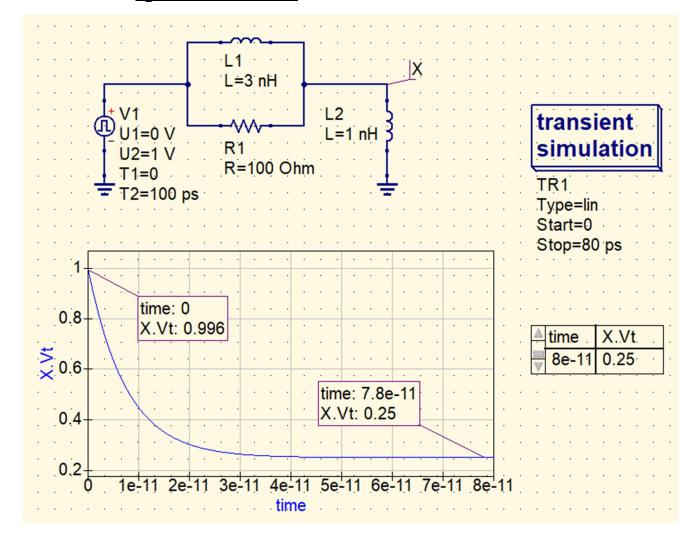
- Due to the voltage source V2, the voltage at X is 2V.
- Therefore, it can be said that, at t = 0.5s, the voltage at X is

$$V_x = V_L + V_2 = (0 + 2) V = 2V.$$

• Question 2:



> **QUCS Circuit:**



- X is used to label the node at which the unknown voltage is to be determined.

- Both the inductors L1 and L2 are initially uncharged, i.e., having zero initial current in them.

> **QUCS Result:**

Therefore, from the simulation, we get our answer as:

$$V_x = 0.25V$$

Answer: (f)

Conclusion:

- At t = 0, since both the inductors are uncharged, they have high impedances and behave as open circuits (o.c.).
- Thus, no current flows through them, or even through the R1.
- Therefore, the voltage at node X is

$$V_x \approx V_1 = 1V$$

- Now, at t = ∞, both the inductors have reached steady state and have low impedance and behave as almost short circuits (s.c.).
- Since the current gets low impedance path through L₁, the equivalent impedance becomes,

$$|L_1||R_1\approx L_1$$

- Thus, the voltage at X at $t = \infty$ (), is just a voltage divider between L_{1} and L_{2} ,
- The final voltage at X = $\frac{L_2}{L_1 + L_2} = \frac{1}{(1+3)} = \frac{1}{4} = 0.25 \text{ V}$