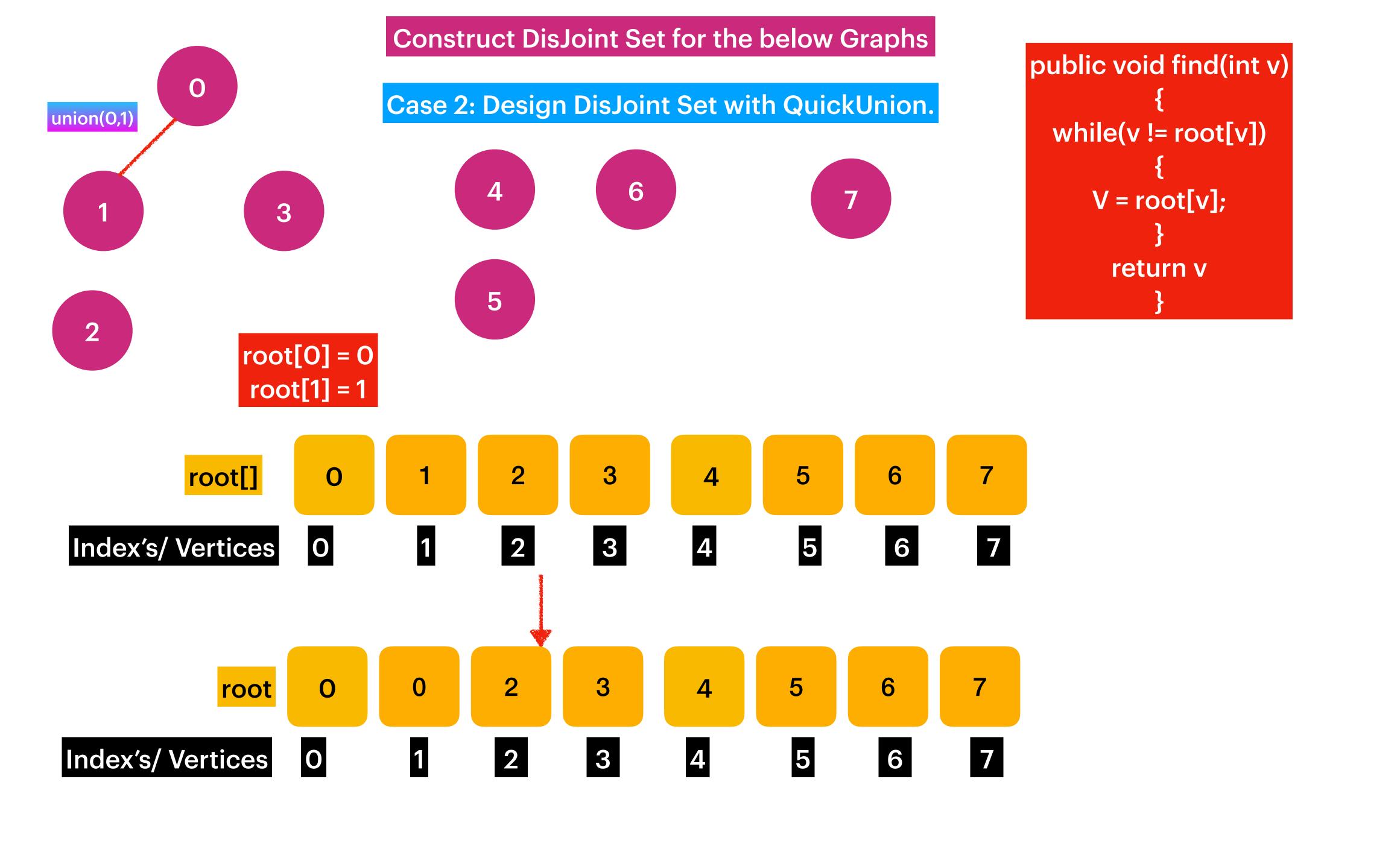
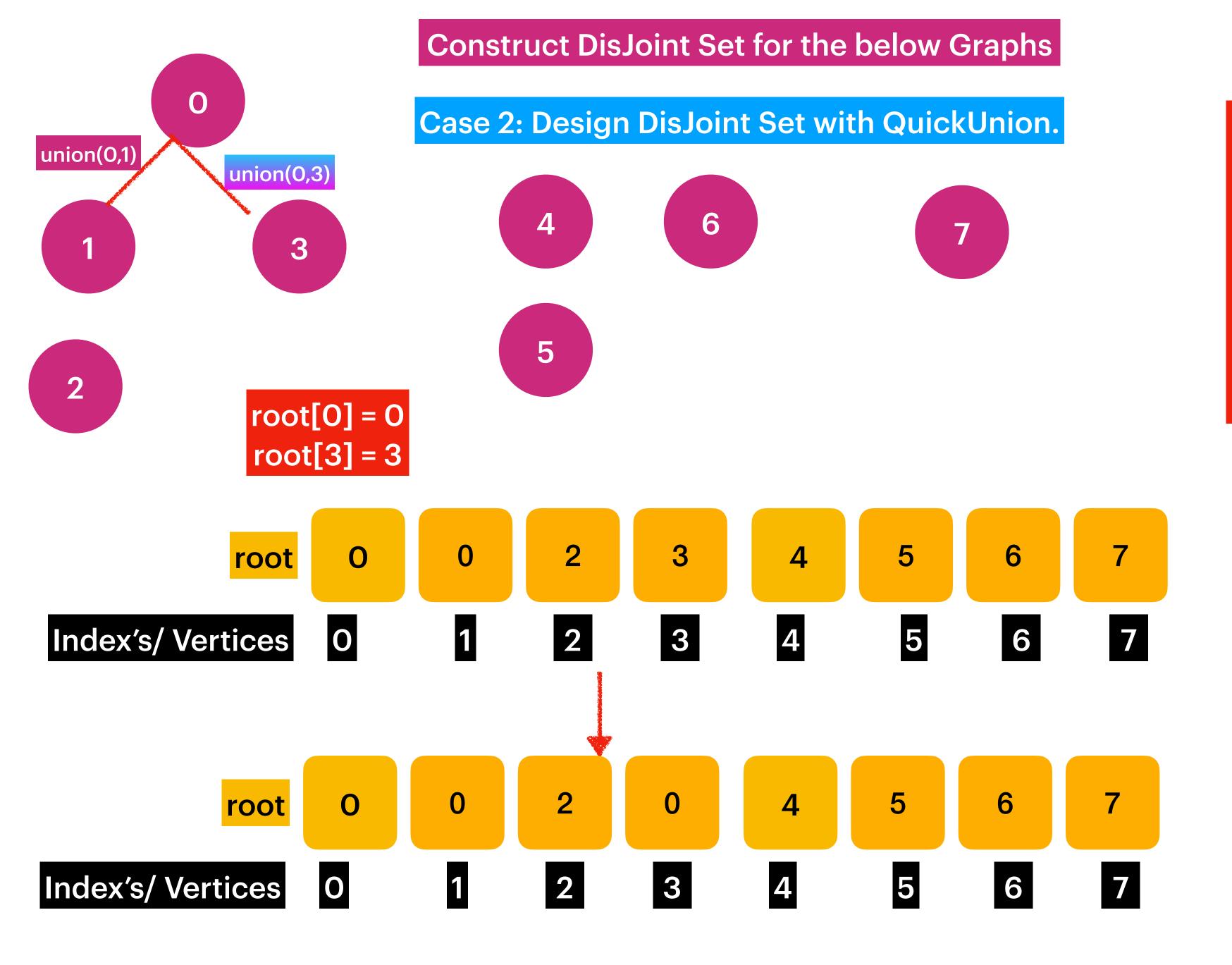
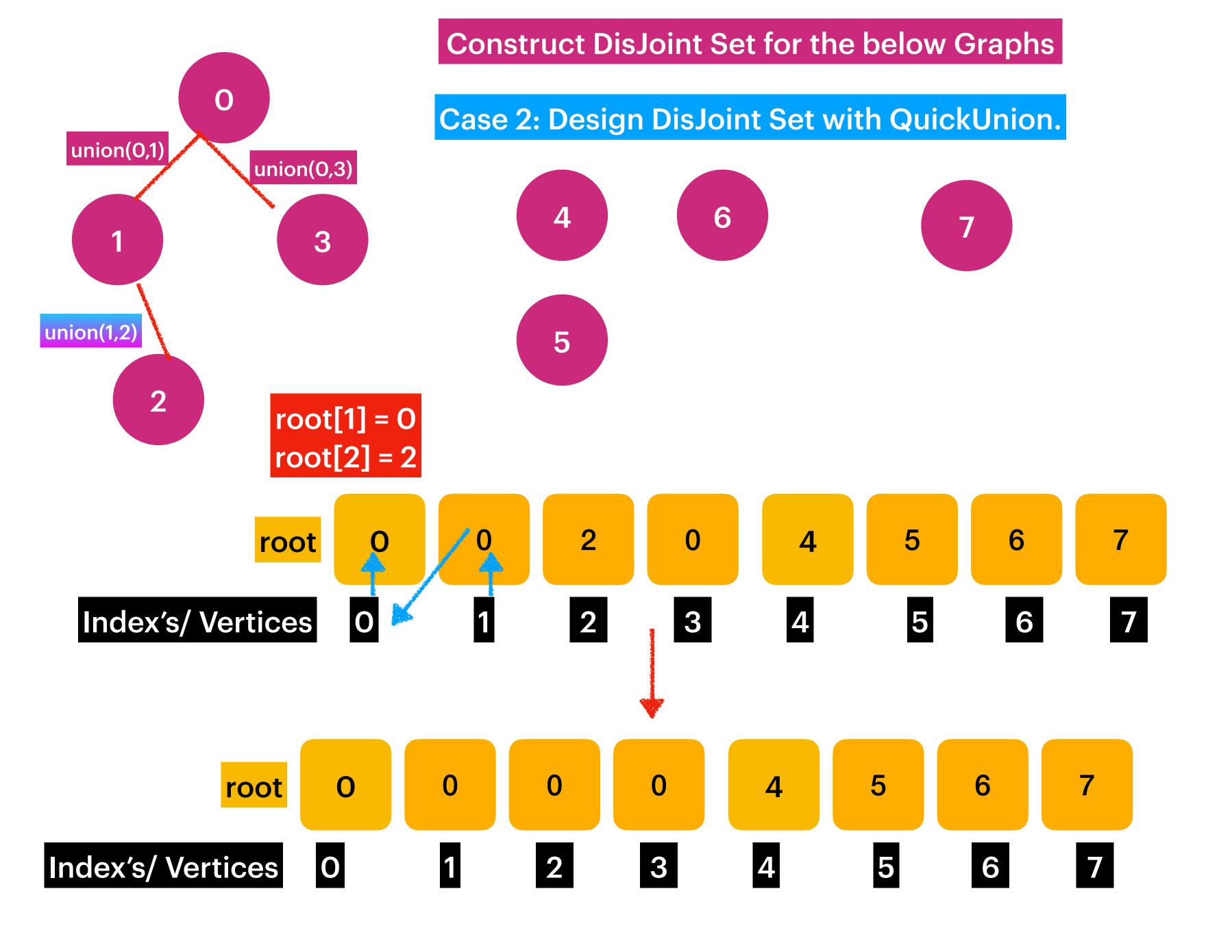
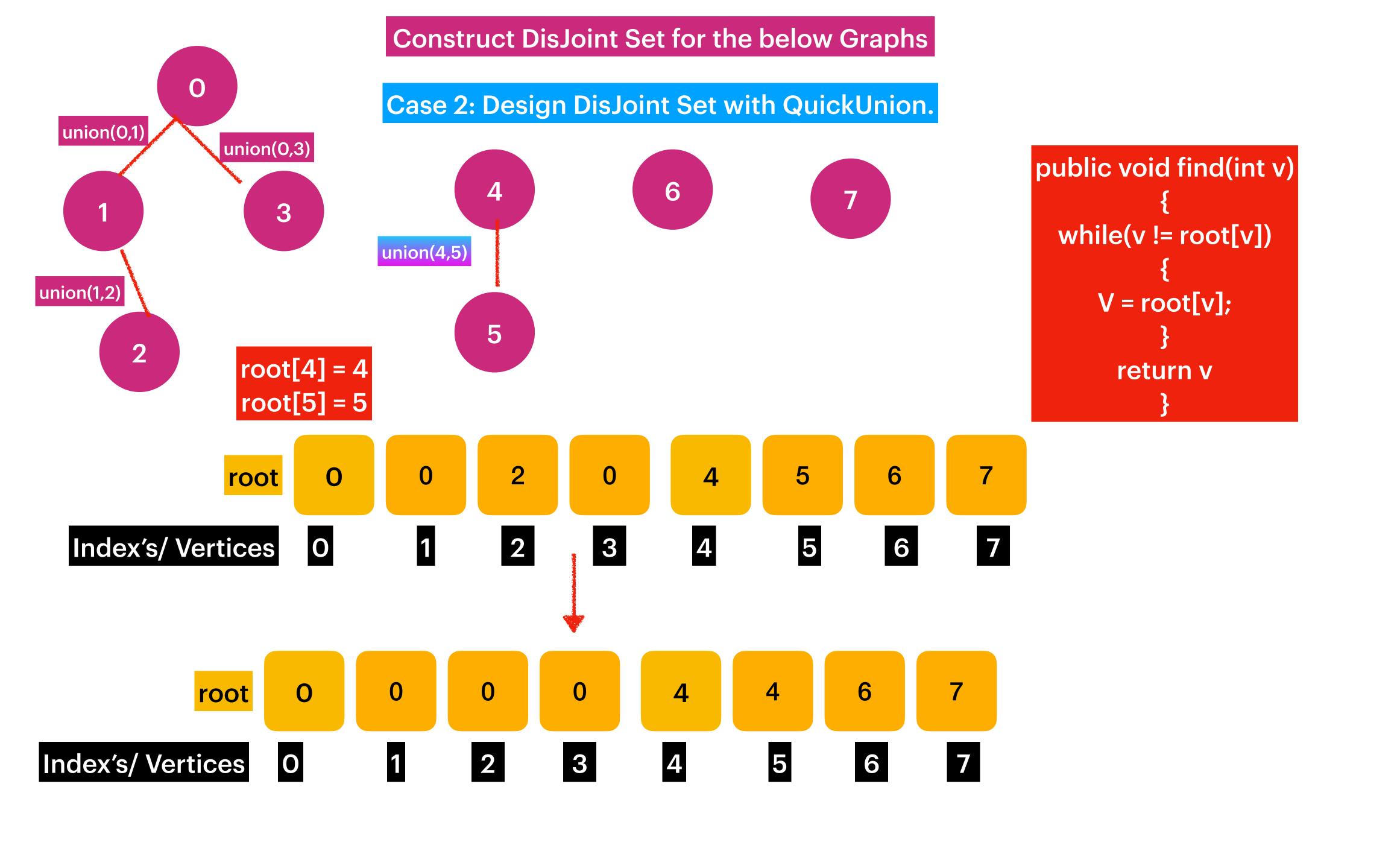


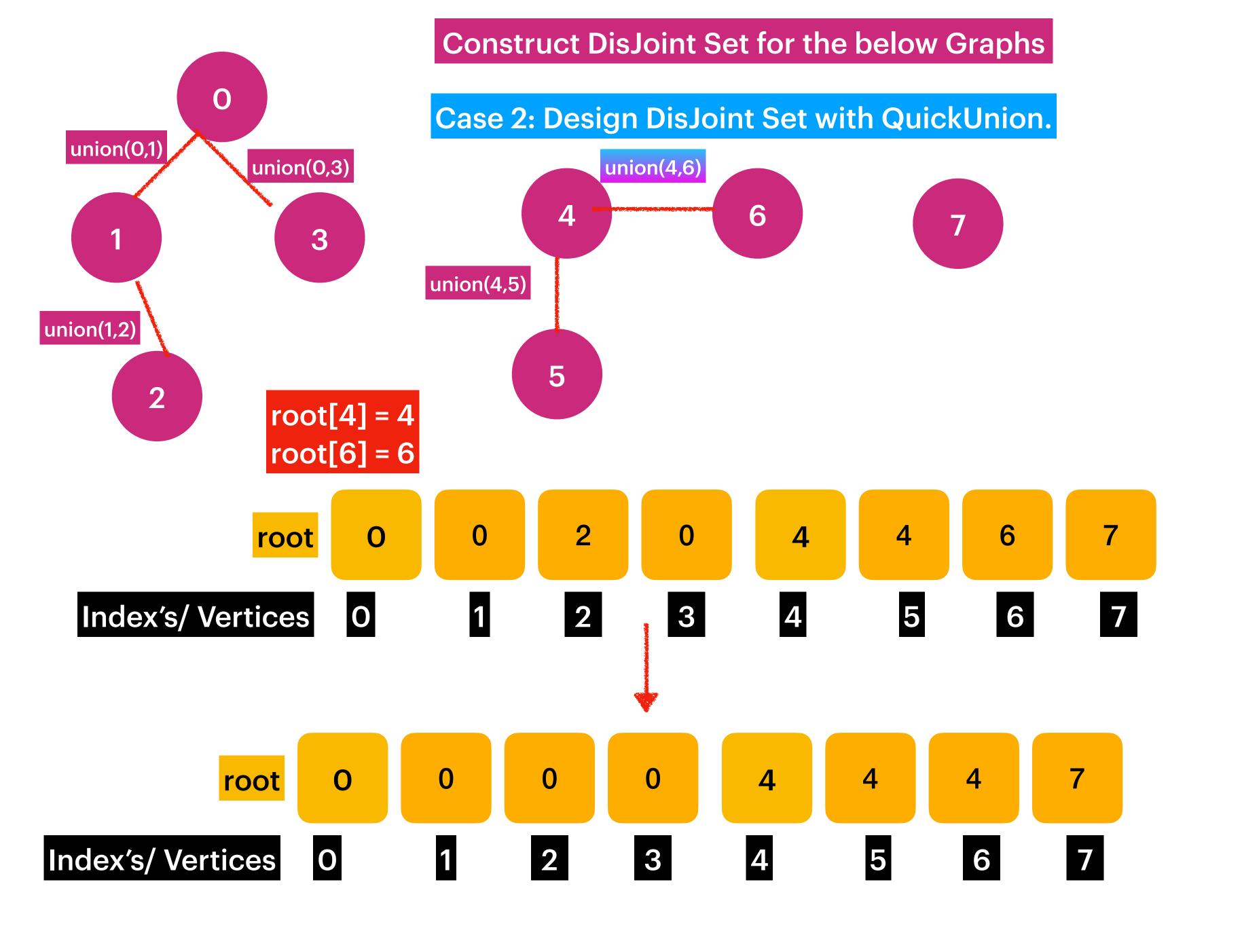
Array Index can be represented as Vertex. Index value can be represented by root.

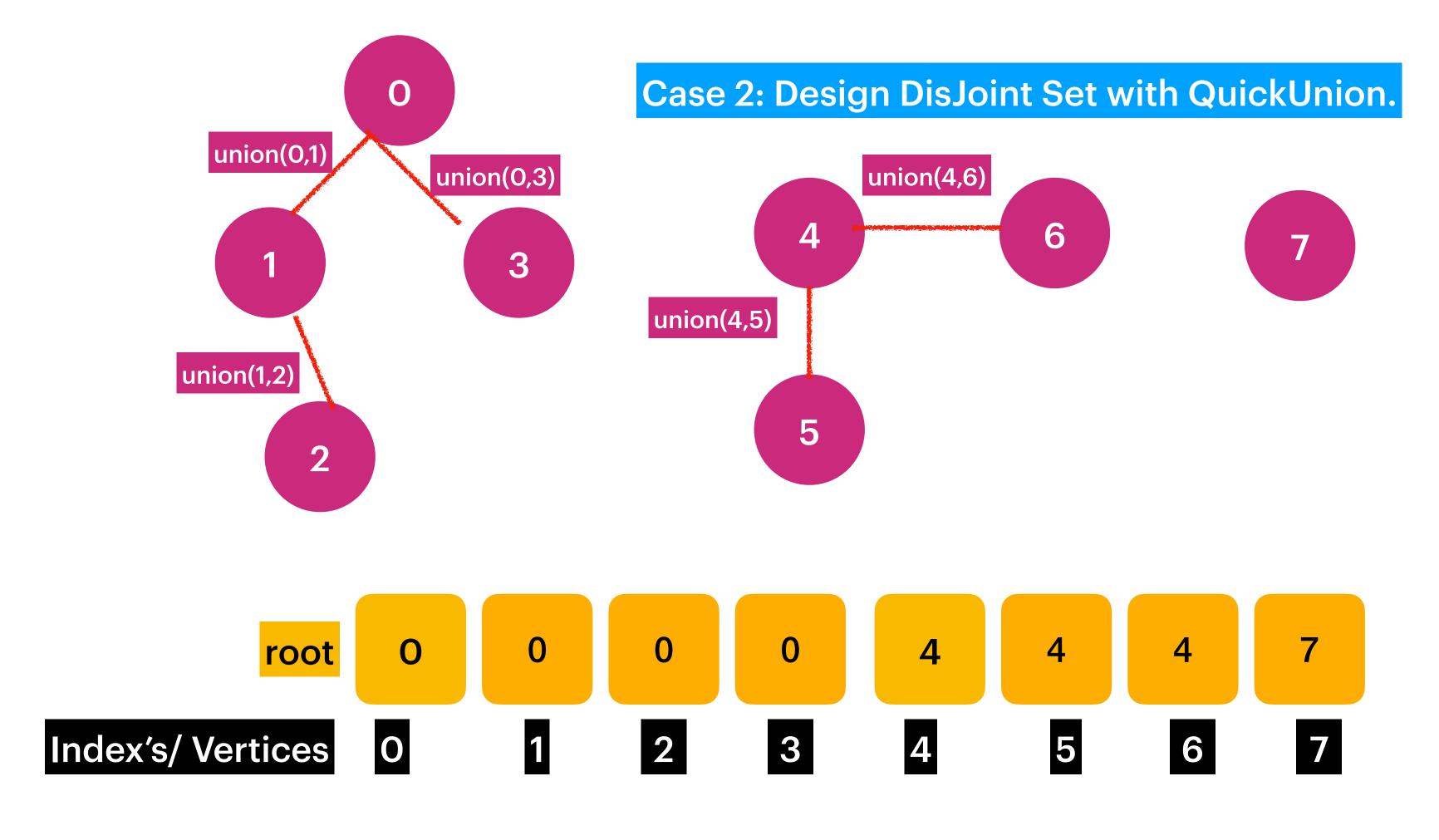






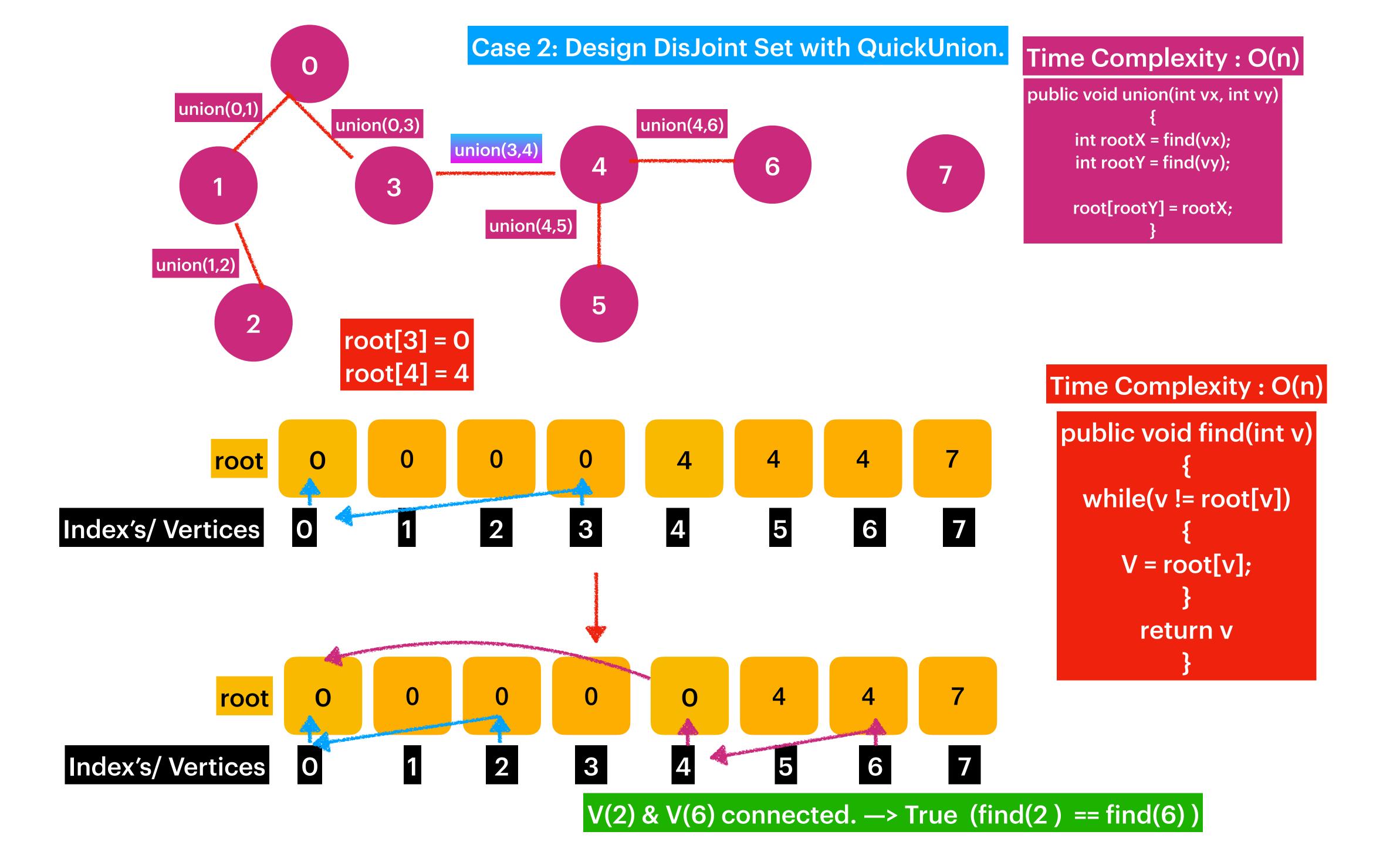


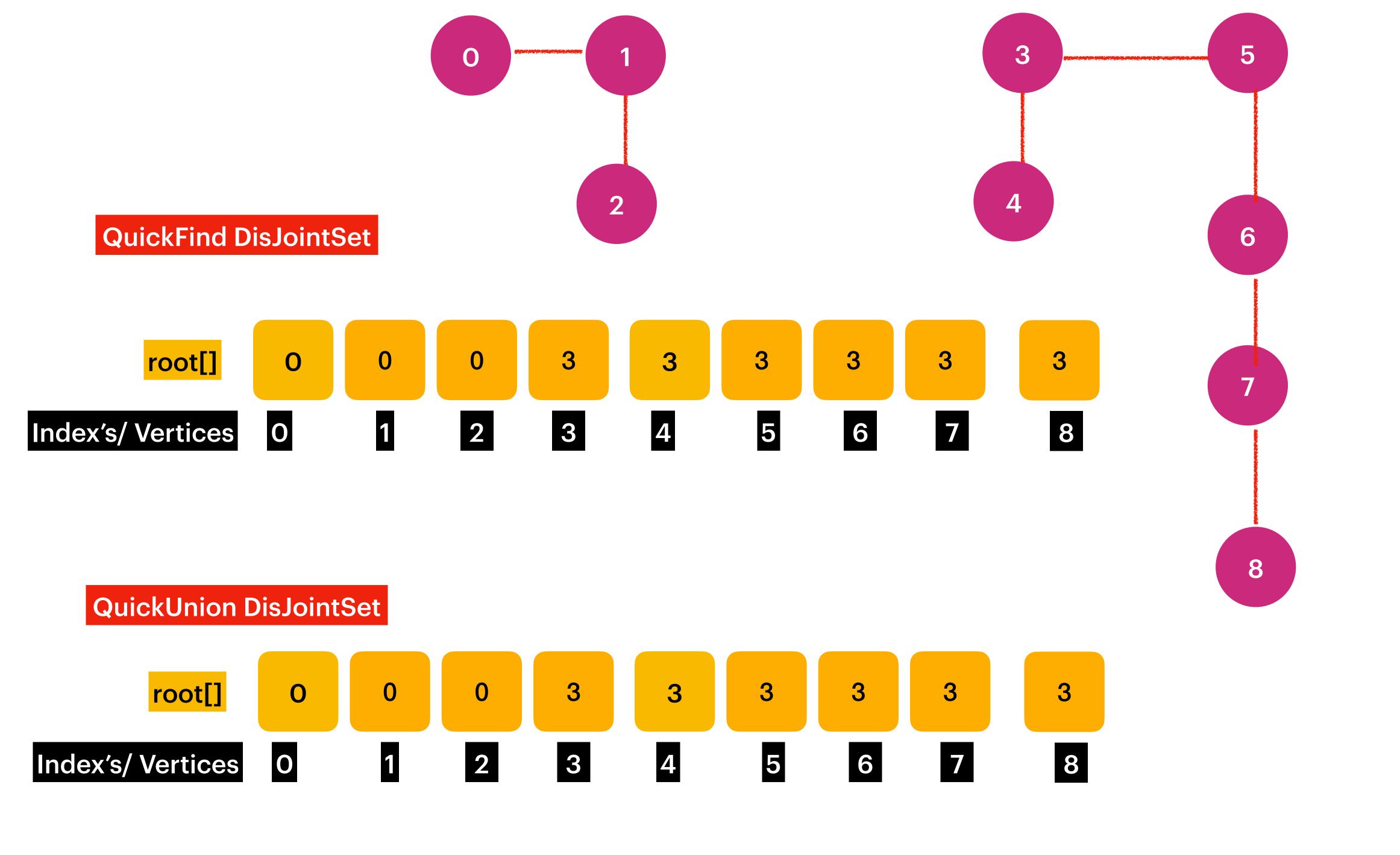


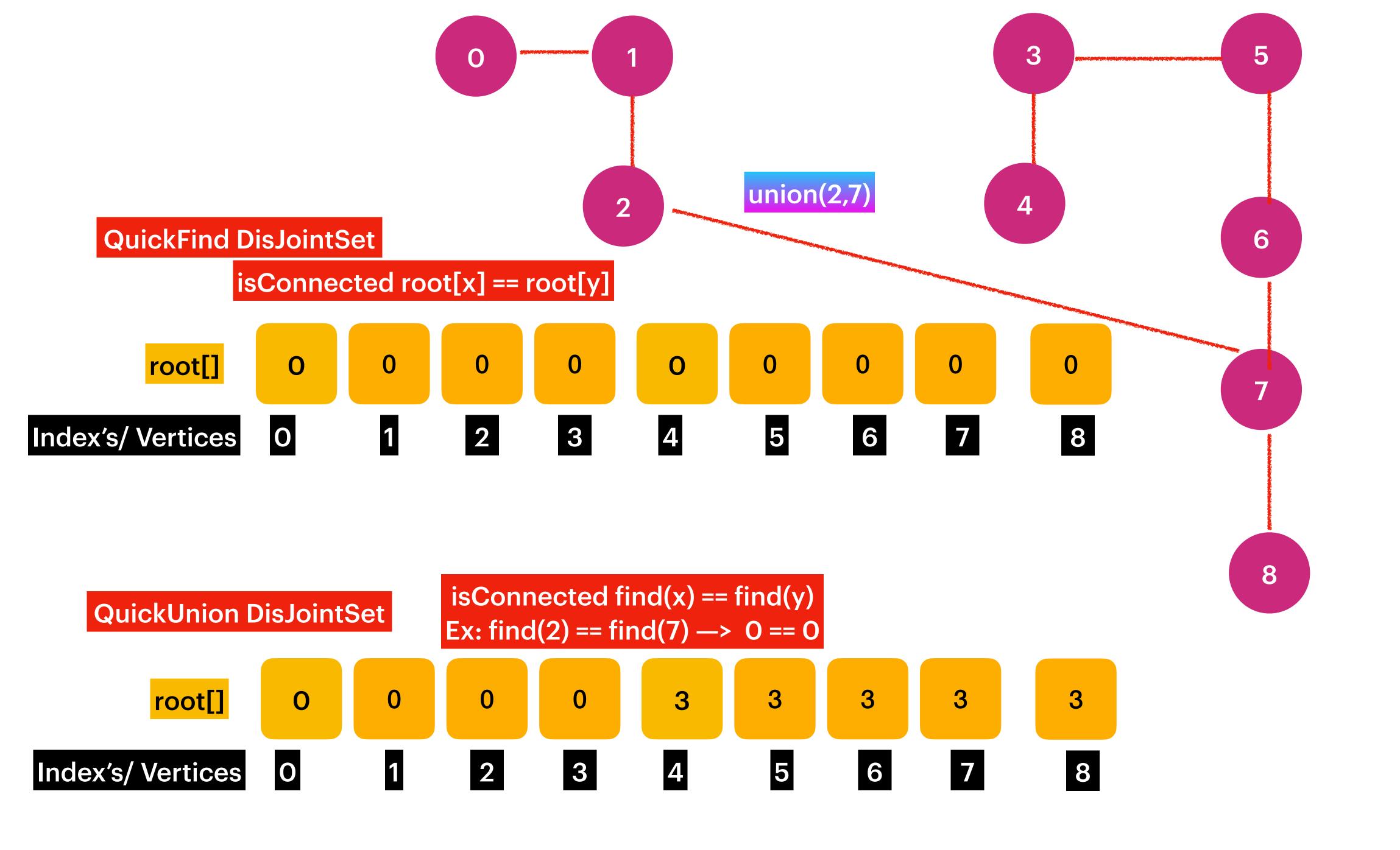


V(2) & V(3) connected. -> True (find(2) == find(3))

V(2) & V(5) are connected. —> False (find(2)!= find(5))







Case 3: Design DisJoint Set with PathOptimisation.

