

1. At what moment did you feel the most engaged with any of the information in this module or the activities?

Ans: The topic of MIPS64 Architecture was the most engaging since in module 1 also we studied about it but in brief.

2. At what moment did you feel the most confused with any of the information of this module or the activities?

Ans: I felt the thinking assignment was really confusing since I had very little idea about coding in MIPS, since I had an idea how to code in C or C++ but coding in MIPS was confusing.

3. What has surprised you the most?

Ans: The facts about ISA and Compilers section surprised me the most. Since I had less knowledge about compilers and a few facts related to performance with compilers surprised me the most.

4. What previous assumptions about this module have been challenged? Confirmed?

Ans: I had assumed the entire concept of big-endian and little-endian completely opposite to what is described in the text book and now have got a clear picture of what exactly it means.

5. In regard to this module, what do you know now that you did not know prior to the learning activities?

Ans: previously I did not know the execution of instructions in details like how fetch is done and results are submitted to registers which I know after taking this course.