FALL SCHEDULE		
WEEK	**LECTURE**	**LABORATORY**
1 Sep 6 Sep 12	Introduction to COE 538 Scope and objectives Management **INTRODUCTION TO THE HCS12 µ-CONTROLLER** - 1.3 Computer Hardware Organization.....2-7 - 1.5 Memory system Operation.....10-12 - 1.6 Program Execution.....12-21 - 1.8 The HCS12 CPU Registers.....22-24 - 1.9 HCS12 Addressing Modes.....24-29 - 1.11 A Sample of HCS12 Instructions.....29-34 **HCS12 ASSEMBLY PROGRAMMING** - 2.2 Assembly Language Program Structure..... 40-41 - 2.3 Assembly Directives..... 42-44	
2 Sep 13 Sep 19	**HCS12 ASSEMBLY PROGRAMMING** - 2.5 Writing Programs to Do Arithmetic..... 47-57, 59-60 - 2.6 Program Loops.....63-72 - 2.7 Shift and Rotate Instructions.....72-79 - 2.8 Boolean Logic Instructions..... 79-80 - 2.9 Bit Test and Manipulate Instruction..... 80-81 - 2.10 Program Execution Time..... 81-83 **HW/SW DEVELOPMENT TOOLS FOR HCS12** - 3.2 Development Tools for the HCS12.....88-90 - 3.8 Using CodeWarrior.....123-137 **ADVANCED ASSEMBLY PROGRAMMING** - 4.10 Intro to Parallel I/O Port & Devices..... 185-188 - 4.11 Simple I/O Devices..... 188-196	***Lab 1*** Using the *CodeWarrior* IDE and Introduction to Assembly Programming
3 Sep 20 Sep 26	**ADVANCED ASSEMBLY PROGRAMMING** - 4.3 Stack.....146-148 - 4.4 What Is a Subroutine.....148-151 - 4.5 Issues related to Subroutine Calls.....151-153 - 4.6 The Stack Frame..... 153-154 - 4.9 Subroutines for Creating Time Delay..... 184-185 **ADVANCED PARALLEL I/O** - 7.5 The HCS12 Parallel Ports..... 305-317 - 7.7 Liquid Crystal Displays (LCDs)..... 322-323 - 7.8 The HD4478U LCD Controller..... 323-336 - 7.9 Interfacing Parallel Ports to a Keypad..... 338-343	***Lab 2*** Programming the I/O Devices

FALL SCHEDULE		
WEEK	**LECTURE**	**LABORATORY**
4 Sep 27 Oct 3	**INTERRUPTS** - 6.2 Fundamental Concepts of Interrupts.....262-266 **ANALOG-TO-DIGITAL CONVERTER** - 12.2 Basics of A/D Conversion..... 592-598 - 12.3 The HCS12 A/D Converter.....598-607 - 12.4 The Functioning of the ATD Module..... 607-610 - 12.5 Procedure for Performing A/D Conversion. 611-614	**---"---**
5 Oct 4 Oct 10	**INTERRUPTS, CLOCK GENERATION, AND OPERATION MODES** - 6.3 Resets..... 266-267 - 6.4 HCS12 Exceptions..... 267-271, 273-274 - 6.6 Clock and Reset Generation Block..... 277-286 - 6.7 Real-Time Interrupt..... 287-289, 294 - 6.11 HCS12 Operation Modes..... 295-296 **TIMER FUNCTIONS** - 8.3 Standard Timer Module..... 365-366 - 8.4 Timer Counter Register..... 366-368 **(Thanksgiving, Monday, Oct. 11, University closed)**	***Lab 3*** Battery and Bumper Displays
6 Oct 11 Oct 17	**TIMER FUNCTIONS** - 8.5 Input-Capture Function..... 368-370, 372-376 - 8.6 Output-Compare Function..... 377-385, 393-396 - 8.7 Pulse Accumulator..... 396-407 - 8.8 Modulus Down Counter..... 407-410	**---"---**
7 Oct 18 Oct 24	**MIDTERM** Covers material up to end of week 5 (excluding chapter 8)	***Lab 4*** Motor Control & Using the Hardware Timer
8 Oct 25 Oct 31	**C LANGUAGE PROGRAMMING** - 5.3 Types, Operators, and Expressions..... 207-211 - 5.4 Control Flow..... 211-214 - 5.5 Input and Output..... 215-216 - 5.6 Functions and Program Structure..... 216-219 - 5.7 Pointers, Arrays, Structures, and Unions..... 219-224 - 5.8 Writing C Programs to Perform Simple I/O... 224-227 - 5.11 Using the CodeWarrior to Develop C Progr. 231-243	***Lab 5*** Robot Roaming Program

FALL SCHEDULE		
WEEK	**LECTURE**	**LABORATORY**
9 Nov 1 Nov 7	**SERIAL COMMUNICATION INTERFACE** - 9.3 The RS-232 Standard.....451-452 - 9.4 The HCS12 SCI..... 453-454 - 9.5 SCI Baud Rate Generation..... 454-455 - 9.6 The SCI Operation..... 455-460 - 9.9 Interfacing SCI with TIA-232..... 464-469	**---"---**
10 Nov 8 Nov 14	**THE SPI FUNCTION** - 10.2 Introduction to the SPI Function..... 474-475 - 10.3 Registers Related to the SPI Subsystem..... 475-478 - 10.4 SPI Operation..... 478-480 - 10.5 SPI circuit connection..... 482-484 - 10.6 Configuration / Data Transfer in SPI..... 484-486 - 10.8 The 74HC595 Shift Register..... 487-489	***Project*** Robot Guidance Challenge
11 Nov 15 Nov 21	**INTER-INTEGRATED CICUIT (I^2C) INTERFACE** - 11.2 The I^2C Protocol..... 530-541 - 11.3 An Overview of the HCS12 I^2C Module..... 541-542 - 11.4 Registers for I^2C Operation..... 542-549 - 11.5 Programming the I^2C Module..... 549-552	**---"---**
12 Nov 22 Nov 28	**INTERNAL MEMORY CONFIGURATION AND EX-TERNAL EXPANSION** - 14.3 Internal Resource Remapping..... 694-699 - 14.4 Expanded Memory Mapping..... 699-701 - 14.7 HCS12 External Memory Interface..... 725-736 - 14.9 Memory Devices.....736-744 - 14.10 Example of External Memory Expansion...744-751	**---"---**
13 Nov 29 Dec 5	**REVIEW AND CATCH-UP**	***Project*** Submission
14 - 15 Dec 6 Dec19	**FINAL EXAMINATION**	Covers material up to end of week 12