

Makefile Explanation

The provided Makefile is a build automation script that helps you compile and link the source files into an executable named `my_program`. It uses the GCC compiler with specific flags to compile the C source files.

How to compile and run the program

1. **Compilation:** To compile the program, open the terminal (command prompt) in the directory where the Makefile and source files (`main.c`, `functions.c`, `functions.h`, and `readFile_randWord.c`) are located. Then, simply run the `make` command without any arguments. This will invoke the default rule `all`, which will build the `my_program` executable using the GCC compiler with the specified flags.
2. **Running the Program:** After the compilation is successful, you can run the program using the `"make run"` command. This command will first check if the `my_program` executable is up-to-date. If not, it will rebuild it. Then, it will execute the program, and you will see the output on the terminal.
3. **Cleaning Up:** If you want to clean up and remove the compiled files (executable and object files), you can use the `clean` rule: `"make clean"`. This will remove the `my_program` executable and all object files, leaving only the source files and the Makefile.