

## MODULE-1

### Functional Materials for Memory and Display Systems

**Memory Devices:** Introduction, organic semiconductors; types of organic semiconductors used in memory devices, p-type semiconductor-pentacene and n-type semiconductor-perfluoropentacene. Differences between organic and inorganic memory devices. Construction, working and advantages of pentacene semiconductor chip.

**Resistive RAM (ReRAM) Materials:** Introduction, synthesis of  $\text{TiO}_2$ -RAM nanomaterial by sol-gel method, properties and applications.

**Display Systems:** Introduction, liquid crystals (LCs)-classification, properties and its applications. Construction, working principle and applications of Light Emitting Diodes (LEDs), Organic Light Emitting Diodes (OLEDs), Active-Matrix Organic Light Emitting Diodes (AMOLEDs) and Quantum Light Emitting Diodes (QLEDs).

#### Introduction:

#### Definition of Memory Devices

A memory device is a hardware component used to store data and instructions either temporarily or permanently. They are essential elements in modern electronic systems such as computers, smartphones, tablets, cameras, and embedded devices. Memory devices enable storage and retrieval of digital information in the form of binary code (“0” and “1”), which can represent data and program instructions.

#### Basic Concepts of Memory:

- **Memory Cell:** The smallest unit of storage; each cell holds one bit. Cells are organized into arrays (rows & columns) for efficient access.
- **Types:**
  - Volatile Memory (e.g., RAM – DRAM, SRAM): Requires power to retain data.
  - Non-Volatile Memory (e.g., ROM, Flash, SSD): Retains data even when power is off.
- **Memory Hierarchy:** Registers → Cache → Main Memory (RAM) → Secondary Storage (HDD/SSD), arranged by speed, cost, and capacity.
- **Working Principle:** Many memory devices switch between:
  - High Resistance State (HRS = “0”, OFF)
  - Low Resistance State (LRS = “1”, ON) under applied voltage.
- **Examples:** SRAM (fast, cache), DRAM (dense, main memory), Flash (USB, SSDs).

- **Trends:** New technologies like MRAM, ReRAM, and PCM offer higher speed, density, and energy efficiency.

## Organic Semiconductors in Memory Devices

Organic semiconductors are carbon-based materials with conjugated  $\pi$ -bond systems that enable charge transport through delocalized electrons. When suitably doped or optically/electrically excited, they exhibit semiconducting behavior. Their key advantages include mechanical flexibility, lightweight nature, low-cost processing, and tunable electronic properties, making them highly valuable in flexible electronics, sensors, and organic memory devices.

In organic memory technology, different types of organic semiconductors are employed:

- **Pentacene (p-type semiconductor):** widely used for efficient hole transport.
- **Perfluoropentacene (n-type semiconductor):** enables stable electron transport.
- **Novel conjugated molecules (e.g., 3,6-DATT):** provide enhanced performance in non-volatile memory cells.

These materials form the foundation of next-generation organic memory devices, bridging the gap between mechanical flexibility and electronic functionality, and enabling flexible, high-performance, and low-cost memory solutions.

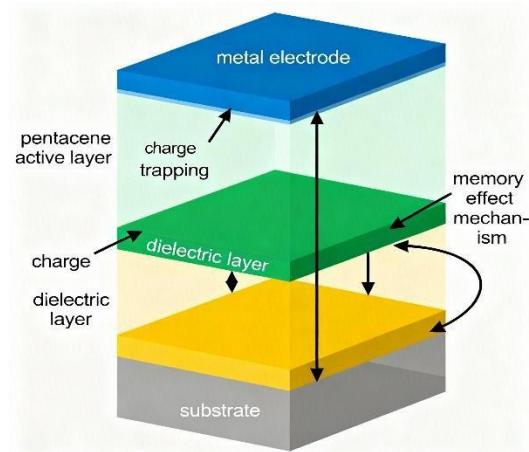
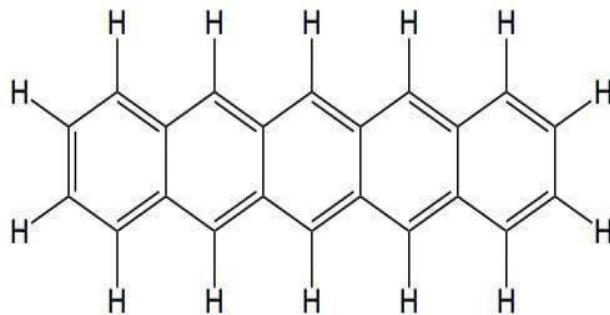
Type	Description	Examples	Carrier Type	Applications
<b>Small-molecule semiconductors</b>	Discrete $\pi$ -conjugated molecules with well-defined structures	Pentacene, Anthracene	p-type	Organic memories, Organic FETs (OFETs)
<b>Polymeric semiconductors</b>	Conjugated polymers forming long-chain backbones	Poly(3-hexylthiophene) (P3HT), Polythiophene	p-type / Ambipolar	Flexible memory films, printable devices
<b>n-type organic semiconductors</b>	Materials optimized for electron conduction	Perfluoropentacene, NDI derivatives	n-type	Electron transport layers in memory cells
<b>Hybrid / 2D organic semiconductors</b>	Organic–inorganic composites or layered crystalline hybrids	MOFs, Perovskite–organic hybrids	Mixed (ambipolar)	Hybrid memories, neuromorphic storage

## Organic molecules memory p-type semiconductor – ex., Pentacene

Or

### Construction, working and advantages of pentacene semiconductor chip.

- Pentacene is a stable and inexpensive organic molecule, widely used in memory devices.
- It consists of five linearly fused benzene rings, forming a planar, conjugated  $\pi$ -system.



- Pentacene is a p-type semiconductor, meaning it has a deficiency of electrons and primarily conducts via holes.
- HOMO (Highest Occupied Molecular Orbital): The topmost filled energy level; electrons are removed from here to generate holes.
- LUMO (Lowest Unoccupied Molecular Orbital): The lowest empty energy level; electrons can be injected here during operation.
- It is typically sandwiched between two electrodes. One electrode is made of a material that donates electrons (an n-type material), and the other electrode is made of a material that accepts electrons (p-type material).
- The pentacene molecule acts as the active layer in the memory device. When a voltage is applied across the two electrodes, electrons flow from the n-type electrode into the pentacene layer, where they are accepted by the pentacene molecules.
- The flow of electrons causes a change in the electrical properties of the pentacene layer, such as a change in its resistance or capacitance. High resistance = '0' (OFF) and Low resistance = '1' (ON). This change can be used to represent a bit of information, such as a 0 or 1.
- The state of the memory device can be read by applying a smaller voltage across the two

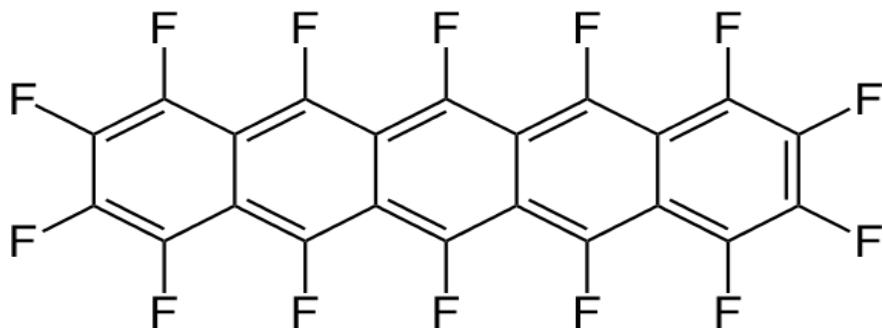
electrodes and measuring the resulting current. The current will be higher or lower depending on the state of the pentacene layer.

### **Advantages:**

- High hole mobility → allows fast charge transport.
- It can be made cheaply using low-temperature methods.
- Flexible and lightweight → good for bendable electronics.
- Chemically stable → lasts longer in devices.
- Has extended  $\pi$ -conjugation → improves conductivity.
- Works well in organic memory devices and OFETs.
- HOMO-LUMO levels can be tuned for better performance.
- More environmentally friendly than some inorganic semiconductors.

### **The n-type organic semiconducting material Perfluoropentacene semiconductor chip.:**

perfluoropentacene is a relatively stable and highly electron-deficient molecule, it can be a good choice for use as an n-type semiconductor in memory devices.



- Perfluoropentacene is an organic molecule that consists of five linearly fused benzene rings, like pentacene. However, in perfluoropentacene, all the hydrogen atoms have been replaced with fluorine atoms. This makes perfluoropentacene an electron-deficient molecule, which can donate electrons.
- To use perfluoropentacene as a memory material, it is typically sandwiched between two electrodes. One electrode is made of a material that donates electrons (n-type material), and the other electrode is made of a material that accepts electrons (an p-type material)
- When a voltage is applied across the two electrodes, electrons flow from the n-type electrode into the perfluoropentacene layer, where they are accepted by the perfluoropentacene molecules. This flow of electrons causes a change in the electrical properties of the perfluoropentacene layer, such as a change in its resistance or capacitance.

- This change in the electrical properties can be used to represent a bit of information, such as a 0 or 1. For example, if the perfluoropentacene layer has a high resistance, it could represent a 0, while a low resistance could represent a 1.
- The state of the memory device can be read by applying a smaller voltage across the two electrodes and measuring the resulting current. The current will be higher or lower depending on the state of the perfluoropentacene layer.

### Advantages:

- n-Type Semiconductor → Efficiently conducts electrons, complementing p-type materials like pentacene.
- High Electron Mobility → Enables fast and efficient charged transport.
- Chemical & Thermal Stability → Resistant to oxidation and heat, improving device lifetime.
- Good Energy Level Alignment → LUMO level is low, making electron injections easier in devices.
- Compatible with Organic Electronics → Can be used in OFETs, organic solar cells, and memory devices.
- Solution Processable & Printable → Allows low-cost, flexible electronics fabrication.
- Environmentally Friendly → Organic, reduces reliance on metals.

### Differences between organic and inorganic memory devices

Feature / Property	Organic Memory Devices	Inorganic Memory Devices
Active Material	$\pi$ -conjugated organic molecules or polymers (e.g., pentacene, polyimide, PFP)	Silicon-based or metal oxides (e.g., $\text{SiO}_2$ , $\text{TiO}_2$ , $\text{Ge}_2\text{Sb}_2\text{Te}_5$ )
Device Fabrication	Solution processing, spin-coating, printing; low-temperature, flexible substrates	High-temperature vacuum deposition, photolithography on rigid silicon wafers
Substrate Type	Flexible (plastic, paper, etc.), low-cost	Rigid (silicon, glass), higher cost
Charge Carrier	Holes (p-type), electrons (n-type), or ambipolar	Electrons and holes (band conduction)
Switching Mechanism	Charge trapping, molecular conformational change, field-induced states	Ion migration, phase change, filament formation

Feature / Property	Organic Memory Devices	Inorganic Memory Devices
<b>Cost and Scalability</b>	Low-cost, scalable, compatible with large-area production	Relatively high-cost, limited scalability to flexible or large-area systems
<b>Flexibility</b>	Highly flexible, suitable for wearable, curved, and lightweight devices	Generally rigid, less suited for flexible applications
<b>Operating Voltage</b>	Generally low-voltage operation possible	May require higher voltages, especially for phase-change memories
<b>Data Retention / Endurance</b>	Moderate; still under research, can be lower than inorganic in some cases	Mature technology, high retention and endurance
<b>Environmental Stability</b>	Moderate; sensitive to moisture and oxygen	High; stable under harsh conditions
<b>Commercial Maturity</b>	Emerging; mostly research and pilot applications	Widely commercialized; standard in microelectronics
<b>Eco-Friendliness</b>	Organic, less toxic, more environmentally friendly	Metal-based; can produce toxic byproducts
<b>Tunability</b>	Molecular properties can be modified to adjust electronic behavior	Mostly fixed properties, limited tunability

## Resistive RAM (ReRAM) Materials: Introduction, synthesis of TiO<sub>2</sub>-RAM nanomaterial by sol-gel method, properties and applications.

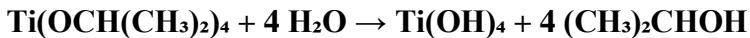
### Introduction:

TiO<sub>2</sub> (titanium dioxide) is widely used as the resistive switching layer in ReRAM devices due to its excellent electrical insulating properties, chemical stability, and compatibility with CMOS processes. TiO<sub>2</sub> exhibits reliable bipolar and unipolar resistive switching behavior, driven mainly by the formation and rupture of conductive filaments composed of oxygen vacancies or titanium interstitials. This makes TiO<sub>2</sub> an ideal candidate for low-power, high-speed, and scalable memory application.

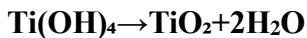
### Synthesis of TiO<sub>2</sub>-RAM nanomaterial

- ⊕ Preparation of Precursors: Dissolve titanium isopropoxide (TTIP) in anhydrous isopropanol or ethanol with stirring. Use a typical molar concentration of 0.1 M TTIP.
- ⊕ Hydrolysis and Condensation: Slowly add deionized water to the TTIP solution under vigorous stirring. Maintain water to TTIP molar ratio between 1:1 and 5:1. Add a few

drops of acid catalyst (e.g., acetic acid) to adjust pH and stabilize the sol. Stir for 1-2 hours at room temperature for hydrolysis and polycondensation.

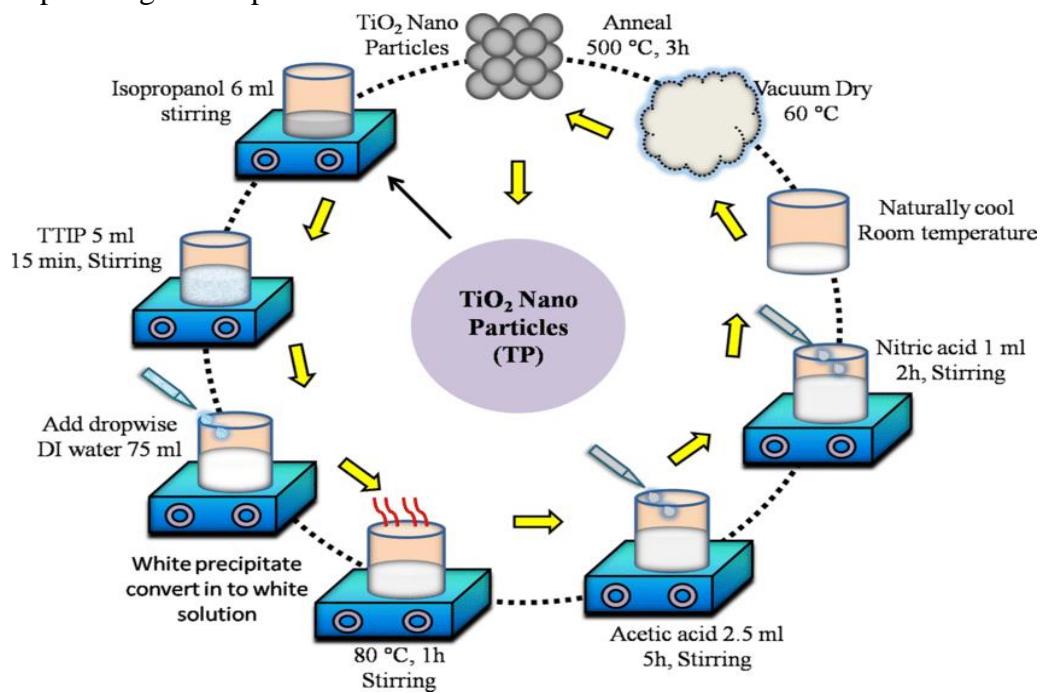


Here, titanium isopropoxide reacts with water forming titanium hydroxide and isopropanol.



The condensation of hydroxyl groups produces a three-dimensional Ti–O–Ti network with elimination of water, leading to formation of amorphous  $\text{TiO}_2$

- Aging: Allow the sol to age for 12-24 hours at room temperature for improved stability.
- Substrate Preparation: Clean substrate by sonication in acetone, isopropanol, and deionized water. Dry the substrate under nitrogen stream or in an oven.
- Thin Film Deposition: Spin-coat the  $\text{TiO}_2$  sol on the substrate at 2000–4000 rpm for 30–60 seconds. Perform multiple coatings, if necessary, with drying between layers.
- Drying: Dry the coated film at 80–100 °C for 10-15 minutes.
- Annealing: Calcine the dried film in air at 400–500 °C for 1–2 hours to crystallize  $\text{TiO}_2$  to anatase phase and densify.
- Device Fabrication: Deposit the top electrode (e.g., Au, Pt, or Ag) by evaporation or sputtering to complete the ReRAM device.



## **Properties of TiO<sub>2</sub> for ReRAM**

- Electrical: Shows resistive switching as oxygen vacancies move, creating or breaking conductive paths.
- Structural: Nanocrystalline anatase TiO<sub>2</sub> has many defects that improve switching and reliability.
- Dielectric: Wide bandgap (~3.2 eV) and high dielectric constant allow stable charge storage.
- Scalability: Easily integrates with CMOS, suitable for nanoscale devices.
- Endurance & Retention: Can last over 10<sup>6</sup> cycles and store data for more than 10 years.

## **Applications of TiO<sub>2</sub>-based ReRAM**

- Non-volatile memory: Faster and more energy-efficient than Flash memory.
- Neuromorphic computing: Works like artificial synapses for brain-inspired systems.
- Logic-in-memory: Combines storage and processing, cutting delay and power use.
- Flexible & transparent devices: Sol-gel TiO<sub>2</sub> films enable bendable, transparent electronics.
- Next-gen storage: Supports multiple resistance states for higher storage density.

## **Display system**

**Introduction:** Display systems are technologies that convert electrical signals into visual information, enabling interaction between humans and machines. They are essential in applications ranging from televisions, computers, smartphones, and calculators to advanced scientific and medical instruments. Over the years, display systems have evolved from bulky cathode ray tubes (CRTs) to flat-panel technologies such as liquid crystal displays (LCDs), light-emitting diode (LED) displays, plasma panels, and organic light-emitting diode (OLED) displays.

## **Liquid crystals (LCs)-classification, properties and its applications**

### **Classification of Liquid Crystals**

Liquid crystals are broadly classified into:

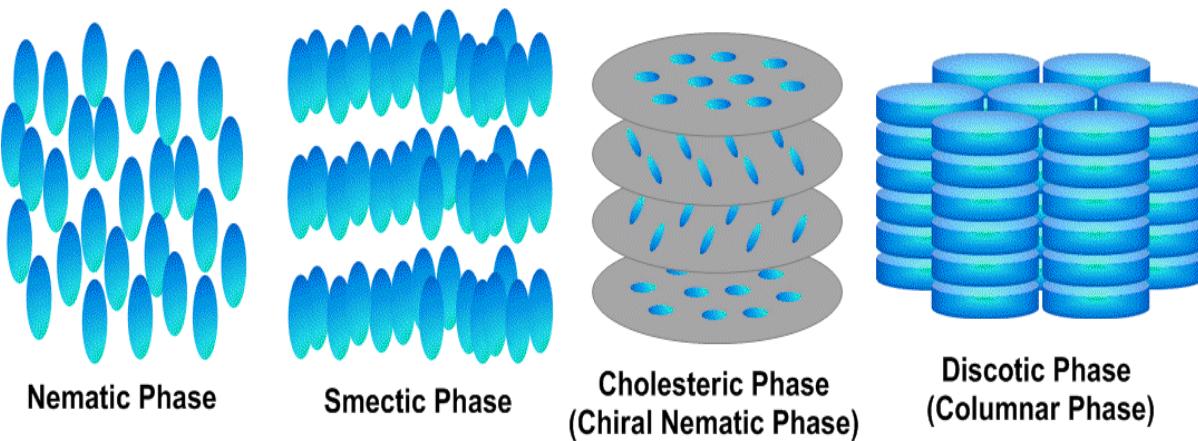
#### **1) Thermotropic Liquid Crystals (TLCs)**

- Show liquid crystal behavior with variation in temperature.
- Examples:
  - Cholesteryl Benzoate (145.5 °C & 178.5 °C)
  - p-Azoxy Anisole (118 °C & 135 °C)

## 2) Lyotropic Liquid Crystals (LLCs)

- Form liquid crystal phases by variation in concentration of a compound dissolved in a solvent.
- Examples: Soap–water mixture, Phospholipid–water mixture

### Types of Thermotropic LCs



#### 1. Nematic Liquid Crystals (NLCs):

- Optically inactive molecules, elongated in shape.
- Molecules align parallel to a common axis called the *director*.
- **Examples:** p-Azoxy Anisole (PAA), p-Azoxy Phenetole.

#### 2. Chiral/Cholesteric Liquid Crystals (CLCs):

- Formed by optically active molecules with a chiral center.
- Molecules acquire a spontaneous helical twist (right- or left-handed).
- **Examples:** Cholesteryl Benzoate, Cholesteryl Formate.

#### 3. Smectic Liquid Crystals (SLCs):

- Exhibit orientational and some positional order.
- Sub-types:
  - *Smectic A:* Director perpendicular to layer plane; least ordered.
  - *Smectic B:* Molecules arranged in hexagonal order.
  - *Smectic C:* Director tilted at an angle other than 90°.

#### 4. Columnar/Discotic Liquid Crystals (DLCs):

- Formed by disc-like molecules.
- Molecules orient along the director and assemble into columns.
- Columns further arrange in a hexagonal lattice.

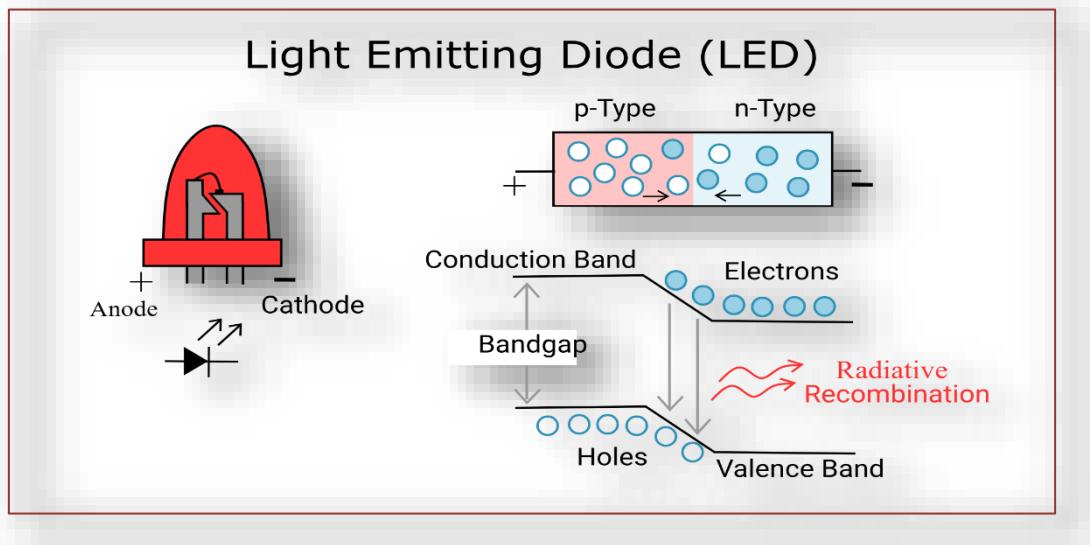
## Properties of Liquid Crystals

- Flow like liquids due to loss of positional order.
- Molecules are elongated and rigid.
- Exhibit partial orientational order.
- Show anisotropy in optical and electrical properties.
- Transitions between phases (crystalline → LC → liquid) occur with temperature changes.

## Applications of Liquid Crystals

1. **Electronic displays** — watches, calculators, mobile phones, laptops, TVs, digital signage.
2. **Medical & instrumentation** — blood pressure monitors, digital thermometers, colorimeters.
3. **Analytical devices** — potentiometers, conductometers.
4. **Indicators** — TV channel indicators, thermal sensors.

## Construction, working principle and applications of Light Emitting Diodes (LEDs)



## Construction of LED

- It consists of P–N Junction which is Core part made of direct band-gap semiconductors (e.g., GaAs, GaP, GaN) that emit light during recombination. The bandgap determines the color of emitted light. This is the area where the p- and n-type materials meet. When forward-biased, electrons from the n-side and holes from the p-side recombine here, releasing energy as photons (light).
- Anode & Cathode:
  - Anode (+): Connected to p-type material (longer lead).
  - Cathode (-): Connected to n-type material (shorter lead).

- Reflector Cup: A metallic cup that directs and enhances light emission.
- Encapsulation (Epoxy Dome): Transparent covering that protects the diode, focuses light, and reduces losses.

### **Working:**

When the LED is forward biased: Electrons from the N-side cross the junction and recombine with holes in the P-side. During recombination, the energy released is in the form of photons (light) instead of heat. The color (wavelength) of light depends on the bandgap energy ( $E_g$ ) of the semiconductor material used: GaAs → Infrared, GaAsP → Red/Orange/Yellow, GaP → Green, InGaN → Blue/White This process of light generation in semiconductors is called electroluminescence.

### **Applications of LED**

- **Indicator Lamps:** Power ON/OFF indicators in electronic devices.
- **Display Systems:** Digital watches, calculators, seven-segment displays, advertising boards.
- **Lighting:** LED bulbs, tube lights, streetlights, automotive headlights due to high efficiency and long lifespan.
- **Optical Communication:** Used in remote controls, fiber optic communication (as light sources).
- **Medical Applications:** Surgical lighting, phototherapy, and diagnostic instruments.
- **Consumer Electronics:** TVs, smartphones, laptops (as backlight and display panels).
- **Signaling:** Traffic signals, railway signals, aviation lights.

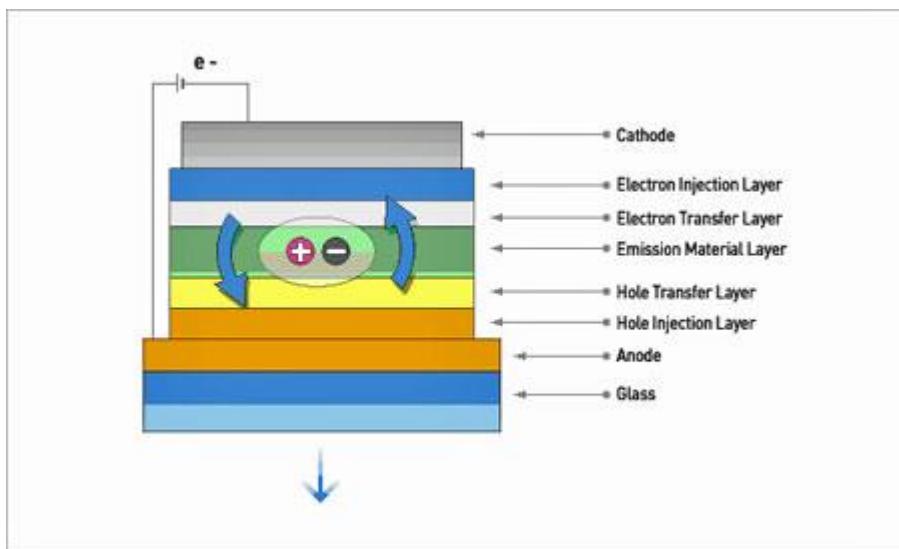
### **Organic Light Emitting Diodes (OLEDs)**

#### **Construction:**

An OLED is a multilayered semiconductor device in which organic materials emit light under electrical excitation. Its basic structure includes:

- **Substrate:** Usually, glass or flexible plastic that provides mechanical support.
- **Anode:** Transparent electrode (commonly Indium Tin Oxide, ITO) that injects holes into the organic layer.
- **Organic Layers:**
  - Hole Transport Layer (HTL): Facilitates hole movement from the anode to the emissive layer.

- Emissive Layer (EML): Made of conjugated organic molecules or polymers; light is generated here when electrons and holes recombine.
- Electron Transport Layer (ETL): Guides electrons from the cathode to the emissive layer efficiently.
- **Cathode:** Metal electrode (e.g., aluminum, calcium) that injects electrons into the device.



### **Working Principle:**

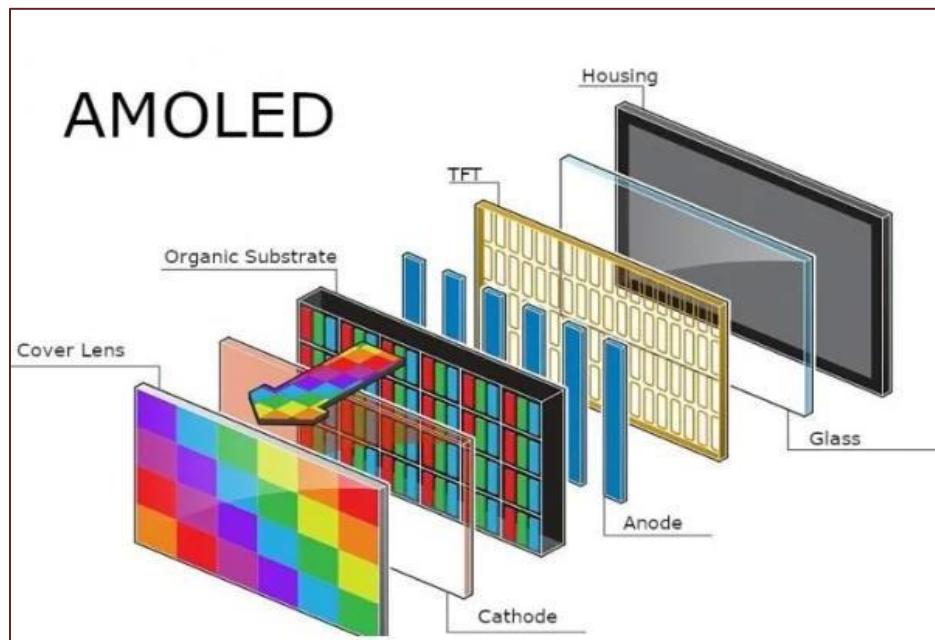
OLEDs operate based on **electroluminescence** of organic materials:

- When a voltage is applied across the electrodes, holes are injected from the anode and electrons from the cathode.
- These charge carriers move through the HTL and ETL into the emissive layer.
- Electron-hole recombination occurs in the emissive layer, forming excitons (bound electron-hole pairs).
- Excitons release energy in the form of visible light when they decay radiatively.
- The emitted light passes through the transparent anode and substrate to produce a visible display.

### **Applications:**

- **Displays:** Smartphones, tablets, TVs, smartwatches, and flexible displays.
- **Lighting:** Energy-efficient, thin, and flexible lighting panels.
- **Wearable Electronics:** Curved or foldable screens and fashion-integrated lighting.
- **Advanced Applications:** Transparent and rollable displays, automotive dashboards, and signage.

## Assisted Matrix Organic Light Emitting Diodes (AMOLEDs)



### **Construction of AMOLEDs**

- An AMOLED is a type of OLED display that uses an active matrix of thin-film transistors (TFTs) to control each individual pixel. Its construction consists of:
- Substrate: Usually glass or flexible plastic, providing mechanical support.
- Thin-Film Transistor (TFT) Layer: Each pixel is connected to a TFT that acts as a switch, allowing precise control of current.
- Organic Layers:
  - Hole Injection Layer (HIL) – facilitates injection of holes from the anode.
  - Hole Transport Layer (HTL) – transports holes to the emissive layer.
  - Emissive Layer (EML) – contains organic molecules that emit light when electrons and holes recombine.
  - Electron Transport Layer (ETL) – transports electrons from the cathode to the emissive layer.
  - Electron Injection Layer (EIL) – aids electron injection from the cathode.
- Cathode and Anode: Transparent anode (often ITO) and metal cathode.
- Encapsulation Layer: Protects organic materials from moisture and oxygen.

### Working Principle of AMOLEDs

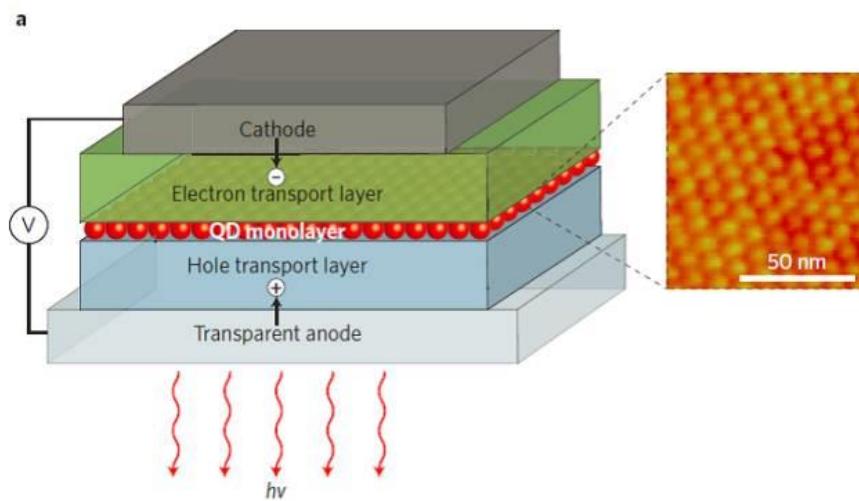
- When voltage is applied across a pixel, the TFT switch turns on, allowing current to flow through the organic layers.
- Electrons from the cathode and holes from the anode meet in the emissive layer.
- Recombination of electrons and holes generates photons, producing light.
- Each pixel emits light independently, enabling high contrast, fast response, and precise color control.

### Applications of AMOLEDs

- **Smartphone and Tablet Displays:** High-resolution, flexible, and energy-efficient screens.
- **Wearables:** Smartwatches and fitness bands with curved or flexible displays.
- **Televisions:** Large, ultra-thin displays with high contrast ratios.
- **Virtual Reality (VR) and Augmented Reality (AR):** Fast response time for immersive experiences.
- **Automotive Displays:** Instrument panels and infotainment systems with wide viewing angles.

### Quantum dot Light Emitting Diodes (QLEDs)

#### Construction:



Quantum Light Emitting Diodes are display devices that utilize **quantum dots (QDs)**—semiconductor nanocrystals with size-dependent optical properties. Their construction generally involves:

- Substrate: Glass or flexible polymer base.

- Backplane (Active Matrix): Typically made of amorphous silicon (a-Si) or indium gallium zinc oxide (IGZO) thin-film transistors (TFTs) for pixel addressing.
- Blue LED Layer: Provides excitation light for the quantum dots.
- Quantum Dot Layer: A thin film of red and green quantum dots dispersed in a polymer matrix; these absorb blue light and re-emit at longer wavelengths (red/green).
- Optical Layers: Include color filters, barrier layers, and encapsulation for stability.

### **Working Principle**

- Quantum dots exhibit quantum confinement, meaning their electronic and optical properties depend on particle size.
- When the blue LED backlight excites the QD layer:
  - Red QDs emit red light.
  - Green QDs emit green light.
  - Unconverted blue light passes directly.
- This produces high-purity RGB colors with tunable emission spectra.
- In some advanced electrically driven QLEDs, quantum dots act as direct electroluminescent materials where injected electrons and holes recombine within QDs to emit light, eliminating the need for a backlight.

### **Applications**

1. **Display Technology:** QLED TVs, monitors, and smartphones with wide color gamut, high brightness, and improved energy efficiency.
2. **Optoelectronic Devices:** Quantum dot lasers, photodetectors, and optical sensors.
3. **Biomedical Imaging:** Fluorescent probes for high-resolution cell and tissue imaging due to narrow emission spectra.
4. **Lighting:** Quantum dot-based LEDs for solid-state lighting with precise color tuning and high luminous efficacy.
5. **Solar Cells:** Integration into quantum dot photovoltaics for enhanced absorption and spectral conversion.