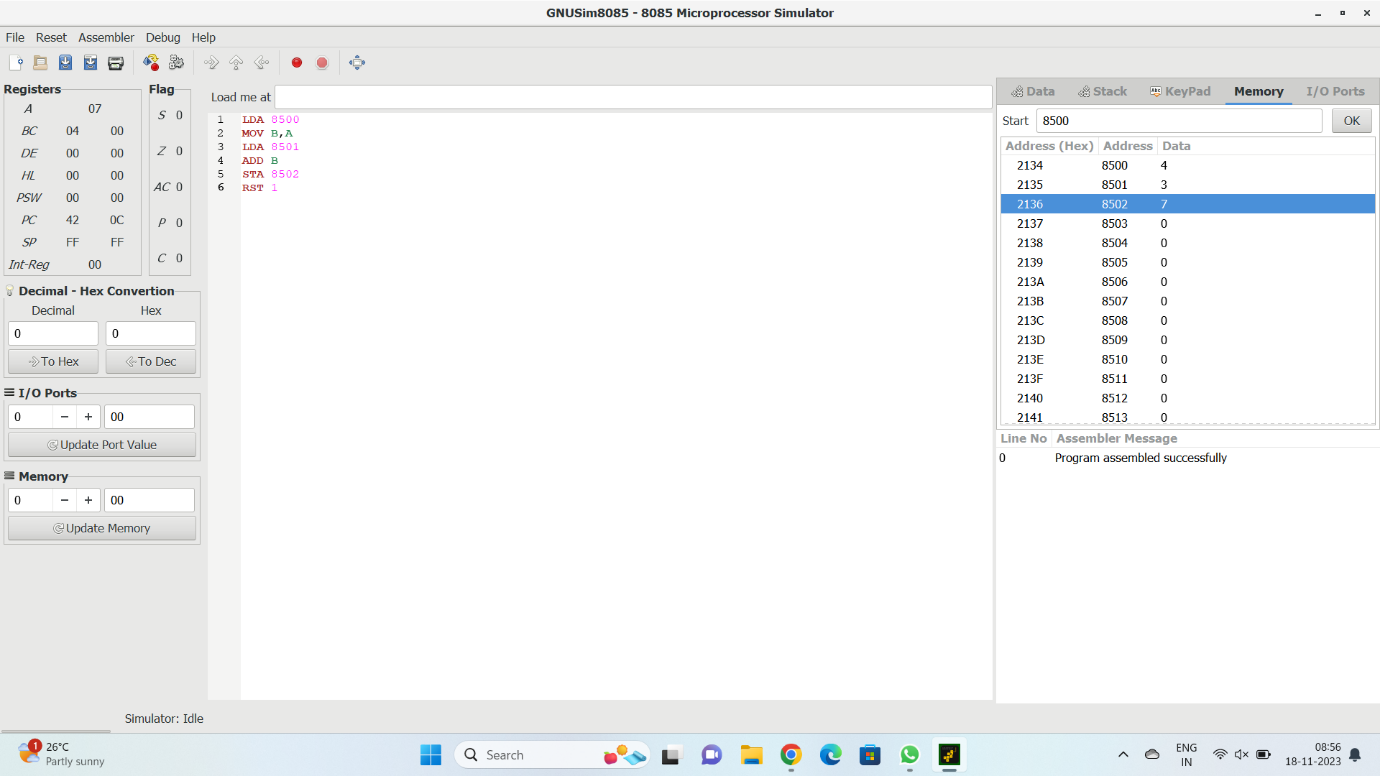
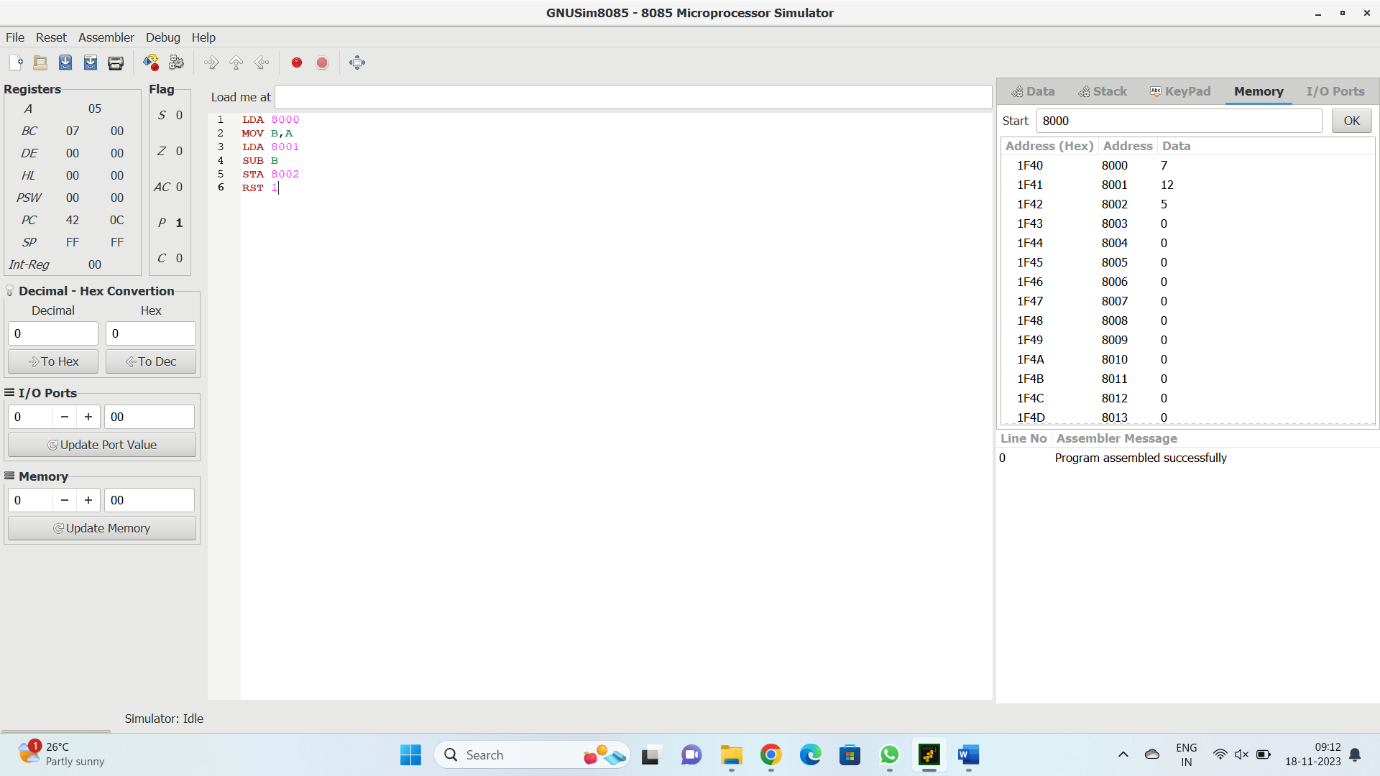
**COMPUTER ARCHITECTURE LAB**

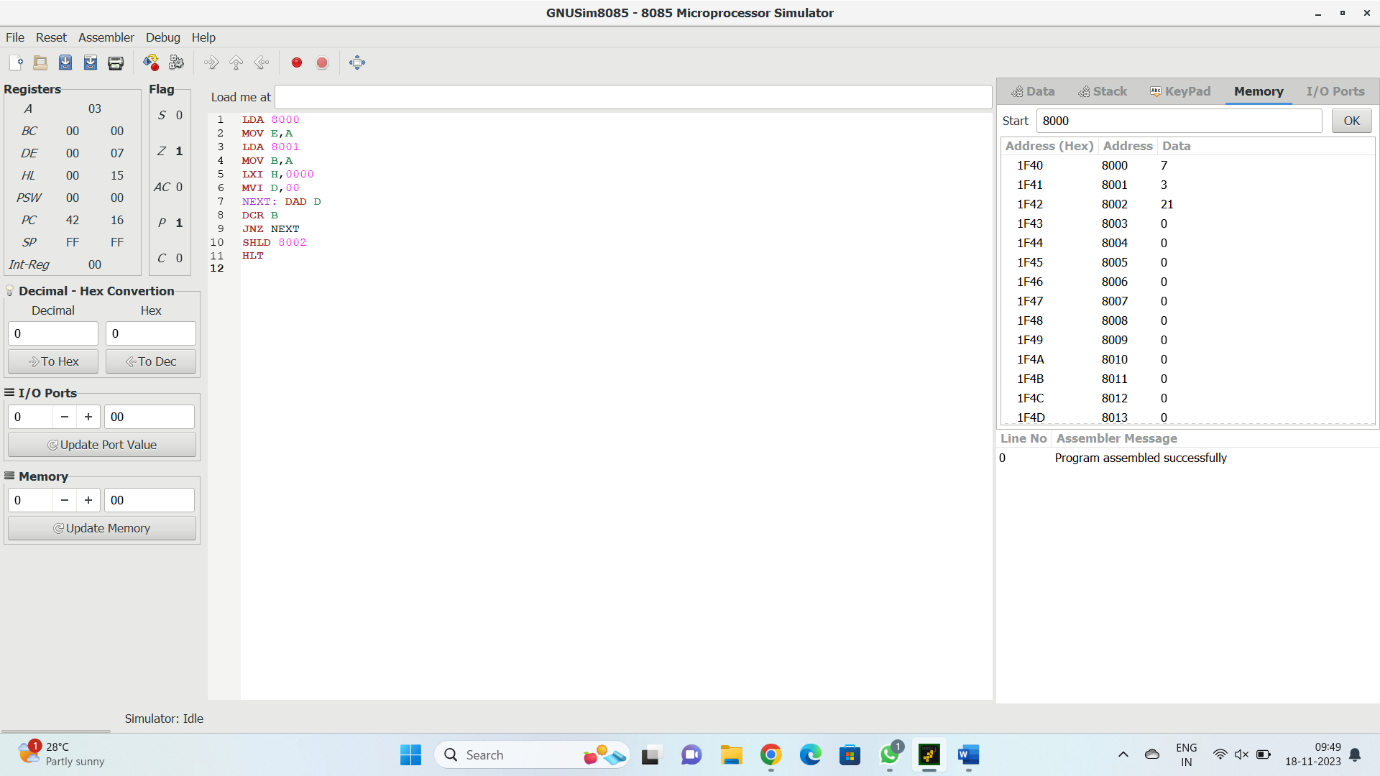
1. **8-bit addition**



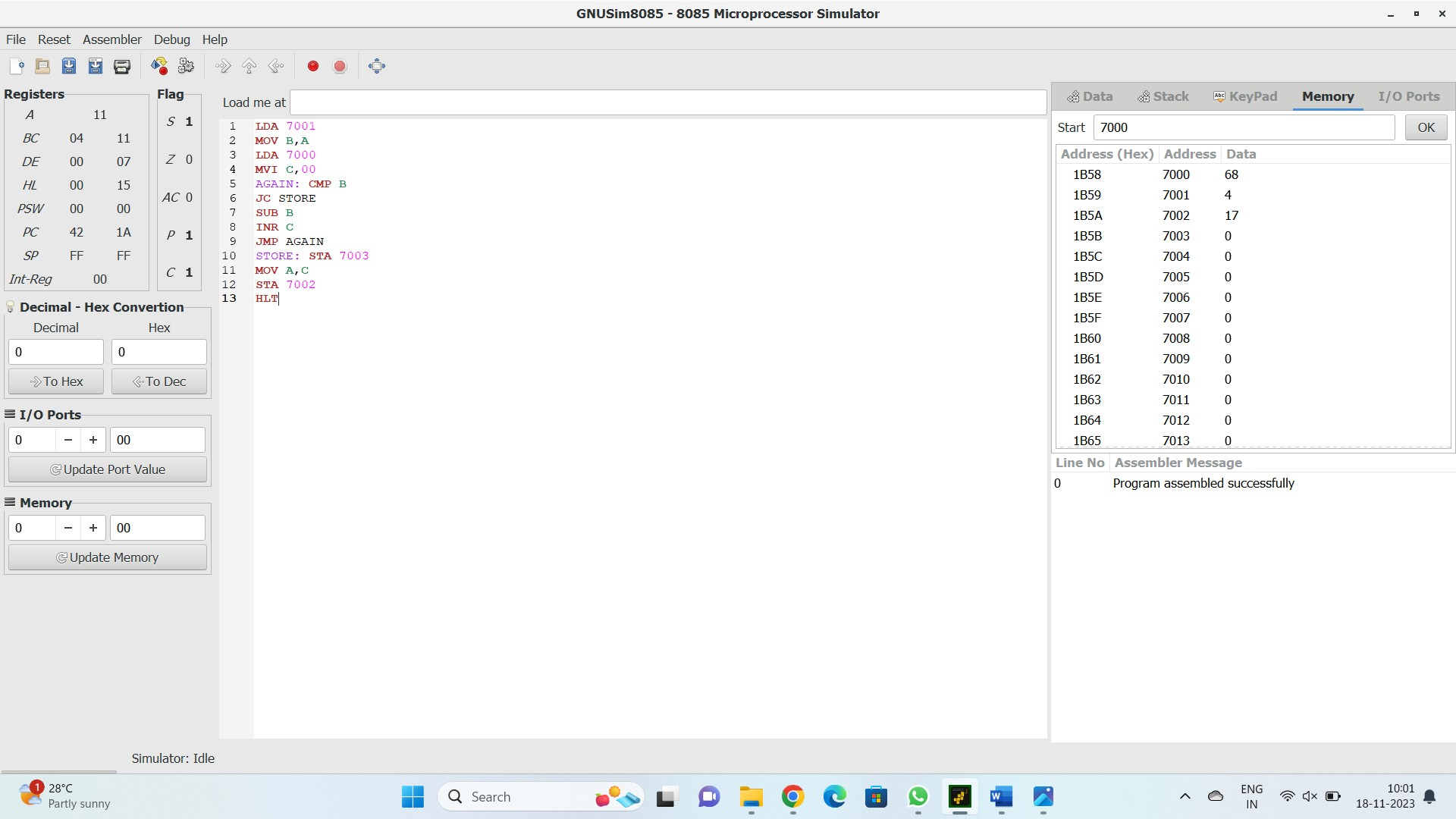
**2. 8-bit subtraction**



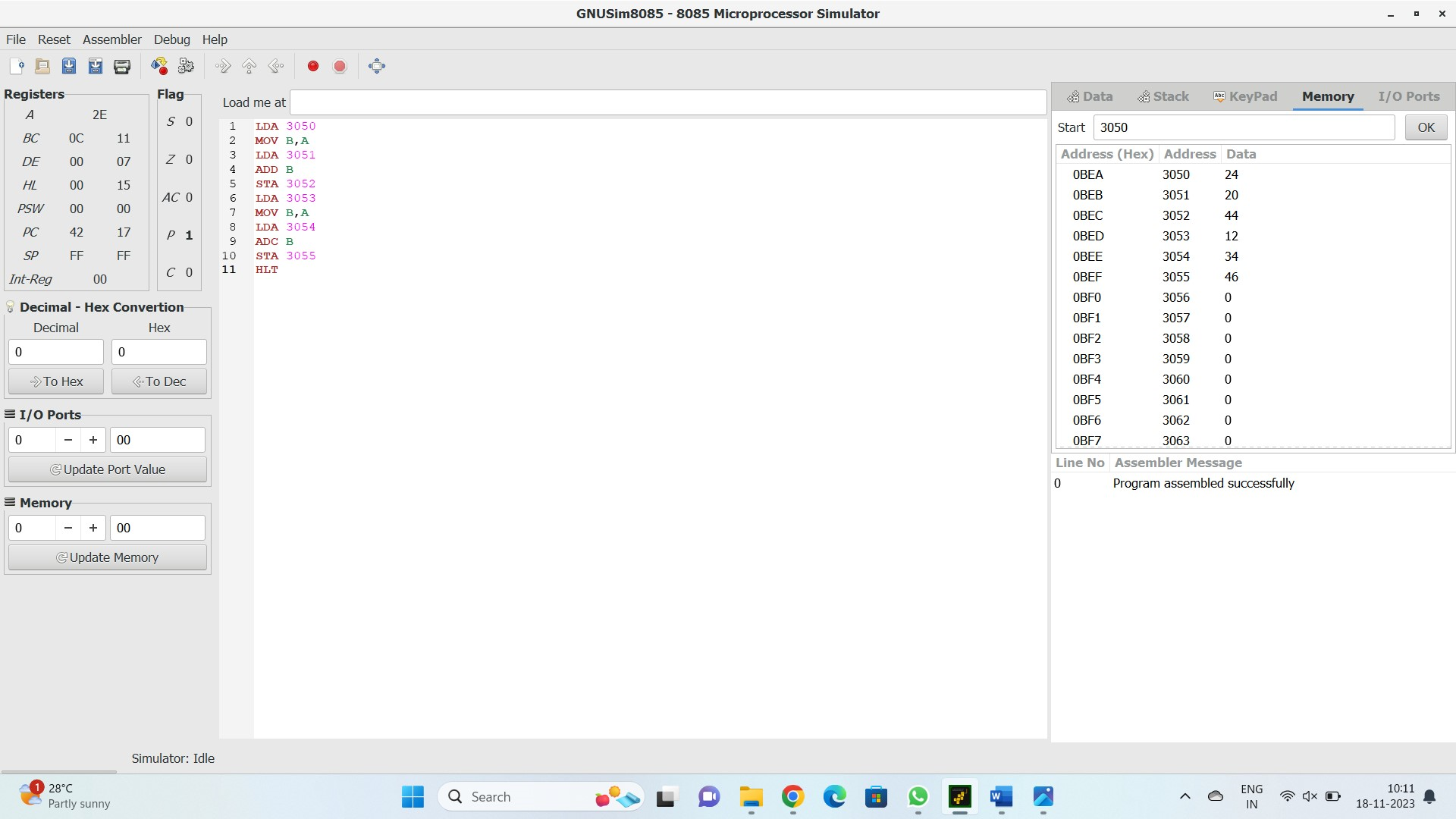
1. **8-bit multiplication**



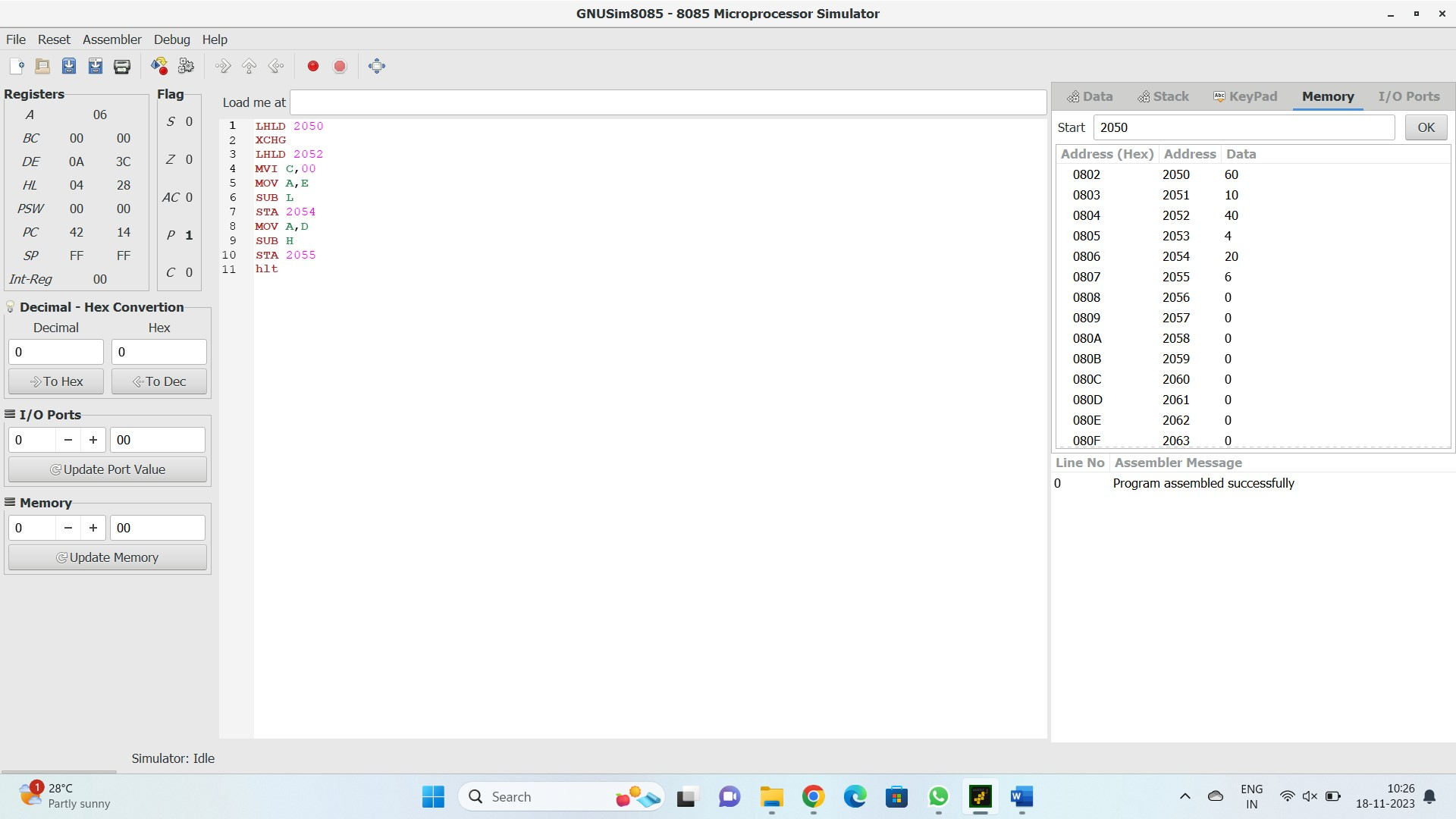
1. **8-bit division**



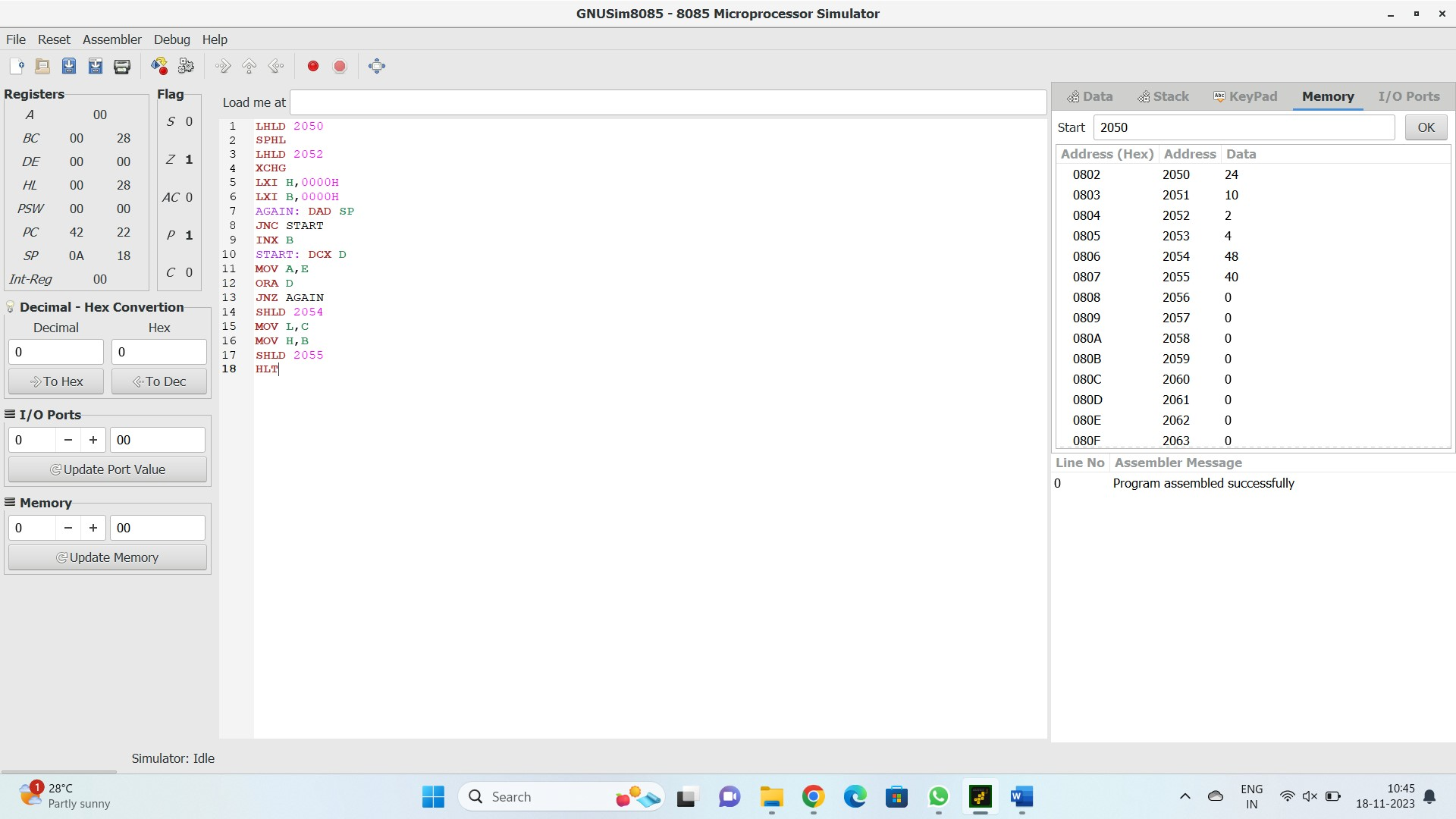
1. **16-bit addition**



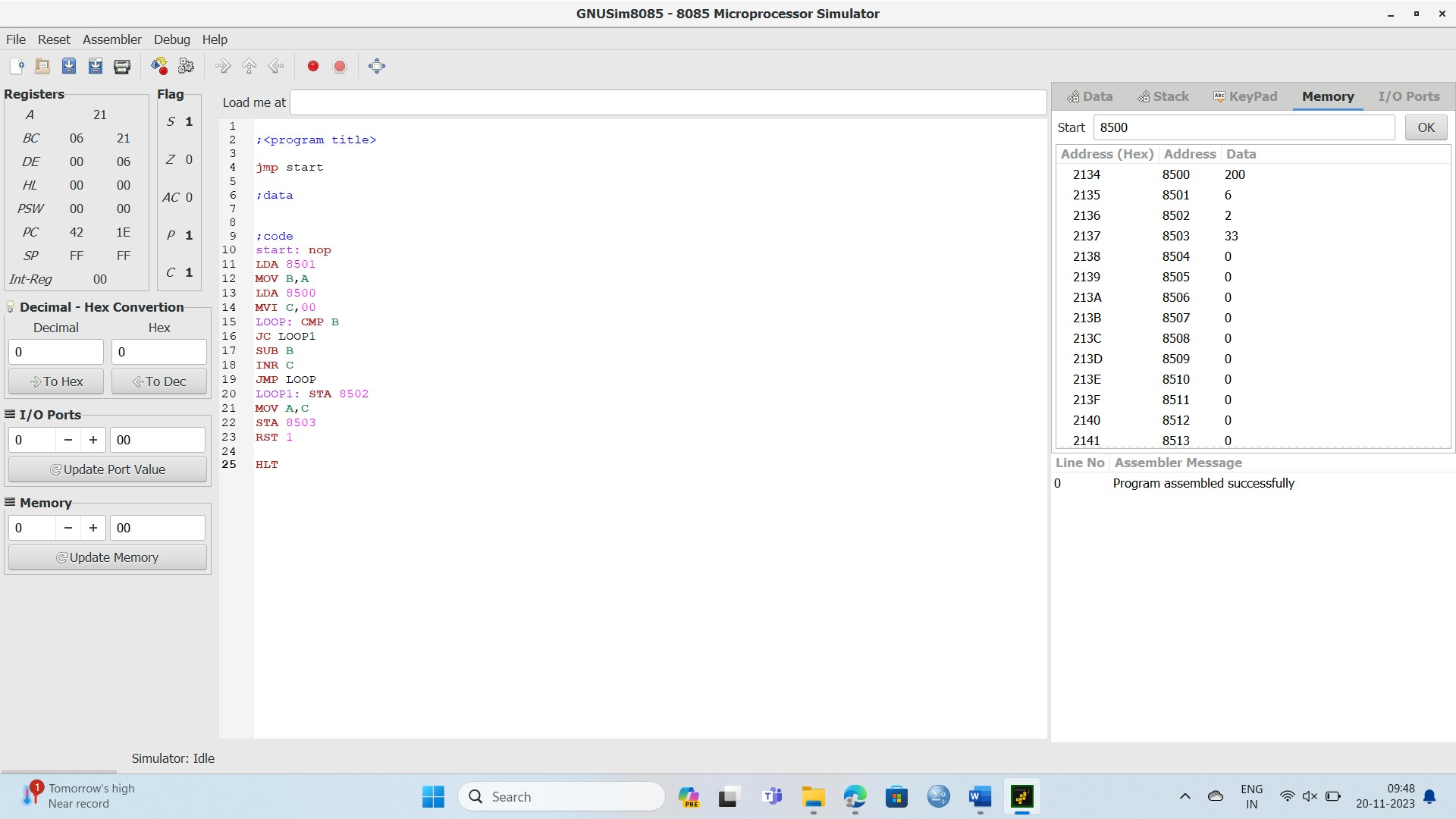
1. **16-bit subtraction**



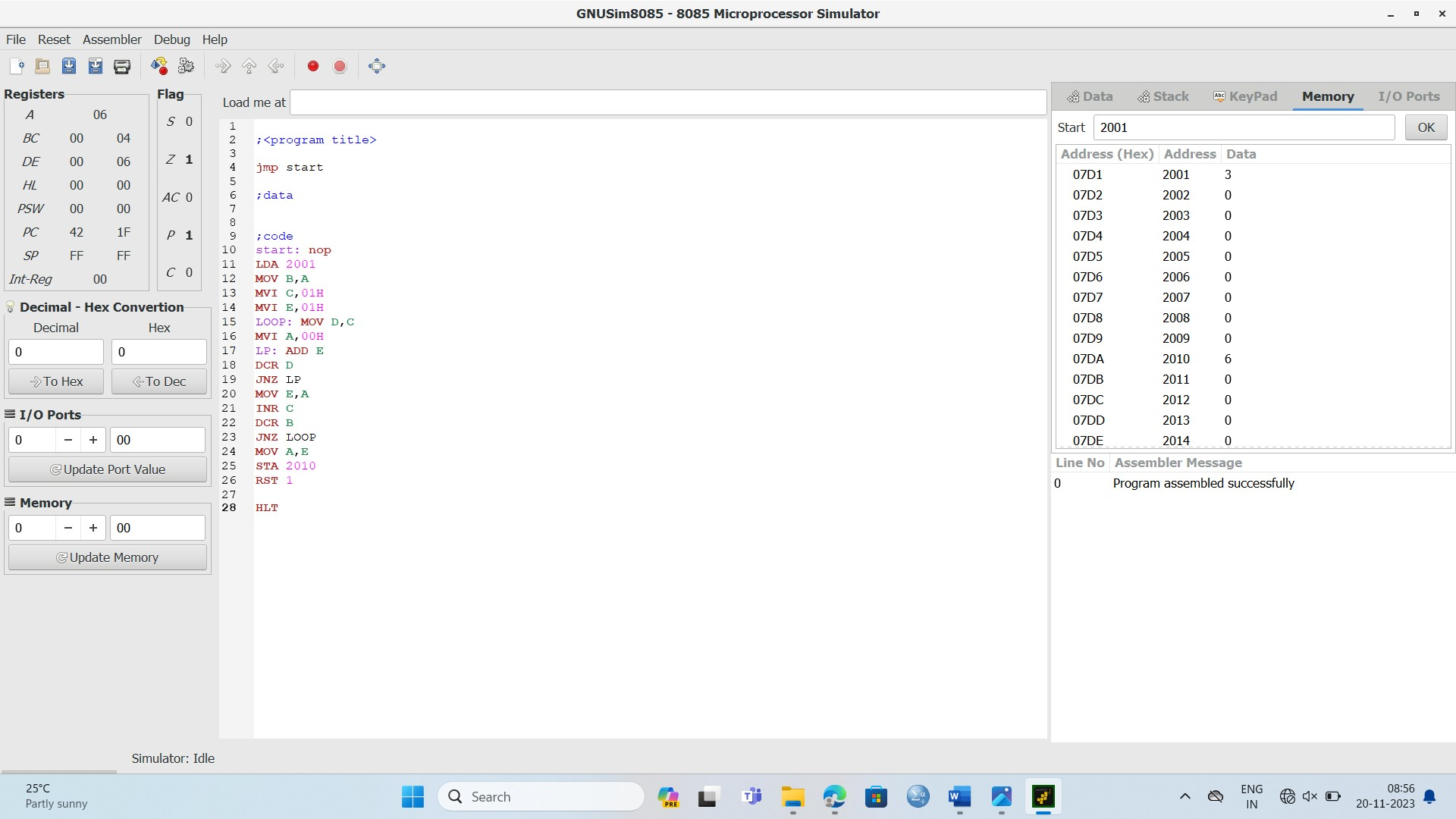
1. **16-bit multiplication**



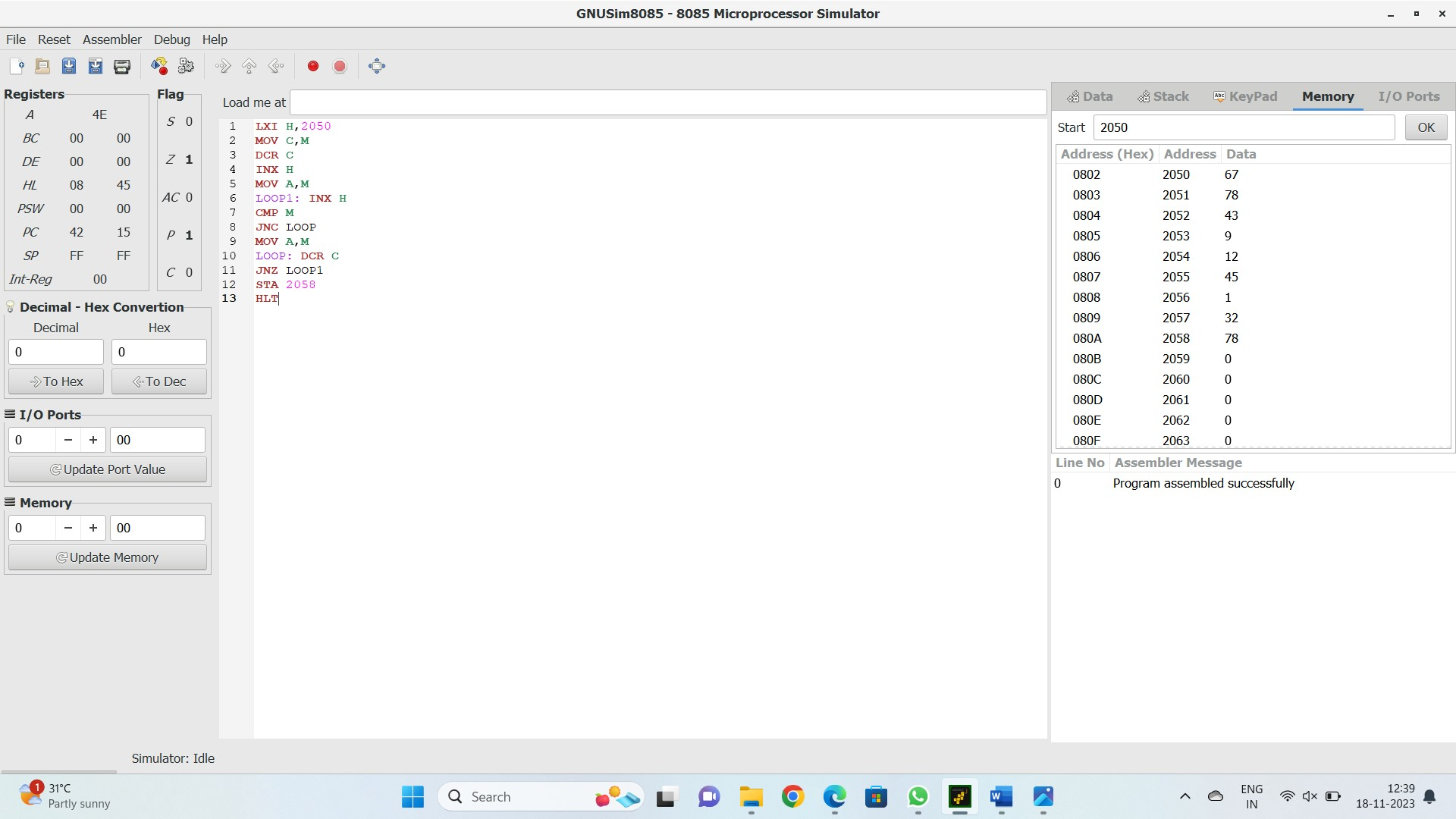
1. **16-bit division**



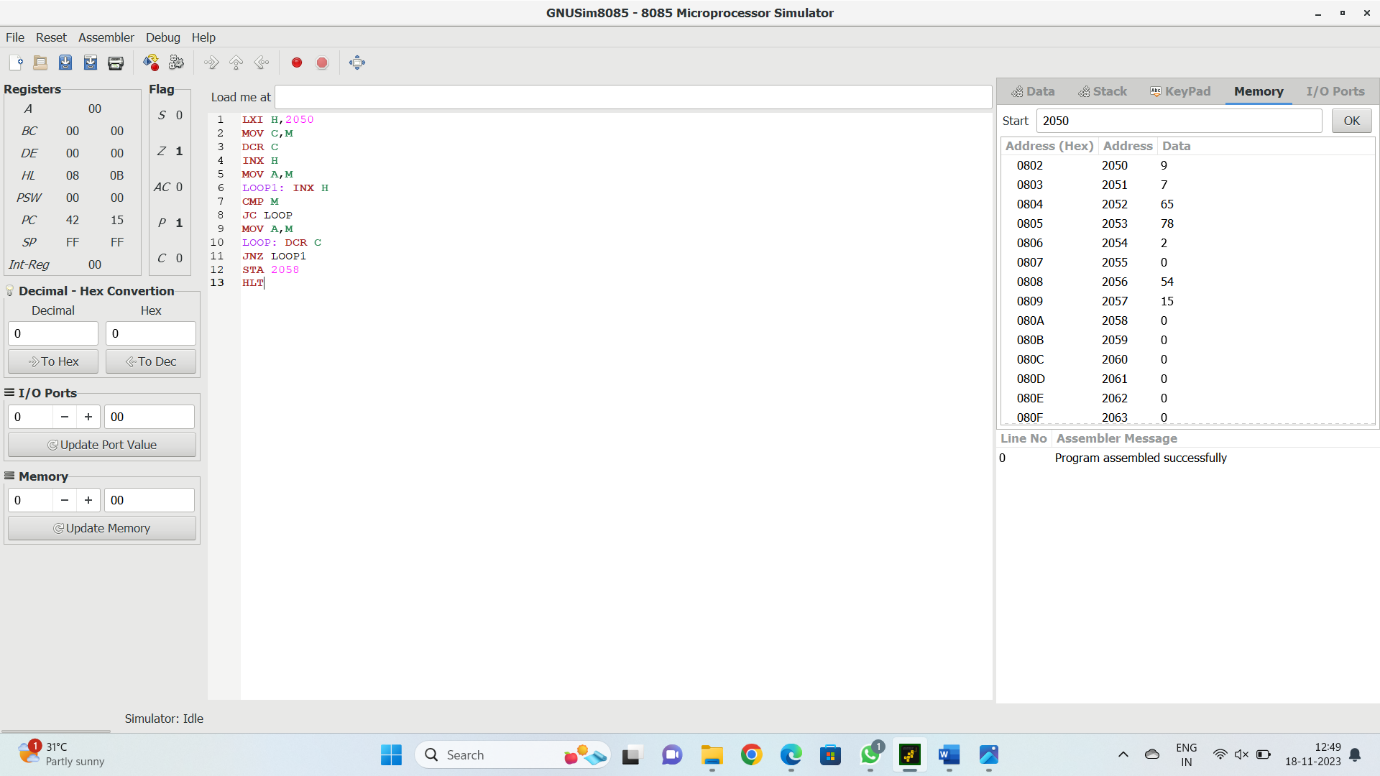
**9.Factorial of the number**



**10.Largest number in the array**



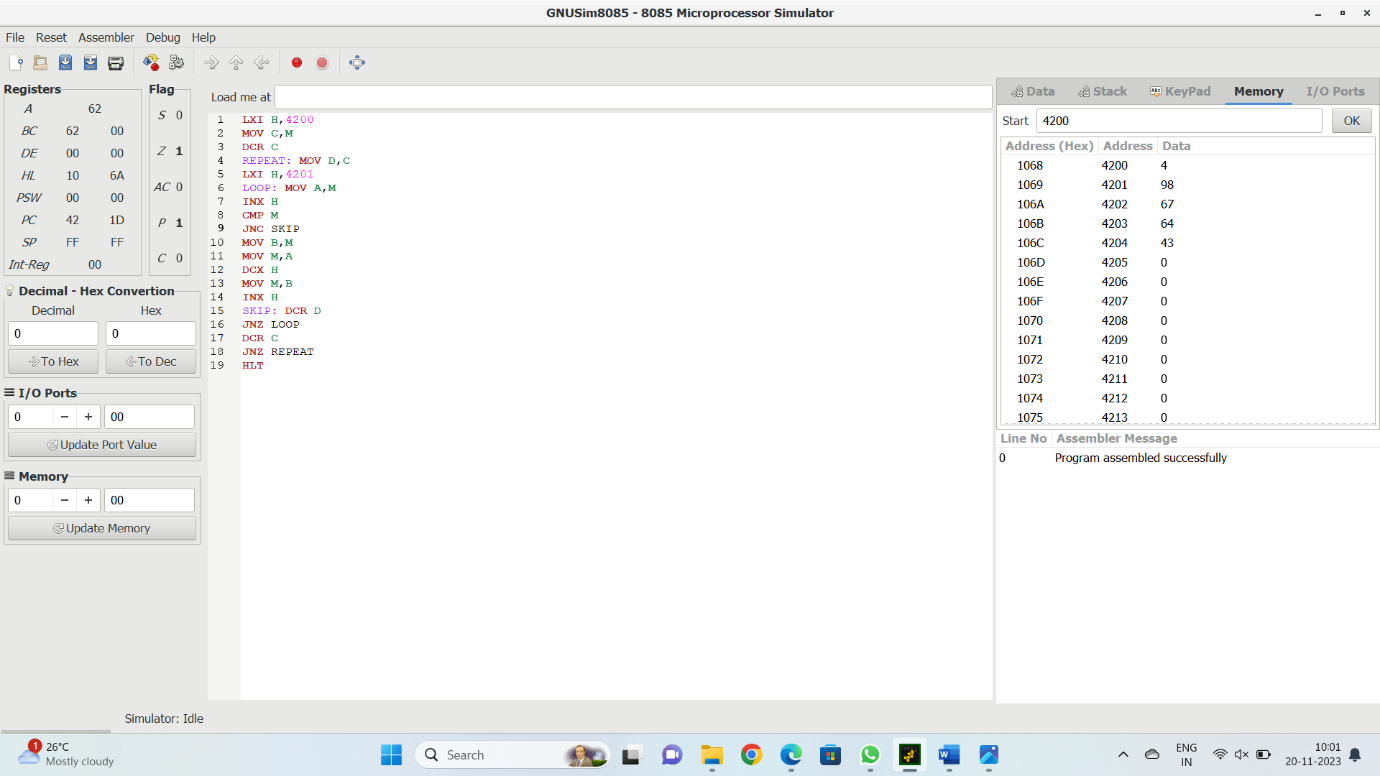
**16.Smallest number in the array**



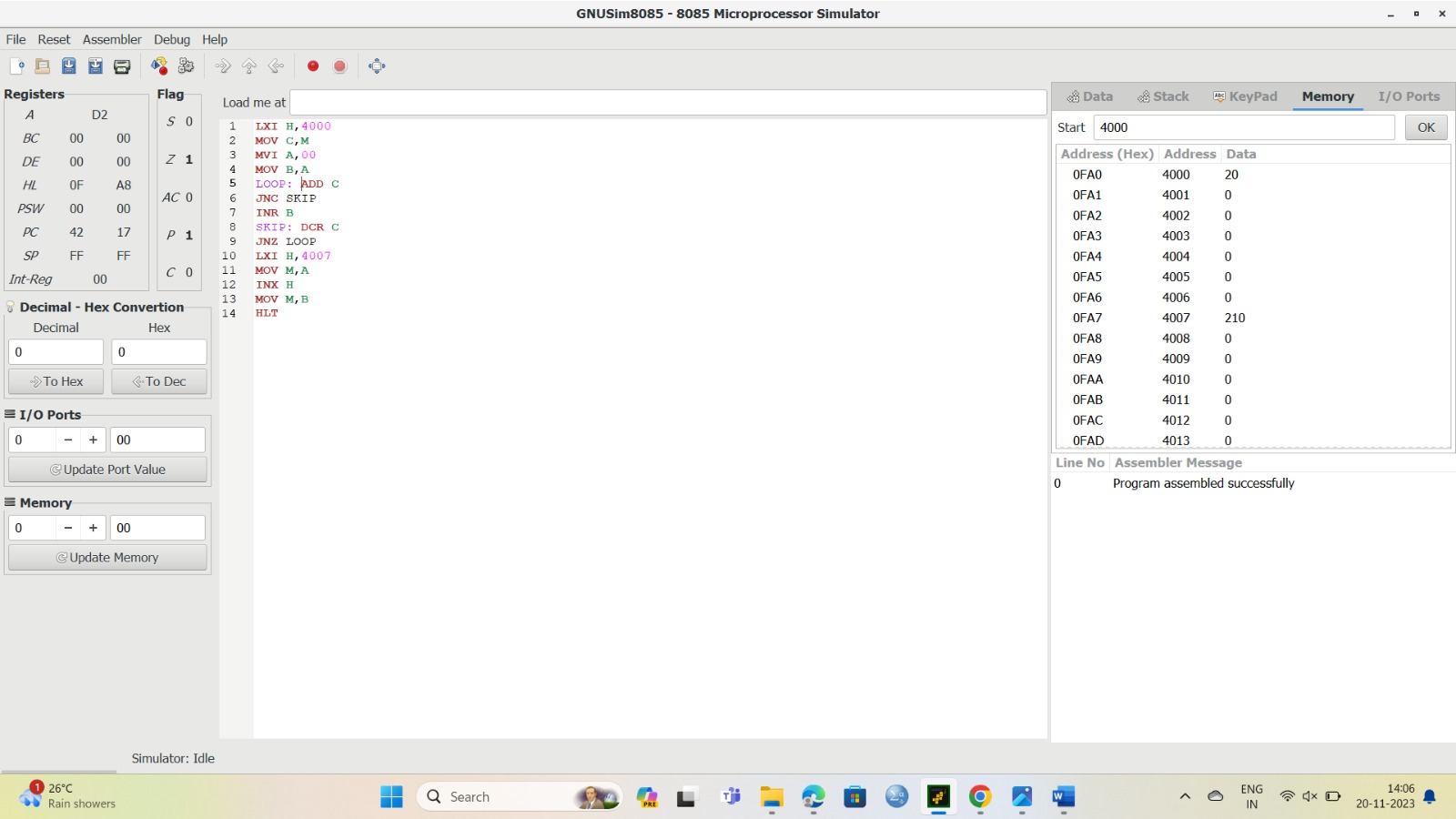
**17.Ascending order**



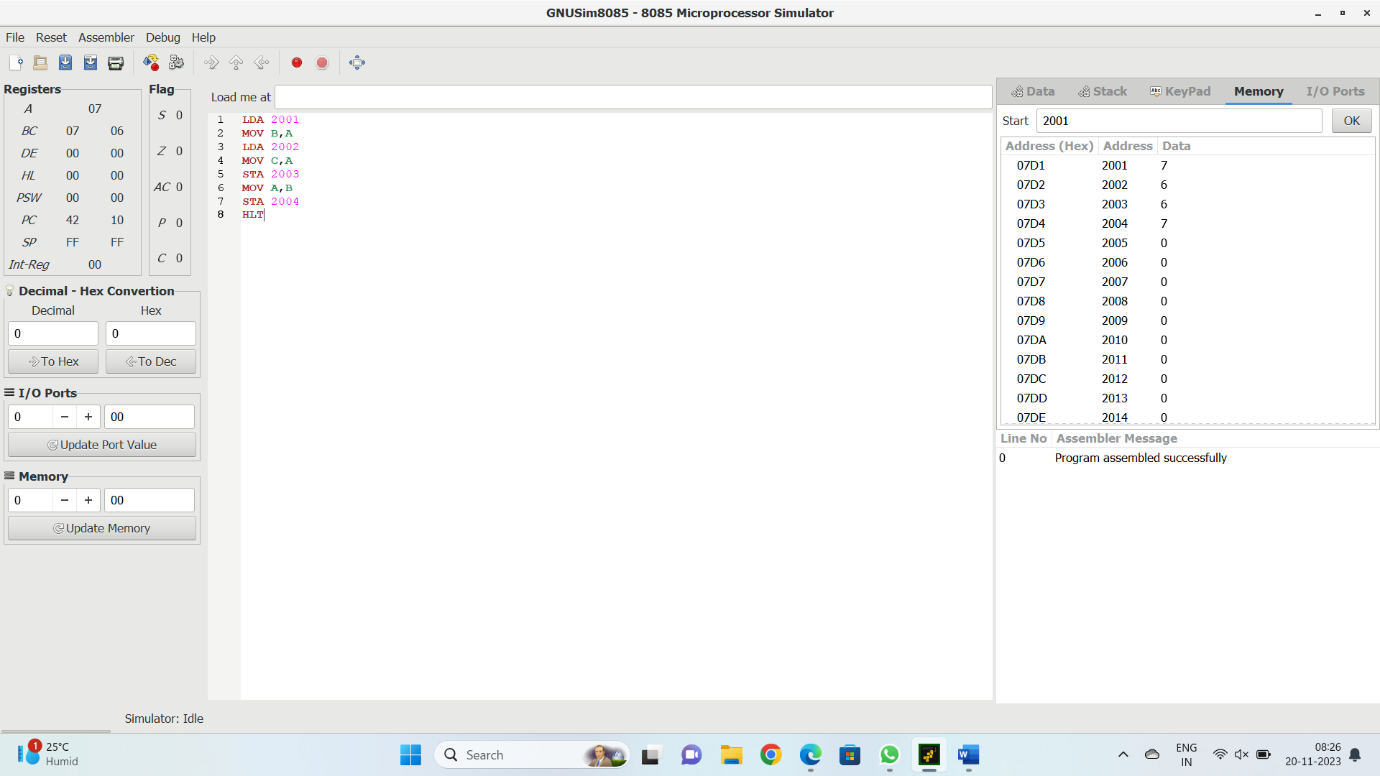
**18.Descending order**



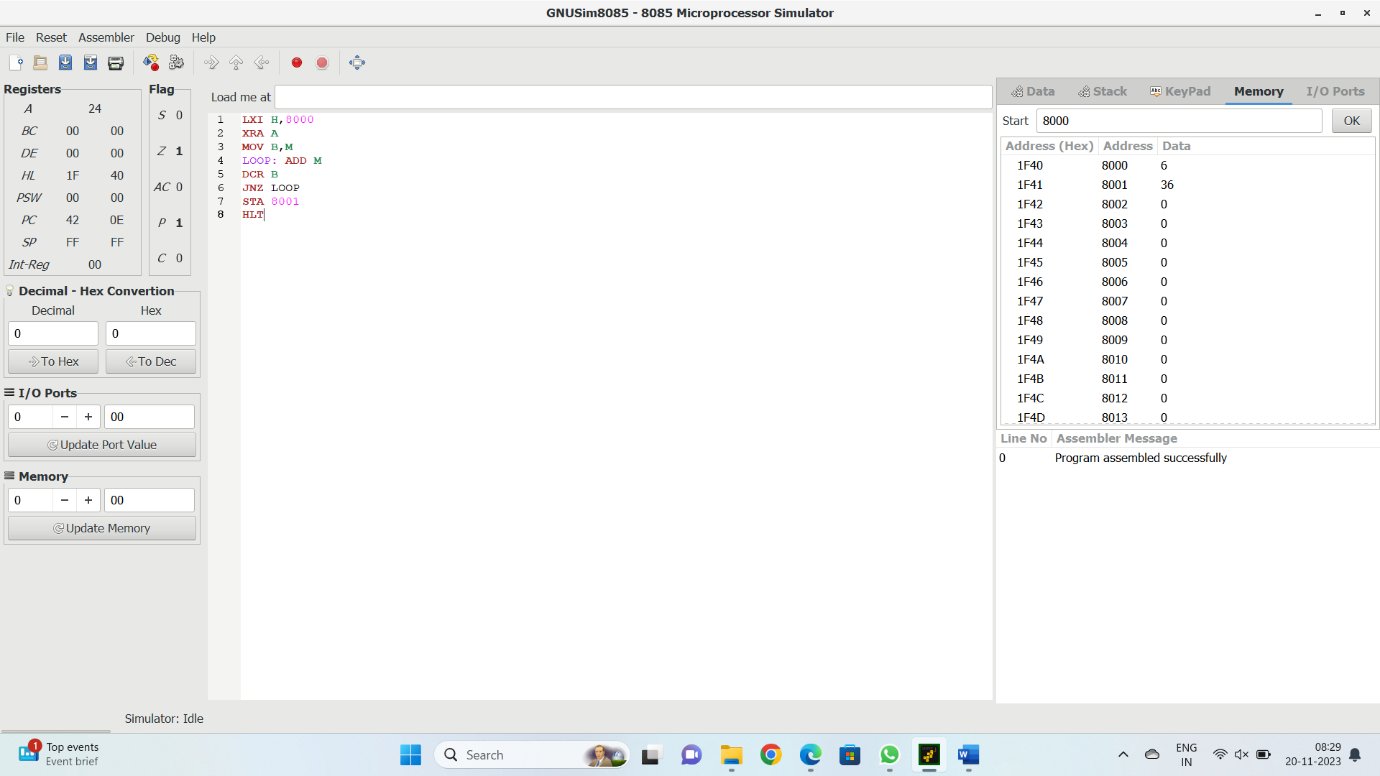
**19.Addition of N numbers**



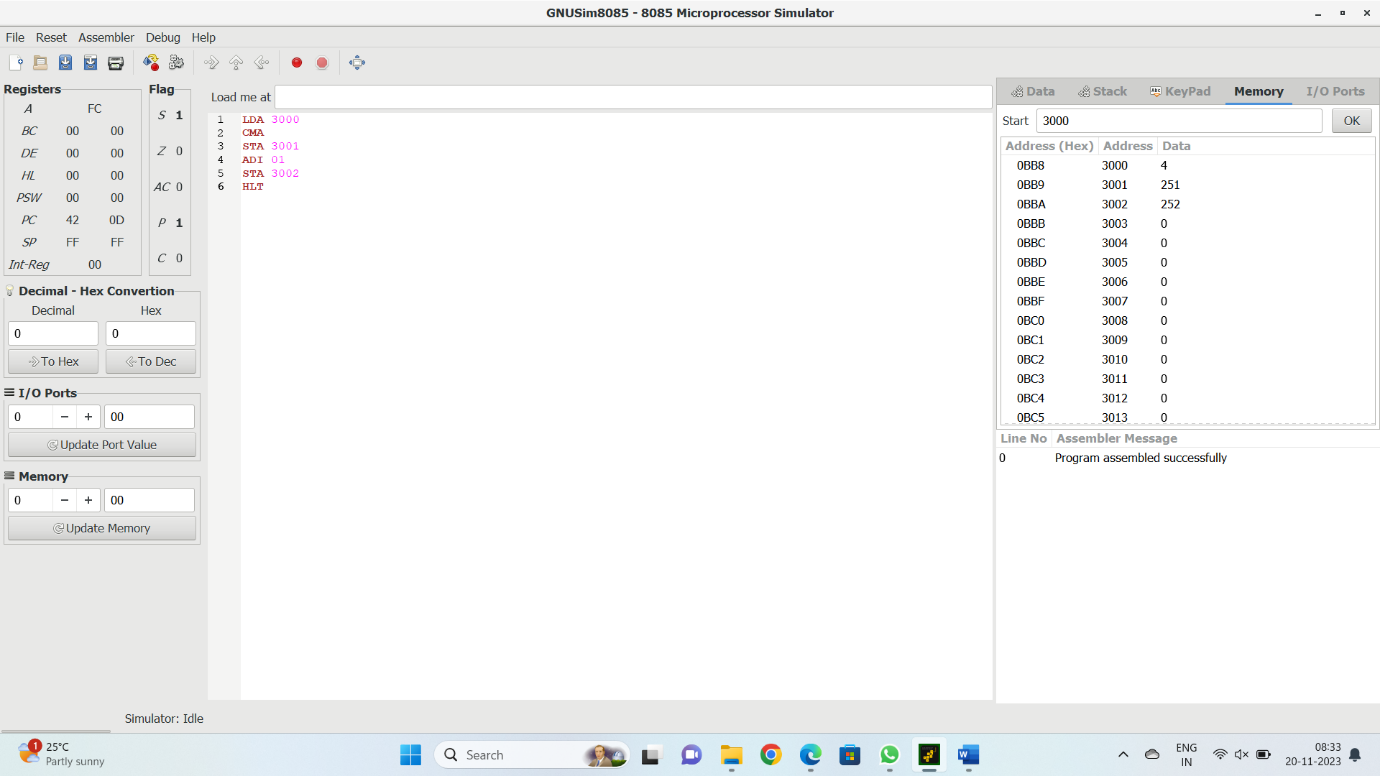
**20.Swapping of two numbers**



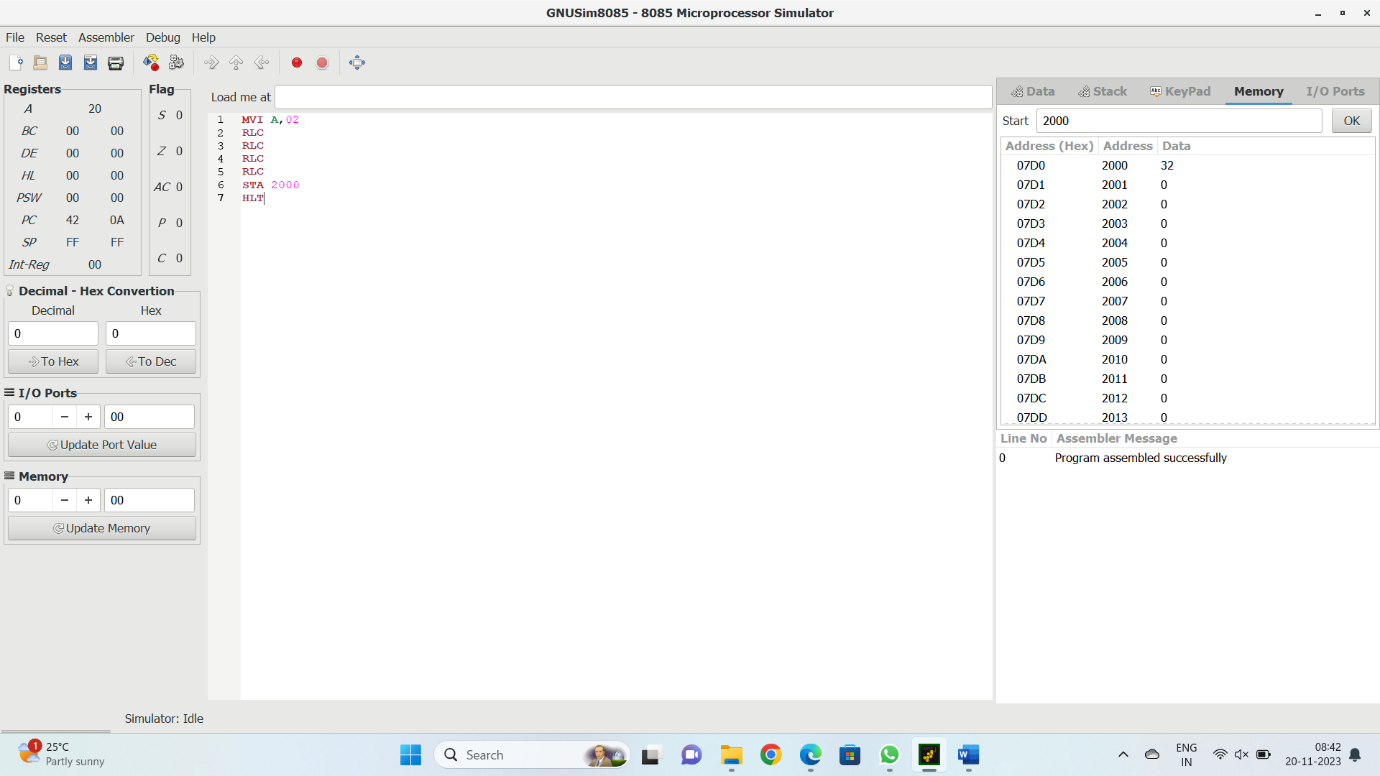
**21.Square of the number**



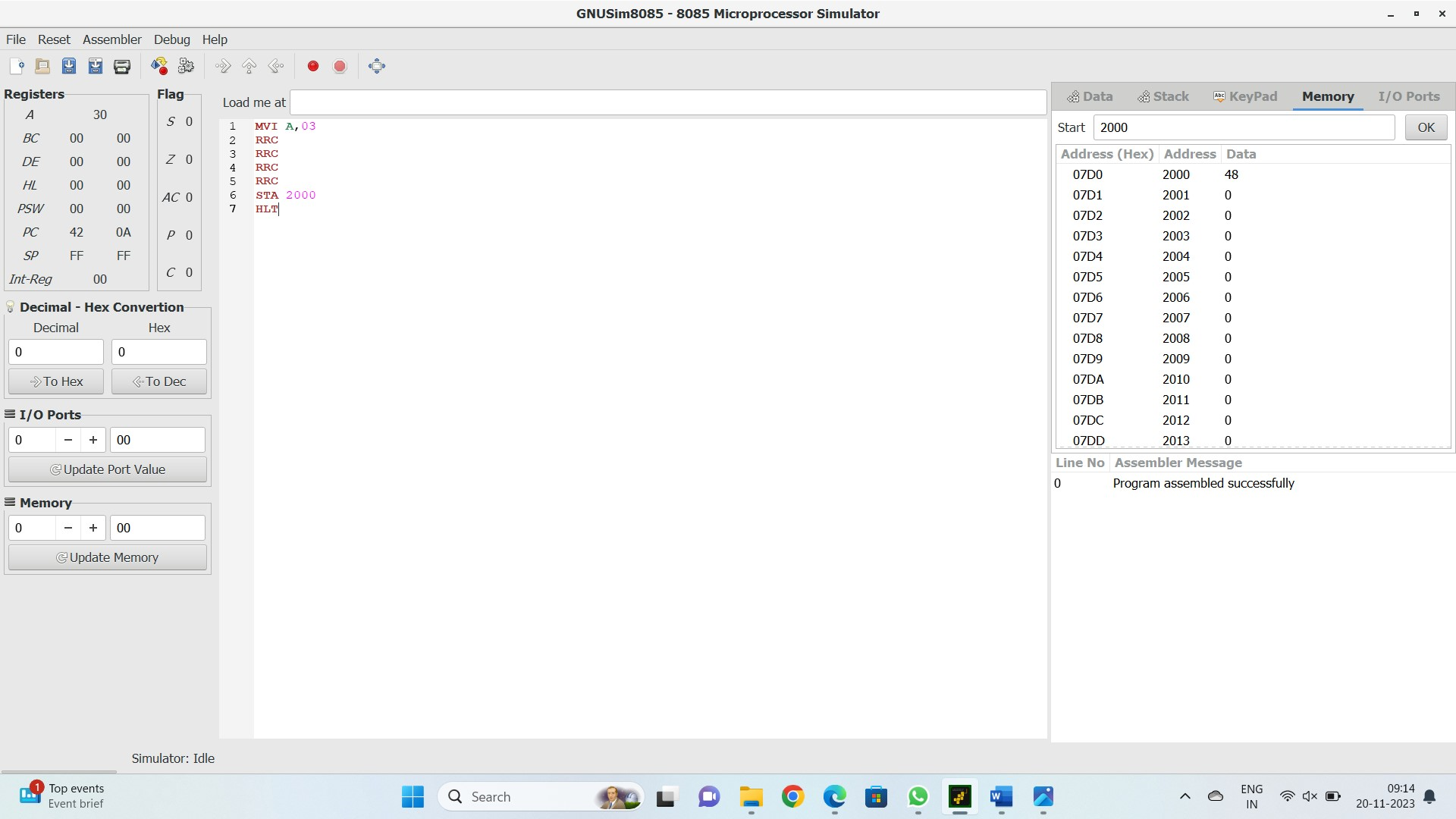
**22.One’s and two’s complement**



**23.Rotate left operation**

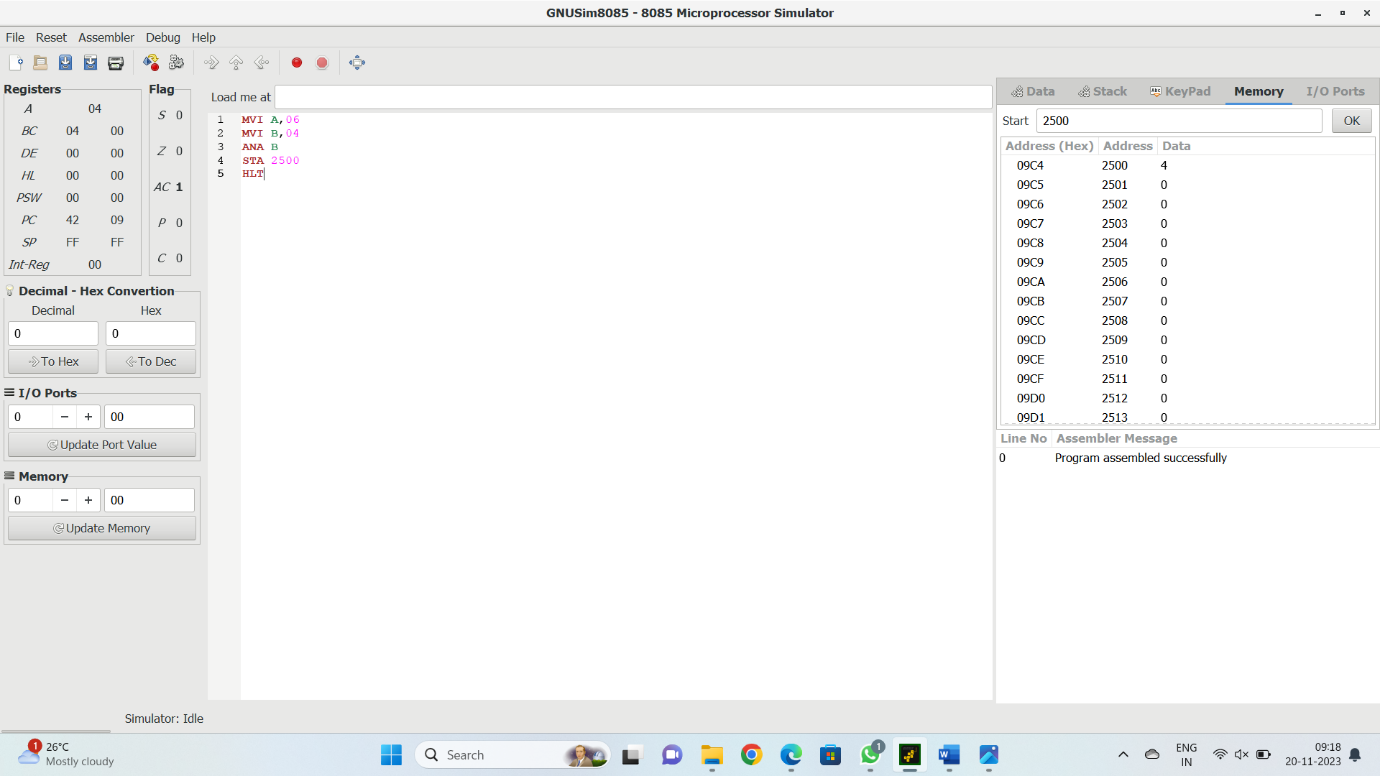


**24.Rotate right operation**

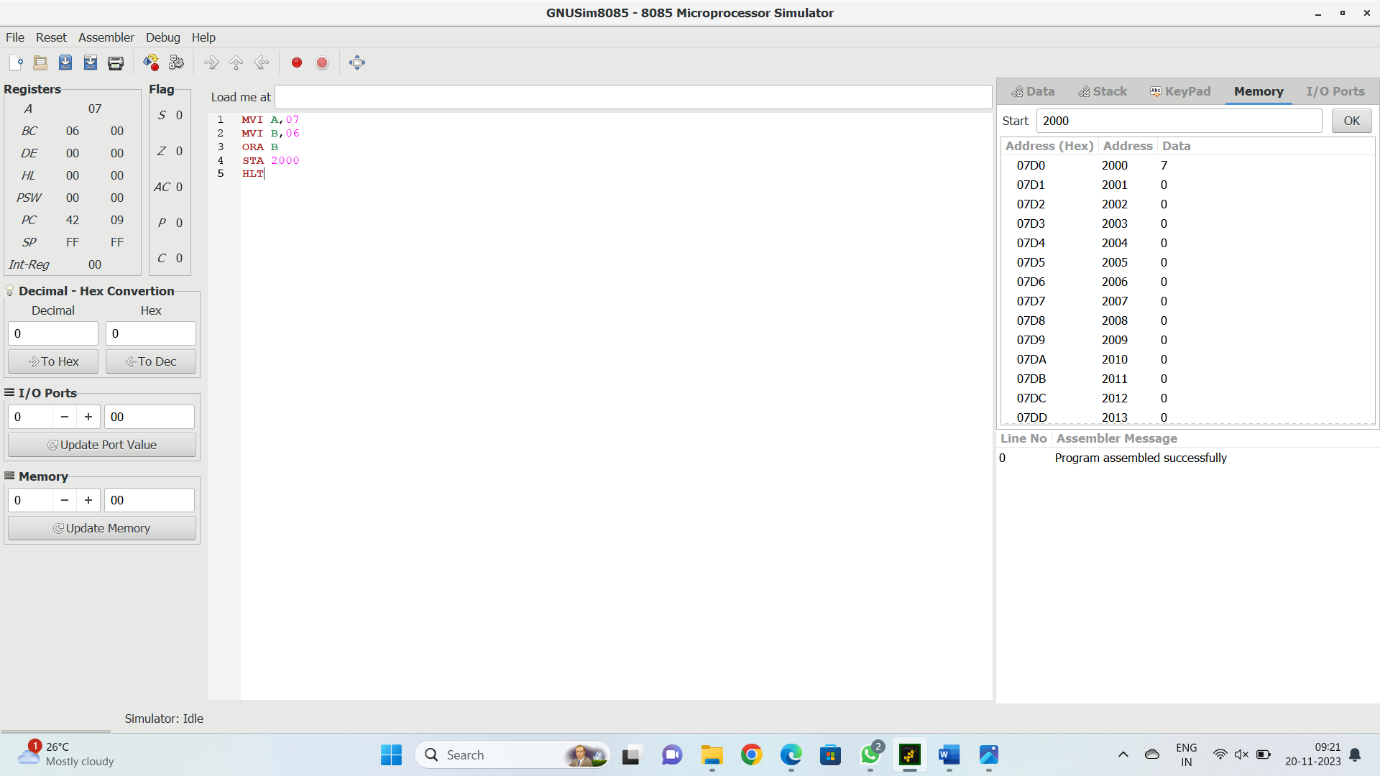


**25.Logical operations**

**And operation**



**Or operation**



**Xor operation**

