High Performance Computing Pate of Completion: - 26.8.2020 Title: - Vector and Matrix operation using CUDA. Problem Statement: - Design parallel afgorithm to:

1) Add two lange vectors

2) Multiply Vector and Matrix

3) Multiply two NXN aways using n2

Processors. Objective: - learn CUDA architecture & programming concepts-Outcomes: - use CUDA programming concepts to perform on envertor and matrix. Requirements: - Ubunty, NVCC compiles, google Cobab (if NVIDI) Theory: -CUDA = compute Unified Device Architecture Host and Device CPU - Host GPU - Device

Kurnel -.

Function to be executed on GPU

prefixed with __global__ eg _ global _ wid add (int to , int & b) Thread Single instance of execution. Block A group of thereads. grid A group of blocks. Architecture Diagram Applications Applications Applications using CUPA driver API Applications Pirect x open GL compute Driver cruntime for cupn CUDA Support for OS Kernel
CUDA parallel Compute engine inside
NYSPIA GPUS

	Matrix Matrix Multiplication	The Administration of the Control of
	Matrix	
	4 7 8 6	
	4 6 7 3	
	10 2 3 8	
	1 10 4 7	
	1 1 3 7	
	1 1	
	Matrix 2	
		10
	2 9 8	4/
	3 11 0	
	9 8	
	6 10 3	
	2 11	
	Result	
	138 149 121	
	107 112 103	
	97 188 130	
	156 125 71	
	123 112 60	
	Conclusion: Thus I understone of matrix, vector aperations in algorithms successfully.	1 second o
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