

Department of Computer Science & Engineering

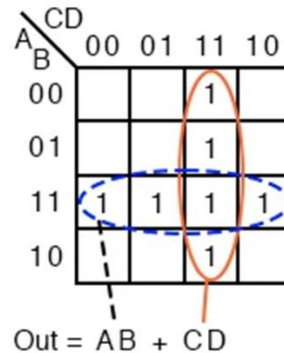
Experiment-1

Given a 4-variable logic expression, simplify it using appropriate technique and simulate the same using basic gates.

Aim: To simplify the given logic expression and simulating the same using basic gates.

Expression: $Out = A'B'CD + A'BCD + ABCD + AB'CD + ABC'D' + ABC'D + ABCD'$

Simplification using K-Map:

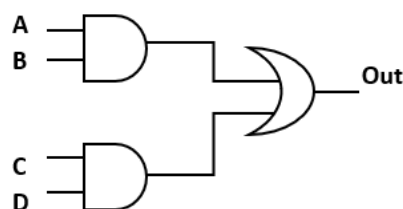


Simplified expression: $Out = AB + CD$

Truth table:

A	B	C	D	F
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	1
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	1
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	1
1	1	0	0	1
1	1	0	1	1
1	1	1	0	1
1	1	1	1	1

Realization using logic gates:



Result: Given logic expression is simplified and the same is simulated using basic gates.