GestureID @ 0x01 Bit5 Bit7 Bit6 Bit4 Bit3 Bit2 Bit1 Bit0 GestureID Desc. Bit Name description [7: 0] GestureID Gesture code 0x00: no gesture 0x01: on a slippery 0x02: decline 0x03: Left slip 0x04: Right slide 0x05: Click 0x0B: Double-click 0x0C: Press FingerNum @ 0x02 Bit7 Bit6 Bit5 Bit4 Bit3 Bit2 Bit1 Bit0 FingerNum Desc. Bit Name description [7: 0] FingerNum The number of fingers. 0: No 1 finger: a finger @ 0x03 XposH Bit3 Bit2 Bit1 Bit0 Bit7 Bit6 Bit5 Bit4 Xpos [11: 8] Desc. Bit Name description [3: 0] XPos X coordinate of the upper 4 bits **XposL** @ 0x04 Bit7 Bit6 Bit5 Bit4 Bit3 Bit2 Bit1 Bit0 Desc. Xpos [7: 0] Bit Name description [7: 0] XPos X coordinate of the lower 8 bits @ 0x05 YposH Bit7 Bit6 Bit5 Bit4 Bit3 Bit2 Bit1 Bit0 Desc. Ypos [11: 8] Bit Name description [3: 0] YPos Y coordinate of the upper 4 bits YposL @ 0x06 Bit7 Bit5 Bit4 Bit3 Bit2 Bit1 Bit0 Bit6 Desc. Ypos [7: 0] Bit Name description [7: 0] YPos Y coordinate of the lower 8 bits BPC0H @ 0xB0 Bit7 Bit5 Bit4 Bit3 Bit2 Bit1 Bit6 Bit0 Desc. BPC0 [15: 8]

Bit Name description [7: 0] BPC0H BPC0 value of upper 8 bits BPC0L @ 0xB1 Bit7 Bit5 Bit4 Bit3 Bit2 Bit1 Bit0 Bit6 Desc. BPC0 [7: 0] Bit Name description [7: 0] BPC0L The lower 8 bits of the value BPC0 BPC1H @ 0xB2 Bit5 Bit7 Bit6 Bit4 Bit3 Bit2 Bit1 Bit0 BPC1 [15: 8] Desc Bit Name description [7: 0] BPC1H BPC1 high value 8 BPC1L @ 0xB3 Bit7 Bit6 Bit5 Bit4 Bit3 Bit2 Bit1 Bit0 BPC1 [7: 0] Desc. Bit Name description [7: 0] BPC1L The lower 8 bits of the value BPC1 ChipID @ 0xA7 Bit5 Bit3 Bit2 Bit7 Bit6 Bit4 Bit1 Bit0 Desc. ChipID Bit Name description [7: 0] ChipID DH **ProjID** @_0xA8 Bit7 Bit6 Bit5 Bit4 Bit3 Bit2 Bit1 Bit0 Desc. ProjID Bit Name description [7: 0] ProjID Project Number **FwVersion** @ 0xA9 Bit7 Bit6 Bit5 Bit4 Bit3 Bit2 Bit1 Bit0 Desc. **FwVersion** Bit Name description [7: 0] FwVersion Software version number MotionMask @ 0xEC Bit7 Bit5 Bit3 Bit2 Bit1 Bit6 Bit4 Bit0 Desc. EnConLR EnConUD EnDClick Bit Name description [2] EnConLR Continuous operation can slide around [1] EnConUD Slide up and down to enable continuous operation [0] **EnDClick** Enable Double-click action

IrqPluseWidth @ 0xED Bit7 Bit6 Bit5 Bit4 Bit3 Bit2 Bit1 Bit0 IrqPluseWidth Desc. Bit Name description [7: 0] IrqPluseWidth Interrupt output low pulse width. Unit 0.1ms, optional values: 1-200. The default value is 10. NorScanPer @ 0xEE Bit7 Bit6 Bit5 Bit4 Bit3 Bit2 Bit1 Bit0 Desc. NorScanPer Bit Name description [7: 0] NorScanPer Rapid detection of the normal cycle. This value will affect the LpAutoWakeTime and AutoSleepTime. Units of 10ms, selectable values: 1 to 30. The default is 1. MotionSlAngle @ 0xEF Bit7 Bit6 Bit5 Bit4 Bit3 Bit2 Bit1 Bit0 MotionSIAngle Desc. Bit Name description [7: 0] MotionSIAngle Sliding partition gesture detection angle control. Angle = tan (c) * 10 c is a positive x-axis direction as a reference angle. LpScanRaw1H @ 0xF0 Bit5 Bit7 Bit6 Bit4 Bit3 Bit2 Bit1 Bit0 Desc. LpScanRaw1H Bit Name [7: 0] LpScanRaw1H Scanning the reference channel 1 low power 8-bit value is high. @ 0xF1 LpScanRaw1L Bit7 Bit6 Bit5 Bit4 Bit3 Bit2 Bit1 Bit0 Desc. LpScanRaw1L Bit Name description [7: 0] LpScanRaw1L The lower 8 bits of the reference value of the low-power scanning No. 1 channel. LpScanRaw2H @ 0xF2 Bit7 Bit5 Bit4 Bit3 Bit1 Bit6 Bit2 Bit0 Desc. LpScanRaw2H Bit Name description [7: 0] LpScanRaw2H Scanning the reference channel 1 low power 8-bit value is high. LpScanRaw2L @ 0xF3 Bit7 Bit6 Bit5 Bit4 Bit3 Bit2 Bit1 Bit0 Desc. LpScanRaw2L Bit Name description [7: 0] LpScanRaw2L The lower 8 bits of the reference value of the low-power scanning No. 1 channel.

		D:10			D:10	Bit2	Bit1	Bit0		
	Bit7	Bit6	Bit5	Bit4	Bit3	DILZ	Bitt	Dito		
Desc.	LpAutoWakeTime									
	Bit Name description									
	[7: 0] Auto-	calibration cycle	LpAutoWakeTim	e low power cor	nsumption.					
				Unit 1 minute	optional value:	1-5. The default	t is 5.			
LpScanTH	@ 0xF5									
	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0		
Desc.	LpScanTH									
	Bit Na			description						
	[7: 0] Lp	ScanTH		Low-power scar	nning wake thres	hold. The smalle	er the more			
				sensitive. Optio	nal Value: 1 to 2	55. The default v	alue is 48.			
.pScanWin		@ 0xF6								
	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0		
Desc.				LpSca						
•	Bit Na			description						
	[7: 0] Lp	ScanWin		•		-	sitivity, the higher	the		
				power consump	otion. Optional va	lue: 0,1,2,3. The	default is 3.			
_pScanFreq		@ 0xF7								
	D.12		D.::E	D'' 4	D://0	D:10	D'' 4	D:10		
	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0		
Desc.		Bit6	Bit5	LpScar	nFreq	Bit2	Bit1	Bit0		
Desc.	Bit Na	Bit6	Bit5	LpScar description	nFreq		Bit1	Bit0		
Desc.	Bit Na	Bit6	Bit5	LpScar description Low-power scar	nFreq	The smaller the		Bit0		
Desc.	Bit Na	Bit6	Bit5	LpScar description Low-power scar	nFreq	The smaller the		Bit0		
	Bit Na	Bit6 me ScanFreq	Bit5	LpScar description Low-power scar	nFreq	The smaller the		Bit0		
	Bit Na [7: 0] Lp:	Bit6 me ScanFreq @ 0xF8		LpScar description Low-power scar more sensitive.	nFreq nning frequency. Optional Value:	The smaller the 1 to 255. The det	fault value is 7.			
.pScanIdac	Bit Na	Bit6 me ScanFreq	Bit5	LpScar description Low-power scar more sensitive.	nning frequency. Optional Value:	The smaller the		Bit0		
	Bit Na [7: 0] Lp: Bit7	Bit6 me ScanFreq @ 0xF8 Bit6		LpScar description Low-power scar more sensitive. Bit4 LpScar	nning frequency. Optional Value: Bit3	The smaller the 1 to 255. The det	fault value is 7.			
_pScanidac	Bit Na [7: 0] Lp: Bit7	Bit6 me ScanFreq @ 0xF8 Bit6		LpScar description Low-power scar more sensitive. Bit4 LpScar description	nning frequency. Optional Value: Bit3	The smaller the 1 to 255. The det Bit2	fault value is 7.			
_pScanidac	Bit Na [7: 0] Lp: Bit7	Bit6 me ScanFreq @ 0xF8 Bit6		LpScar description Low-power scar more sensitive. Bit4 LpScar description Low power scar	nning frequency. Optional Value: Bit3 nldac	The smaller the 1 to 255. The det Bit2	fault value is 7.			
_pScanidac	Bit Na [7: 0] Lp: Bit7	Bit6 me ScanFreq @ 0xF8 Bit6		LpScar description Low-power scar more sensitive. Bit4 LpScar description Low power scar	nning frequency. Optional Value: Bit3	The smaller the 1 to 255. The det Bit2	fault value is 7.			
pScanIdac Desc.	Bit Na [7: 0] Lp: Bit7 Bit Na [7: 0] Lp:	Bit6 me ScanFreq @ 0xF8 Bit6 me ScanIdac		LpScar description Low-power scar more sensitive. Bit4 LpScar description Low power scar	nning frequency. Optional Value: Bit3 nldac	The smaller the 1 to 255. The det Bit2	fault value is 7.			
_pScanIdac Desc.	Bit Na [7: 0] Lp: Bit 7 Bit Na [7: 0] Lp:	Bit6 me ScanFreq @ 0xF8 Bit6 me ScanIdac	Bit5	LpScar description Low-power scar more sensitive. Bit4 LpScar description Low power scar sensitive. Optio	nning frequency. Optional Value: Bit3 nIdac n current. The sn	The smaller the 1 to 255. The det Bit2 naller the more 55.	fault value is 7.	Bit0		
_pScanIdac Desc.	Bit Na [7: 0] Lp: Bit7 Bit Na [7: 0] Lp:	Bit6 me ScanFreq @ 0xF8 Bit6 me ScanIdac		LpScar description Low-power scar more sensitive. Bit4 LpScar description Low power scar sensitive. Option	nning frequency. Optional Value: Bit3 nIdac n current. The snanal Value: 1 to 2:	The smaller the 1 to 255. The det Bit2	fault value is 7.			
LpScanidac Desc.	Bit Na [7: 0] Lp: Bit Na [7: 0] Lp: ne Bit7	Bit6 me ScanFreq @ 0xF8 Bit6 me ScanIdac @ 0xF9 Bit6	Bit5	LpScar description Low-power scar more sensitive. Bit4 LpScar description Low power scar sensitive. Optio Bit4 AutoSlee	nning frequency. Optional Value: Bit3 nIdac n current. The sn anal Value: 1 to 2	The smaller the 1 to 255. The det Bit2 naller the more 55.	fault value is 7.	Bit0		
LpScanIdac Desc.	Bit Na [7: 0] Lp: Bit 7 Bit Na [7: 0] Lp: me Bit7	Bit6 me ScanFreq @ 0xF8 Bit6 me ScanIdac @ 0xF9 Bit6	Bit5	LpScar description Low-power scar more sensitive. Bit4 LpScar description Low power scar sensitive. Option Bit4 AutoSleed description	nning frequency. Optional Value: Bit3 nIdac n current. The snanal Value: 1 to 2: Bit3	The smaller the 1 to 255. The det Bit2 maller the more 55.	fault value is 7. Bit1	Bit0		
LpScanIdac Desc.	Bit Na [7: 0] Lp: Bit 7 Bit Na [7: 0] Lp: me Bit7	Bit6 me ScanFreq @ 0xF8 Bit6 me ScanIdac @ 0xF9 Bit6	Bit5	LpScar description Low-power scar more sensitive. Bit4 LpScar description Low power scar sensitive. Option Bit4 AutoSleed description When no touch	nning frequency. Optional Value: Bit3 nIdac n current. The smanal Value: 1 to 2: Bit3 epTime , into low power r	The smaller the 1 to 255. The det Bit2 maller the more 55.	fault value is 7. Bit1	Bit0		
_pScanIdac Desc.	Bit Na [7: 0] Lp: Bit 7 Bit Na [7: 0] Lp: me Bit7	Bit6 me ScanFreq @ 0xF8 Bit6 me ScanIdac @ 0xF9 Bit6	Bit5	LpScar description Low-power scar more sensitive. Bit4 LpScar description Low power scar sensitive. Option Bit4 AutoSleed description	nning frequency. Optional Value: Bit3 nIdac n current. The smanal Value: 1 to 2: Bit3 epTime , into low power r	The smaller the 1 to 255. The det Bit2 maller the more 55.	fault value is 7. Bit1	Bit0		
Desc. AutoSleepTin	Bit Na [7: 0] Lp: Bit 7 Bit Na [7: 0] Lp: me Bit7	Bit6 me ScanFreq @ 0xF8 Bit6 me ScanIdac @ 0xF9 Bit6 me toSleepTime	Bit5	LpScar description Low-power scar more sensitive. Bit4 LpScar description Low power scar sensitive. Option Bit4 AutoSleed description When no touch	nning frequency. Optional Value: Bit3 nIdac n current. The smanal Value: 1 to 2: Bit3 epTime , into low power r	The smaller the 1 to 255. The det Bit2 maller the more 55.	fault value is 7. Bit1	Bit0		
Desc. AutoSleepTin	Bit Na [7: 0] Lp: Bit Na [7: 0] Lp: Bit Na [7: 0] Lp: Bit Na [7: 0] Au	Bit6 me ScanFreq @ 0xF8 Bit6 me ScanIdac @ 0xF9 Bit6	Bit5	LpScar description Low-power scar more sensitive. Bit4 LpScar description Low power scar sensitive. Option Bit4 AutoSleed description When no touch	nning frequency. Optional Value: Bit3 nIdac n current. The sn anal Value: 1 to 2: Bit3 apTime , into low power resis 2S.	The smaller the 1 to 255. The def Bit2 maller the more 55. Bit2	Bit1 Bit1	Bit0		
Desc. Desc. Desc.	Bit Na [7: 0] Lp: Bit 7 Bit Na [7: 0] Lp: me Bit 7 Bit Na [7: 0] Au	Bit6 me ScanFreq @ 0xF8 Bit6 me ScanIdac @ 0xF9 Bit6 me toSleepTime @ 0xFA Bit6	Bit5	LpScar description Low-power scar more sensitive. Bit4 LpScar description Low power scar sensitive. Option Bit4 AutoSleed description When no touch. Unit 1S, default	nning frequency. Optional Value: Bit3 nIdac n current. The smanal Value: 1 to 2: Bit3 epTime , into low power r	The smaller the 1 to 255. The det Bit2 maller the more 55.	fault value is 7. Bit1	Bit0		
Desc. AutoSleepTin	Bit Na [7: 0] Lp: Bit Na [7: 0] Lp: Bit Na [7: 0] Lp: Bit Na [7: 0] Au Bit Na	Bit6 me ScanFreq @ 0xF8 Bit6 me ScanIdac @ 0xF9 Bit6 me toSleepTime	Bit5	LpScar description Low-power scar more sensitive. Bit4 LpScar description Low power scar sensitive. Option Bit4 AutoSlee description When no touch, Unit 1S, default Bit4 LP place	nning frequency. Optional Value: Bit3 nIdac n current. The sn anal Value: 1 to 2: Bit3 apTime , into low power recis 2S.	The smaller the 1 to 255. The def Bit2 maller the more 55. Bit2	Bit1 Bit1	Bit0		
_pScanIdac Desc. AutoSleepTir Desc.	Bit Na [7: 0] Lp: Bit 7 Bit Na [7: 0] Lp: me Bit 7 Bit Na [7: 0] Au	Bit6 me ScanFreq @ 0xF8 Bit6 me ScanIdac @ 0xF9 Bit6 me toSleepTime @ 0xFA Bit6	Bit5	LpScar description Low-power scar more sensitive. Bit4 LpScar description Low power scar sensitive. Option Bit4 AutoSleet description When no touch. Unit 1S, default Bit4 LP place description	nning frequency. Optional Value: Bit3 InIdac In current. The smanal Value: 1 to 2: Bit3 EpTime Into low power resis 2S. Bit3	The smaller the 1 to 255. The det Bit2 maller the more 55. Bit2 mode automatica	Bit1 Bit1	Bit0		

[5]	EnChange	Upon detecting a touch state changes, pulsed Low.
[4]	EnMotion	When the detected gesture is pulsed Low.
[0]	OnceWLP	Press gesture only issue a pulse signal is low.

AutoReset @ 0xFB

		Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
Desc	Э.				Debouce	Time			

Bit Name description

[7: 0] DebounceTime X seconds but no valid touch gesture, automatically reset. Unit 1S, do

not enable this function is zero. The default is 5.

LongPressTime @ 0xFC

	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
Desc.				LongPres	sTime			

Bit Name description

[7: 0] LongPressTime Press automatically reset after x seconds.

Unit 1S, do not enable this function is zero. The default is 10.

IOCtI @ 0xFD

	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
Desc.						SOFT_RST II	C_OD	En1v8
	Bit Nam	ne		description				

[2] SOFT_RST Master reset by pulling the soft touch IRQ pin implemented. 0: Disable soft reset. 1: Enable soft reset.

[1] IIC_OD IIC pin drive mode, the default pull-up resistor. 0: pull-up resistor 1: OD

[0] En1v8 IIC and IRQ pin level selected, the default is the VDD level. 0: VDD 1:

1.8V

....

DisAutoSleep @ 0xFE

DISAULUGICE	Ρ	W UXFL						
	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
Desc.				DisAuto	Sleep			

Bit Name [7: 0] DisAutoSleep

The default is 0, automatically enabled into low power mode. Is a non-zero value, to disable automatic into low power mode.