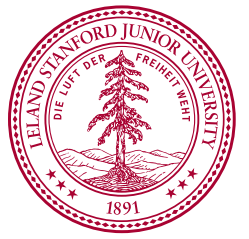
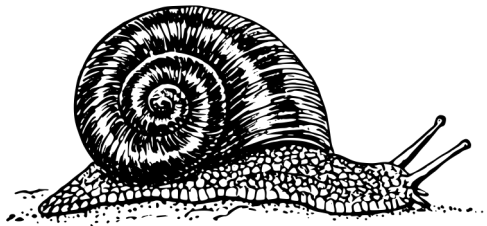


Mixed-Signal Emulation

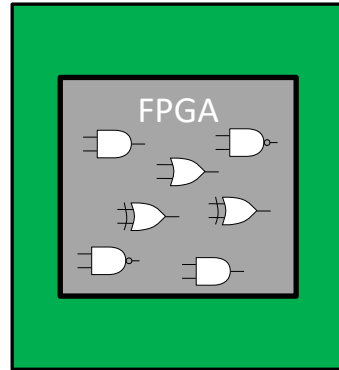
Steven Herbst, Byong Chan Lim, and Mark Horowitz



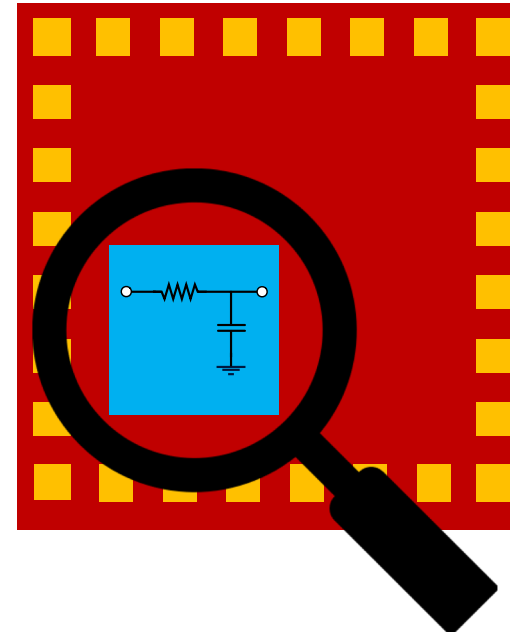
CPU simulation
Slow...



FPGA emulation
Fast!



However...
One analog block can
become the bottleneck



Solution
Digitally-driven
emulation!

