



The diagram shows a rectangular chip with a dashed white border. The overall dimensions are 4.9mm in width and 4.1mm in height, indicated by arrows at the bottom and right. A white arrow points from the 'Project Area' label to a dashed white rectangle that encloses the 'CPU', 'Global Buffer', and 'CGRA' components. The 'CPU' is a small square in the top-left corner. The 'Global Buffer' is a large rectangular area in the top-right. The 'CGRA' is a large rectangular area at the bottom. The background of the chip is a colorful, abstract pattern.

Project Area

CPU

**Global
Buffer**

CGRA

4.1mm

4.9mm