

FMA-32

March 25, 2012

1 Design Map

Tables are tables

INFO.Pareto	0	0	0	0	
TOP.DesignIndex	0	1	2	3	
TOP.VT	hvt	hvt	hvt	hvt	
TOP.Voltage	0.8	0.8	0.8	0.8	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	1	1	1	1	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	

INFO.Pareto	1	1	1	1	
TOP.DesignIndex	4	5	6	7	
TOP.VT	hvt	hvt	hvt	hvt	
TOP.Voltage	0.8	0.8	0.8	0.8	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	2	2	2	2	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	

INFO.Pareto	1	1	1	1	
TOP.DesignIndex	8	9	10	11	
TOP.VT	hvt	hvt	hvt	hvt	
TOP.Voltage	0.8	0.8	0.8	0.8	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	3	3	3	3	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	

INFO.Pareto	0	1	1	0	
TOP.DesignIndex	12	13	14	15	
TOP.VT	hvt	hvt	hvt	hvt	
TOP.Voltage	0.8	0.8	0.8	0.8	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	ON	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	4	4	NA	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	ON	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	Wallace	NA	NA	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	0	0	0	0	
TOP.DesignIndex	16	17	18	19	
TOP.VT	hvt	hvt	hvt	hvt	
TOP.Voltage	0.9	0.9	0.9	0.9	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	1	1	1	1	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	1	1	1	1	
TOP.DesignIndex	20	21	22	23	
TOP.VT	hvt	hvt	hvt	hvt	
TOP.Voltage	0.9	0.9	0.9	0.9	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	2	2	2	2	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	0	1	1	0	
TOP.DesignIndex	24	25	26	27	
TOP.VT	hvt	hvt	hvt	hvt	
TOP.Voltage	0.9	0.9	0.9	0.9	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	3	3	3	3	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	0	0	1	0	
TOP.DesignIndex	28	29	30	31	
TOP.VT	hvt	hvt	hvt	hvt	
TOP.Voltage	0.9	0.9	0.9	0.9	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	ON	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	4	4	NA	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	ON	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	Wallace	NA	NA	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	

INFO.Pareto	0	0	1	1	
TOP.DesignIndex	32	33	34	35	
TOP.VT	hvt	hvt	hvt	hvt	
TOP.Voltage	1.0	1.0	1.0	1.0	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	1	1	1	1	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	1	1	1	1	
TOP.DesignIndex	36	37	38	39	
TOP.VT	hvt	hvt	hvt	hvt	
TOP.Voltage	1.0	1.0	1.0	1.0	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	2	2	2	2	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	0	1	1	0	
TOP.DesignIndex	40	41	42	43	
TOP.VT	hvt	hvt	hvt	hvt	
TOP.Voltage	1.0	1.0	1.0	1.0	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	3	3	3	3	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	0	0	1	0	
TOP.DesignIndex	44	45	46	47	
TOP.VT	hvt	hvt	hvt	hvt	
TOP.Voltage	1.0	1.0	1.0	1.0	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	ON	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	4	4	NA	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	ON	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	Wallace	NA	NA	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	1	0	1	0	
TOP.DesignIndex	48	49	50	51	
TOP.VT	lvt	lvt	lvt	lvt	
TOP.Voltage	0.8	0.8	0.8	0.8	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	1	1	1	1	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	

INFO.Pareto	1	1	1	1	
TOP.DesignIndex	52	53	54	55	
TOP.VT	lvt	lvt	lvt	lvt	
TOP.Voltage	0.8	0.8	0.8	0.8	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	2	2	2	2	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	1	1	1	1	
TOP.DesignIndex	56	57	58	59	
TOP.VT	lvt	lvt	lvt	lvt	
TOP.Voltage	0.8	0.8	0.8	0.8	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	3	3	3	3	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	0	1	1	1	
TOP.DesignIndex	60	61	62	63	
TOP.VT	lvt	lvt	lvt	lvt	
TOP.Voltage	0.8	0.8	0.8	0.8	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	ON	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	4	4	NA	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	ON	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	Wallace	NA	NA	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	1	0	1	0	
TOP.DesignIndex	64	65	66	67	
TOP.VT	lvt	lvt	lvt	lvt	
TOP.Voltage	0.9	0.9	0.9	0.9	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	1	1	1	1	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	1	1	1	0	
TOP.DesignIndex	68	69	70	71	
TOP.VT	lvt	lvt	lvt	lvt	
TOP.Voltage	0.9	0.9	0.9	0.9	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	2	2	2	2	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	

INFO.Pareto	1	1	1	1	
TOP.DesignIndex	72	73	74	75	
TOP.VT	lvt	lvt	lvt	lvt	
TOP.Voltage	0.9	0.9	0.9	0.9	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	3	3	3	3	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	0	1	1	1	
TOP.DesignIndex	76	77	78	79	
TOP.VT	lvt	lvt	lvt	lvt	
TOP.Voltage	0.9	0.9	0.9	0.9	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	ON	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	4	4	NA	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	ON	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	Wallace	NA	NA	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	1	0	1	0	
TOP.DesignIndex	80	81	82	83	
TOP.VT	lvt	lvt	lvt	lvt	
TOP.Voltage	1.0	1.0	1.0	1.0	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	1	1	1	1	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	1	1	1	0	
TOP.DesignIndex	84	85	86	87	
TOP.VT	lvt	lvt	lvt	lvt	
TOP.Voltage	1.0	1.0	1.0	1.0	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	2	2	2	2	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	1	1	1	1	
TOP.DesignIndex	88	89	90	91	
TOP.VT	lvt	lvt	lvt	lvt	
TOP.Voltage	1.0	1.0	1.0	1.0	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	3	3	3	3	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	

INFO.Pareto	0	1	1	1	
TOP.DesignIndex	92	93	94	95	
TOP.VT	lvt	lvt	lvt	lvt	
TOP.Voltage	1.0	1.0	1.0	1.0	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	ON	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	4	4	NA	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	ON	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	Wallace	NA	NA	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	0	0	0	0	
TOP.DesignIndex	96	97	98	99	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	0.8	0.8	0.8	0.8	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	1	1	1	1	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	1	1	0	1	
TOP.DesignIndex	100	101	102	103	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	0.8	0.8	0.8	0.8	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	2	2	2	2	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	1	1	1	1	
TOP.DesignIndex	104	105	106	107	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	0.8	0.8	0.8	0.8	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	3	3	3	3	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	0	0	1	1	
TOP.DesignIndex	108	109	110	111	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	0.8	0.8	0.8	0.8	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	ON	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	4	4	NA	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	ON	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	Wallace	NA	NA	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	

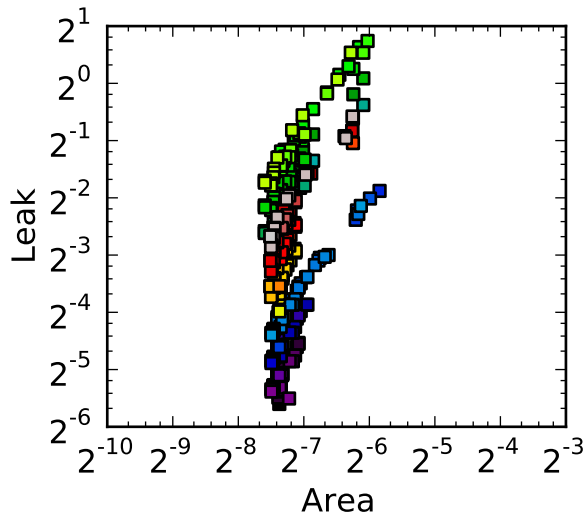
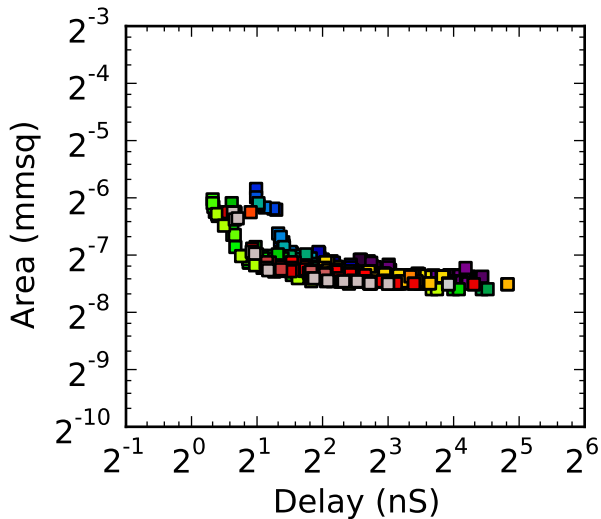
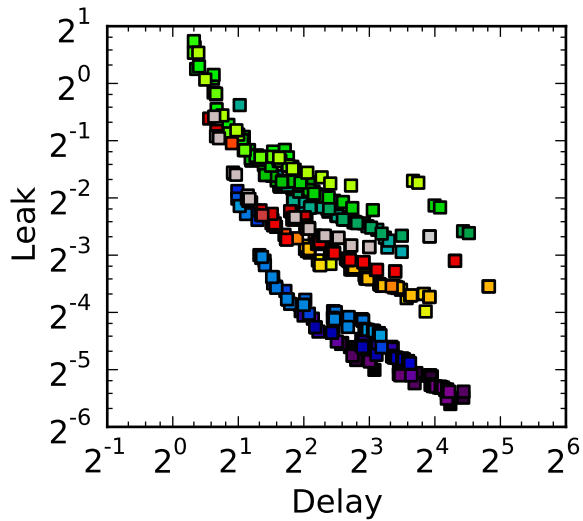
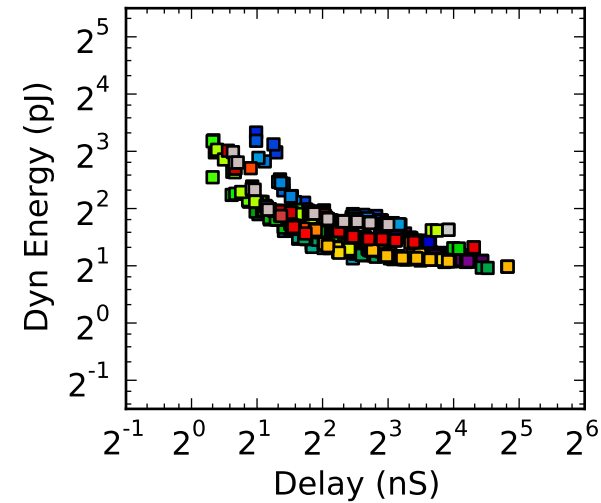
INFO.Pareto	0	0	0	0	
TOP.DesignIndex	112	113	114	115	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	0.9	0.9	0.9	0.9	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	1	1	1	1	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	1	1	0	1	
TOP.DesignIndex	116	117	118	119	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	0.9	0.9	0.9	0.9	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	2	2	2	2	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	1	1	1	1	
TOP.DesignIndex	120	121	122	123	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	0.9	0.9	0.9	0.9	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	3	3	3	3	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	0	0	1	1	
TOP.DesignIndex	124	125	126	127	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	0.9	0.9	0.9	0.9	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	ON	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	4	4	NA	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	ON	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	Wallace	NA	NA	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	
INFO.Pareto	0	0	0	0	
TOP.DesignIndex	128	129	130	131	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	1.0	1.0	1.0	1.0	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	1	1	1	1	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	

INFO.Pareto	1	1	1	1	
TOP.DesignIndex	132	133	134	135	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	1.0	1.0	1.0	1.0	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	2	2	2	2	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	

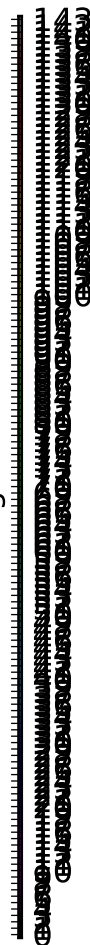
INFO.Pareto	1	1	1	1	
TOP.DesignIndex	136	137	138	139	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	1.0	1.0	1.0	1.0	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	3	3	3	3	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	OS1	Wallace	ZM	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	

INFO.Pareto	0	0	1	1	
TOP.DesignIndex	140	141	142	143	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	1.0	1.0	1.0	1.0	
TOP.top_FMA.FMA.Designware_MODE	OFF	OFF	OFF	ON	
TOP.top_FMA.FMA.EnableDenormals	YES	YES	YES	YES	
TOP.top_FMA.FMA.ExponentWidth	8	8	8	8	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.BoothType	4	4	NA	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.Designware_MODE	OFF	OFF	ON	NA	
TOP.top_FMA.FMA.FMA_MAN0.MUL0.TreeType	Array	Wallace	NA	NA	
TOP.top_FMA.FMA.FractionWidth	23	23	23	23	
TOP.top_FMA.FMA.UseInc	NO	NO	NO	NO	

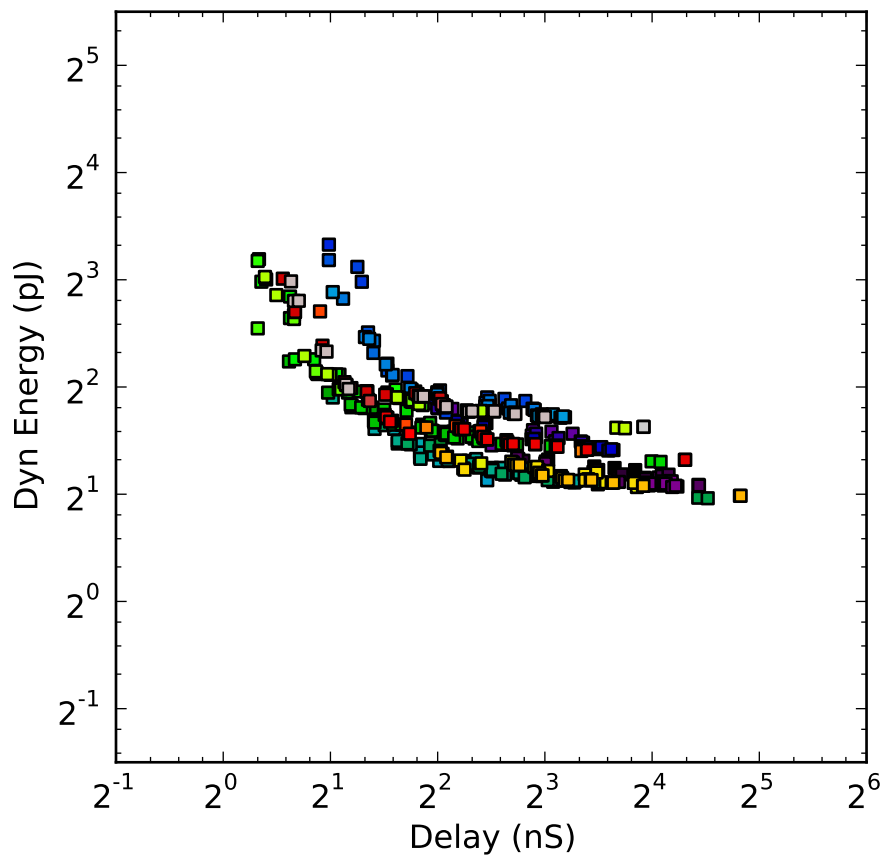
FMA-32-Pareto



Design Index



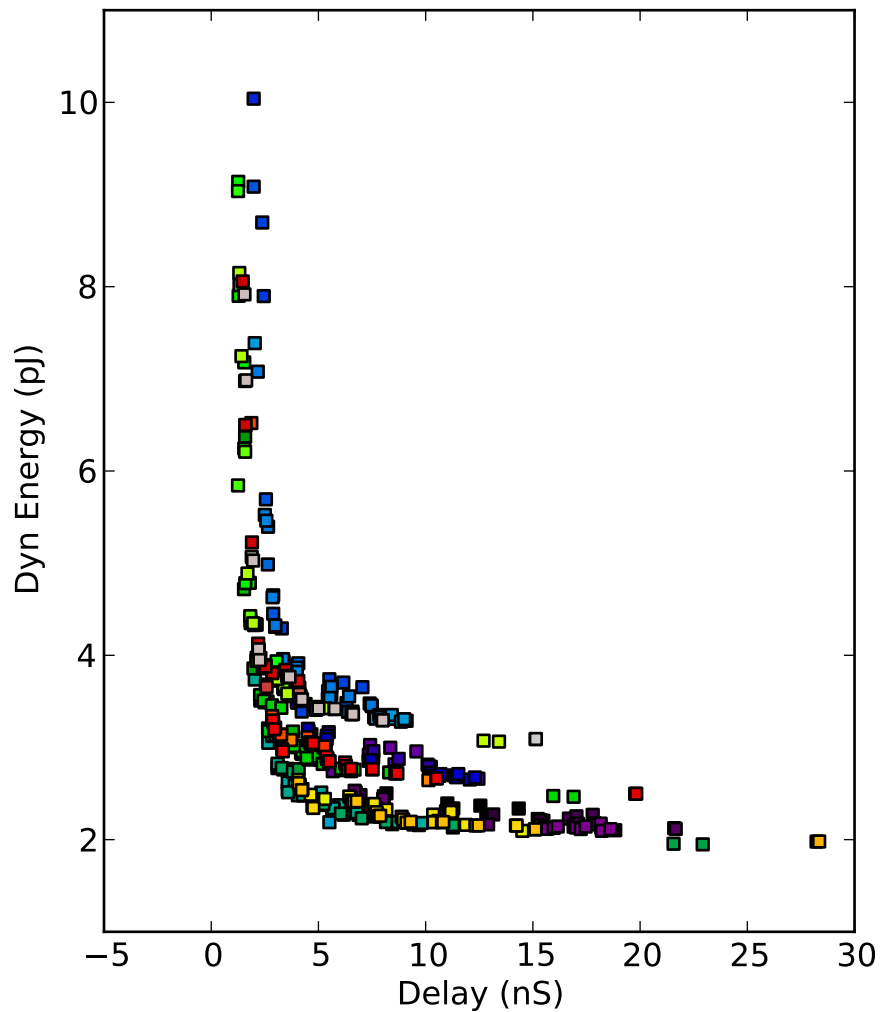
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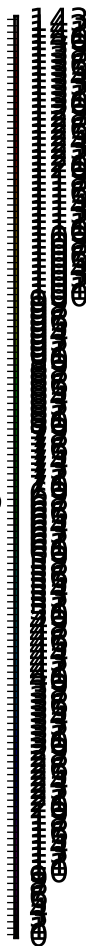
Design Index



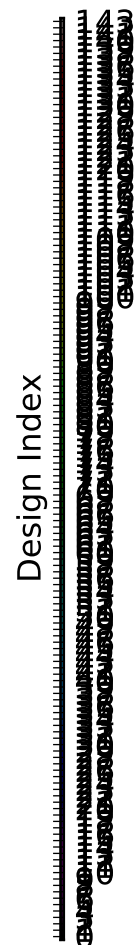
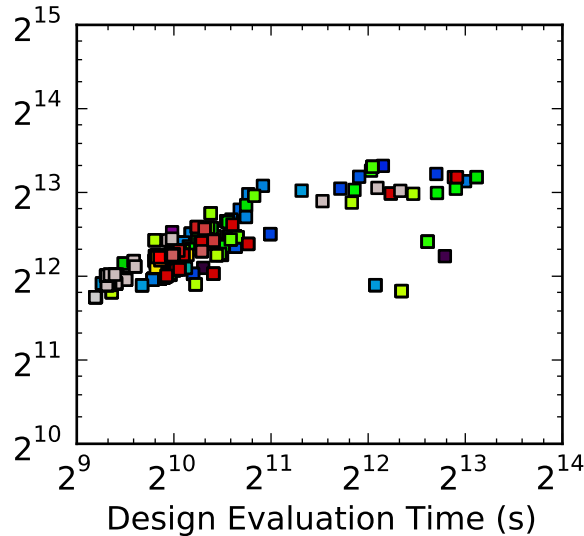
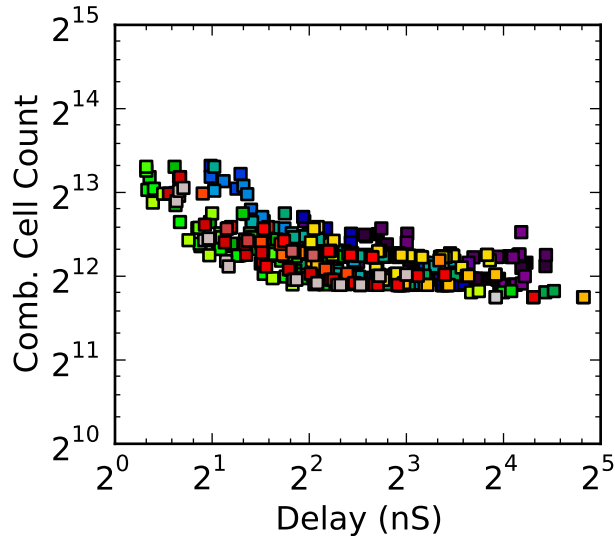
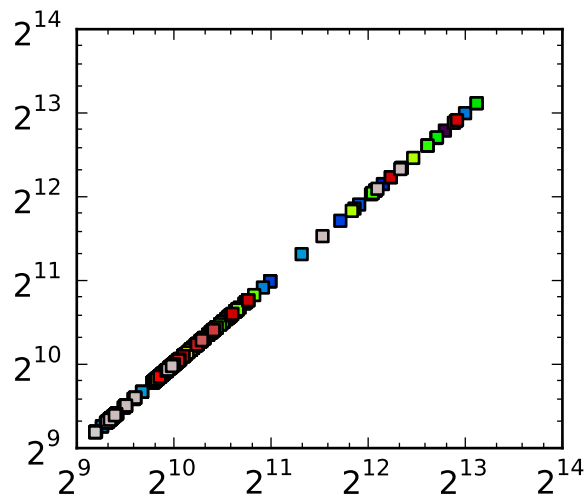
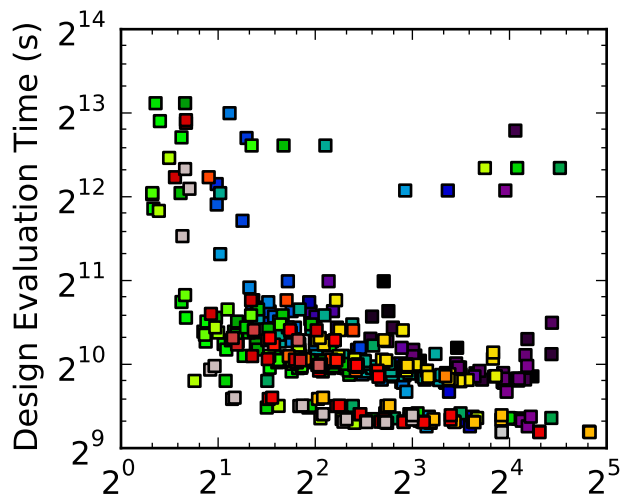
FMA-32-Pareto



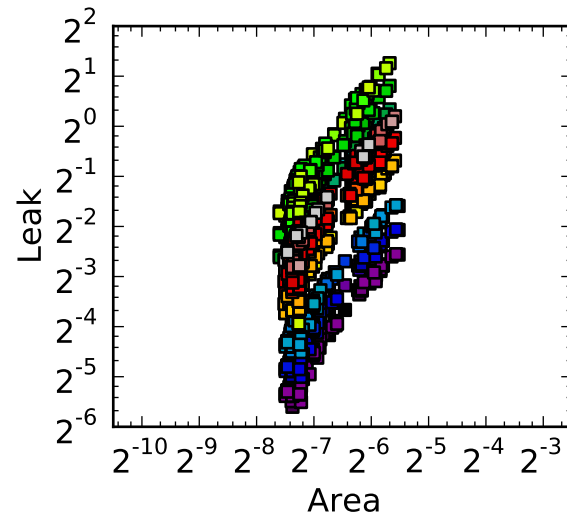
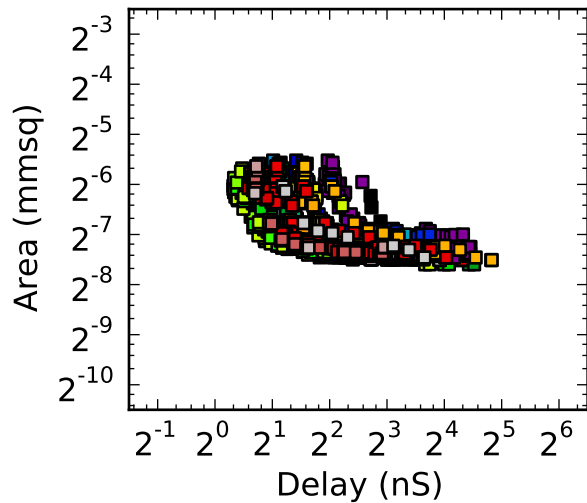
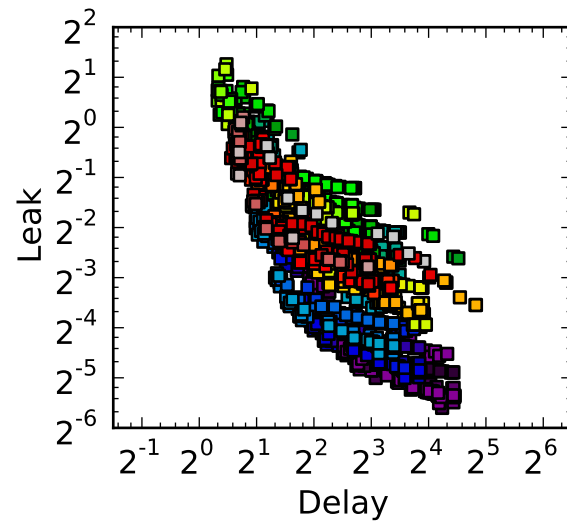
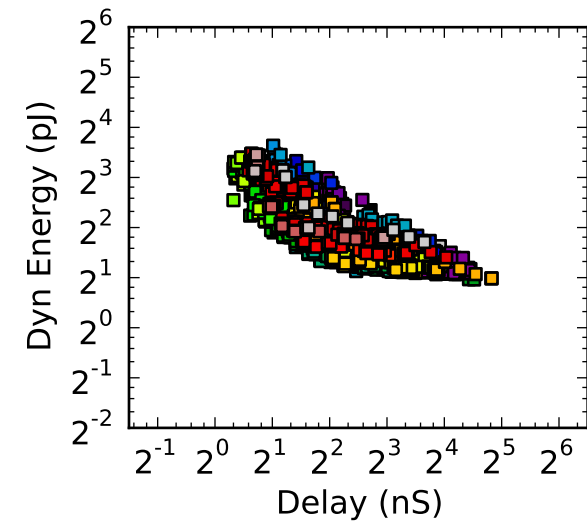
Design Index



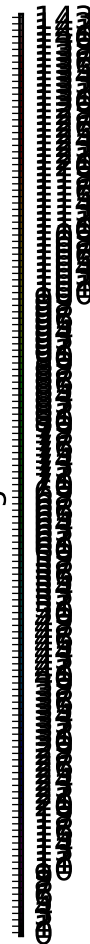
FMA-32-Pareto

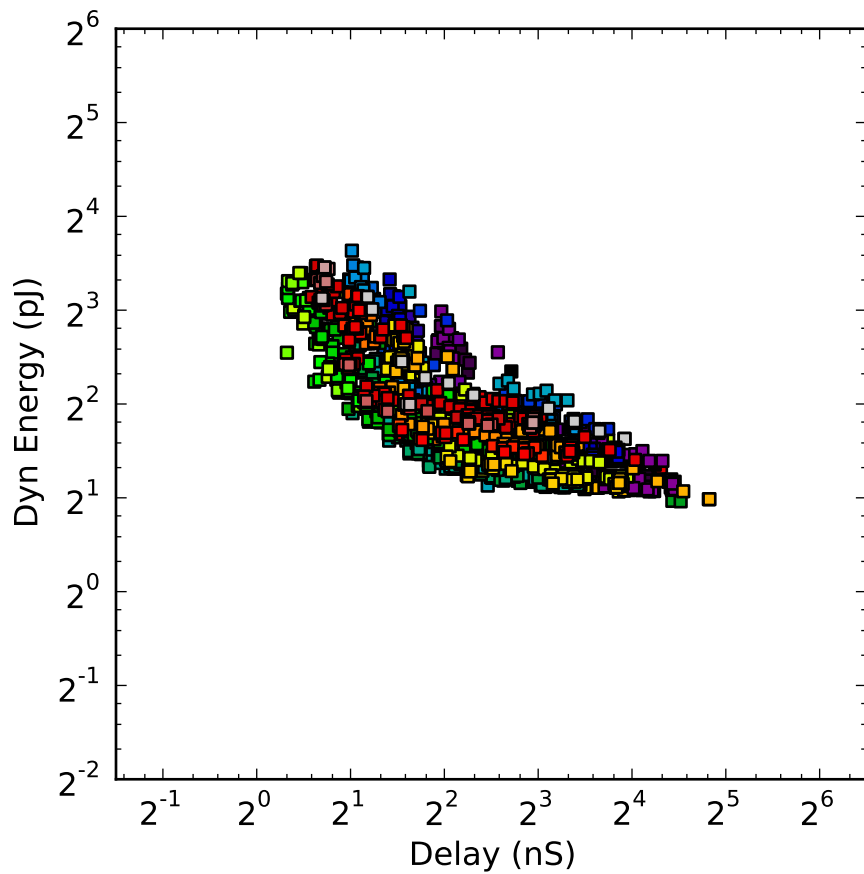


FMA-32-All



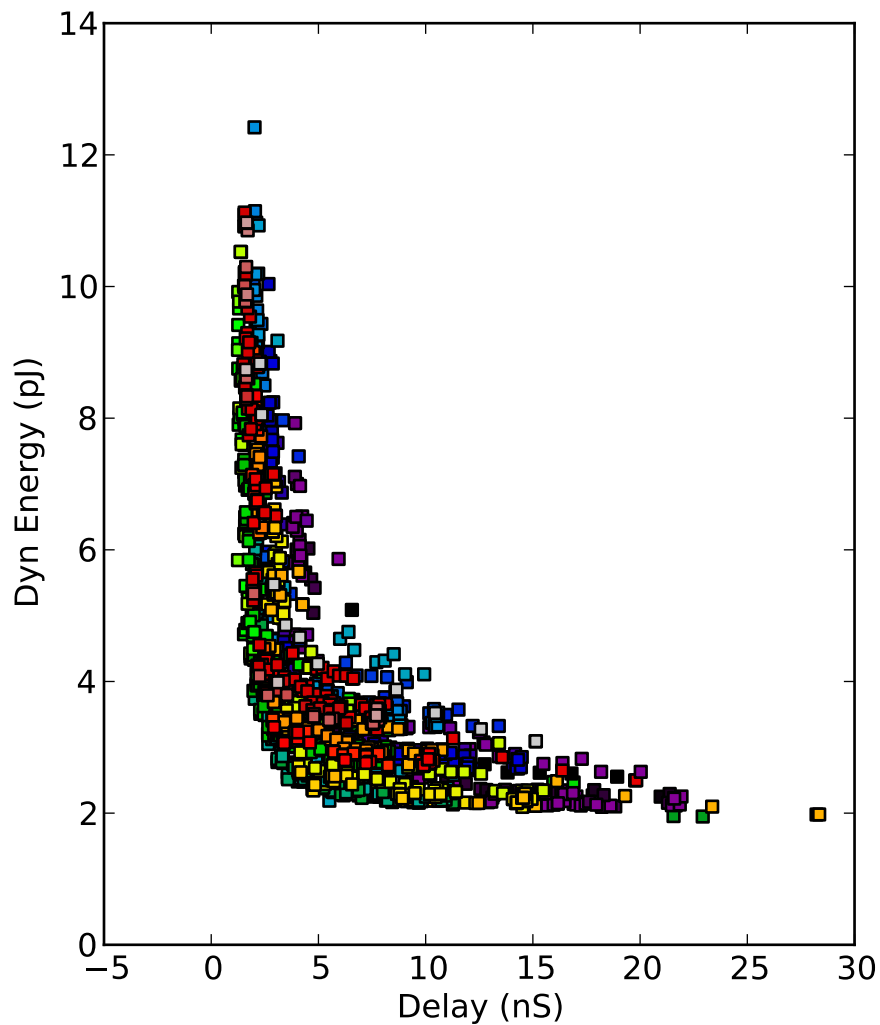
Design Index





Design Index

FMA-32-All



Design Index



FMA-32-All

