MultiplierP-128

March 25, 2012

1 Design Map

Tables	Tables are tables					
	INFO_Pareto TOP.DesignIndex	0	0 1	$_{2}^{0}$	0 3	
	TOP.VT TOP.Voltage	svt 0.8	svt 0.8	svt 0.8	svt 0.8	
	TOP.top_MultiplierP.MultiplierP.BoothType TOP.top_MultiplierP.MultiplierP.Designware_MODE	1 OFF	OFF	1 OFF	OFF	
	TOP.top_MultiplierP.MultiplierP.TreeType	Array	OS1	Wallace	ZM	
-	TOP.top_MultiplierP.MultiplierP.Width	128	128	128	128	
-	INFO_Pareto	1	1	1	1	
	TOP.DesignIndex	4	5	6	7	
	TOP.VT TOP.Voltage	svt 0.8	svt 0.8	$_{0.8}^{\mathrm{svt}}$	0.8	
	TOP. vortage TOP.top_MultiplierP.MultiplierP.BoothType	0.8	0.8	2	0.8	
	TOP.top_MultiplierP.MultiplierP.Designware_MODE	OFF	OFF	OFF	OFF	
	TOP.top_MultiplierP.MultiplierP.TreeType	Array	OS1	Wallace	ZM	
-	TOP.top_MultiplierP.MultiplierP.Width	128	128	128	128	
-	NIDO D					
	INFO_Pareto TOP.DesignIndex	1 8	9	1 10	1 11	
	TOP.VT	svt	svt	svt	svt	
	TOP.Voltage	0.8	0.8	0.8	0.8	
	TOP.top_MultiplierP.MultiplierP.BoothType	3	3	3	3	
	TOP.top_MultiplierP.MultiplierP.Designware_MODE	OFF	OFF	OFF	OFF	
	TOP.top_MultiplierP.MultiplierP.TreeType	Array	OS1	Wallace	ZM	
-	TOP.top_MultiplierP.MultiplierP.Width	128	128	128	128	
-	INFO_Pareto	0	1 0	1	0	
	TOP.DesignIndex	12	13	14	15	
	TOP.VT	svt	svt	svt	svt	
	TOP.Voltage	0.8	0.8	0.8	0.9	
	TOP.top_MultiplierP.MultiplierP.BoothType	4	4	NA	1	
	TOP.top_MultiplierP.MultiplierP.Designware_MODE TOP.top_MultiplierP.MultiplierP.TreeType	OFF	OFF Wallace	ON NA	OFF	
	TOP.top_MultiplierP.MultiplierP.TreeType TOP.top_MultiplierP.MultiplierP.Width	Array 128	128	128	Array 128	
-						
	INFO_Pareto	0	0	0	0	
	TOP.DesignIndex TOP.VT	16 svt	17 svt	18 svt	19 svt	
	TOP.Voltage	0.9	0.9	0.9	0.9	
	TOP.top_MultiplierP.MultiplierP.BoothType	1 1	1	1	2	
	TOP.top_MultiplierP.MultiplierP.Designware_MODE	OFF	OFF	OFF	OFF	
	${\bf TOP.top_MultiplierP.MultiplierP.TreeType}$	OS1	Wallace	ZM	Array	
-	TOP.top_MultiplierP.MultiplierP.Width	128	128	128	128	
		11				

INFO_Pareto TOP.DesignIndex TOP.VT TOP.Voltage TOP.top_MultiplierP.MultiplierP.BoothType TOP.top_MultiplierP.MultiplierP.Designware_MODE TOP.top_MultiplierP.MultiplierP.TreeType TOP.top_MultiplierP.MultiplierP.Width	1 1 1 0 20 21 22 23 svt svt svt svt 0.9 0.9 0.9 0.9 2 2 2 3 OFF OFF OFF OFF OS1 Wallace ZM Array 128 128 128 128
INFO_Pareto TOP.DesignIndex TOP.VT TOP.Voltage TOP.top_MultiplierP.MultiplierP.BoothType TOP.top_MultiplierP.MultiplierP.Designware_MODE TOP.top_MultiplierP.MultiplierP.TreeType TOP.top_MultiplierP.MultiplierP.Width	1 1 1 0 24 25 26 27 svt svt svt svt 0.9 0.9 0.9 0.9 3 3 4 OFF OFF OFF OS1 Wallace ZM Array 128 128 128 128
INFO_Pareto TOP.DesignIndex TOP.VT TOP.Voltage TOP.top_MultiplierP.MultiplierP.BoothType TOP.top_MultiplierP.MultiplierP.Designware_MODE TOP.top_MultiplierP.MultiplierP.TreeType TOP.top_MultiplierP.MultiplierP.Width	0 1 0 0 28 29 30 31 svt svt svt svt 0.9 0.9 1.0 1.0 4 NA 1 1 OFF ON OFF OFF Wallace NA Array OS1 128 128 128 128
INFO_Pareto TOP.DesignIndex TOP.VT TOP.Voltage TOP.top_MultiplierP.MultiplierP.BoothType TOP.top_MultiplierP.MultiplierP.Designware_MODE TOP.top_MultiplierP.MultiplierP.TreeType TOP.top_MultiplierP.MultiplierP.Width	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$
INFO.Pareto TOP.DesignIndex TOP.VT TOP.Voltage TOP.top_MultiplierP.MultiplierP.BoothType TOP.top_MultiplierP.MultiplierP.Designware_MODE TOP.top_MultiplierP.MultiplierP.TreeType TOP.top_MultiplierP.MultiplierP.Width	1 0 0 1 36 37 38 39 svt svt svt svt 1.0 1.0 1.0 1.0 2 2 3 3 OFF OFF OFF OFF Wallace ZM Array OS1 128 128 128 128
INFO_Pareto TOP.DesignIndex TOP.VT TOP.Voltage TOP.top_MultiplierP.MultiplierP.BoothType TOP.top_MultiplierP.MultiplierP.Designware_MODE TOP.top_MultiplierP.MultiplierP.TreeType TOP.top_MultiplierP.MultiplierP.Width	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$
INFO_Pareto TOP.DesignIndex TOP.VT TOP.Voltage TOP.top_MultiplierP.MultiplierP.BoothType TOP.top_MultiplierP.MultiplierP.Designware_MODE TOP.top_MultiplierP.MultiplierP.TreeType TOP.top_MultiplierP.MultiplierP.Width	1 44 svt 1.0 NA ON NA 128























