MultiplierP-64

March 25, 2012

1 Design Map

Tables are tables

INFO_Pareto	0	0	0	0	
TOP.DesignIndex	0	1	2	3	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	0.8	0.8	0.8	0.8	
TOP.top_MultiplierP.MultiplierP.BoothType	1	1	1	1	
TOP.top_MultiplierP.MultiplierP.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_MultiplierP.MultiplierP.TreeType	Array	OS1	Wallace	ZM	
TOP.top_MultiplierP.MultiplierP.Width	64	64	64	64	

INFO_Pareto	1	1	1	1	
TOP.DesignIndex	4	5	6	7	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	0.8	0.8	0.8	0.8	
TOP.top_MultiplierP.MultiplierP.BoothType	2	2	2	2	
TOP.top_MultiplierP.MultiplierP.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_MultiplierP.MultiplierP.TreeType	Array	OS1	Wallace	ZM	
TOP.top_MultiplierP.MultiplierP.Width	64	64	64	64	

INFO_Pareto	1	1	1	1	
TOP.DesignIndex	8	9	10	11	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	0.8	0.8	0.8	0.8	
TOP.top_MultiplierP.MultiplierP.BoothType	3	3	3	3	
TOP.top_MultiplierP.MultiplierP.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_MultiplierP.MultiplierP.TreeType	Array	OS1	Wallace	ZM	
TOP.top_MultiplierP.MultiplierP.Width	64	64	64	64	

INFO_Pareto	0	0	1	0	
TOP.DesignIndex	12	13	14	15	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	0.8	0.8	0.8	0.9	
TOP.top_MultiplierP.MultiplierP.BoothType	4	4	NA	1	
TOP.top_MultiplierP.MultiplierP.Designware_MODE	OFF	OFF	ON	OFF	
TOP.top_MultiplierP.MultiplierP.TreeType	Array	Wallace	NA	Array	
TOP.top_MultiplierP.MultiplierP.Width	64	64	64	64	

INFO_Pareto	0	0	0	0	
TOP.DesignIndex	16	17	18	19	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	0.9	0.9	0.9	0.9	
TOP.top_MultiplierP.MultiplierP.BoothType	1	1	1	2	
TOP.top_MultiplierP.MultiplierP.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_MultiplierP.MultiplierP.TreeType	OS1	Wallace	ZM	Array	
TOP.top_MultiplierP.MultiplierP.Width	64	64	64	64	

INFO_Pareto	1	1	1	0	
TOP.DesignIndex	20	21	22	23	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	0.9	0.9	0.9	0.9	
TOP.top_MultiplierP.MultiplierP.BoothType	2	2	2	3	
TOP.top_MultiplierP.MultiplierP.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_MultiplierP.MultiplierP.TreeType	OS1	Wallace	ZM	Array	
TOP.top_MultiplierP.MultiplierP.Width	64	64	64	64	

INFO_Pareto	1	1	1	0	
TOP.DesignIndex	24	25	26	27	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	0.9	0.9	0.9	0.9	
TOP.top_MultiplierP.MultiplierP.BoothType	3	3	3	4	
TOP.top_MultiplierP.MultiplierP.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_MultiplierP.MultiplierP.TreeType	OS1	Wallace	ZM	Array	
TOP.top_MultiplierP.MultiplierP.Width	64	64	64	64	

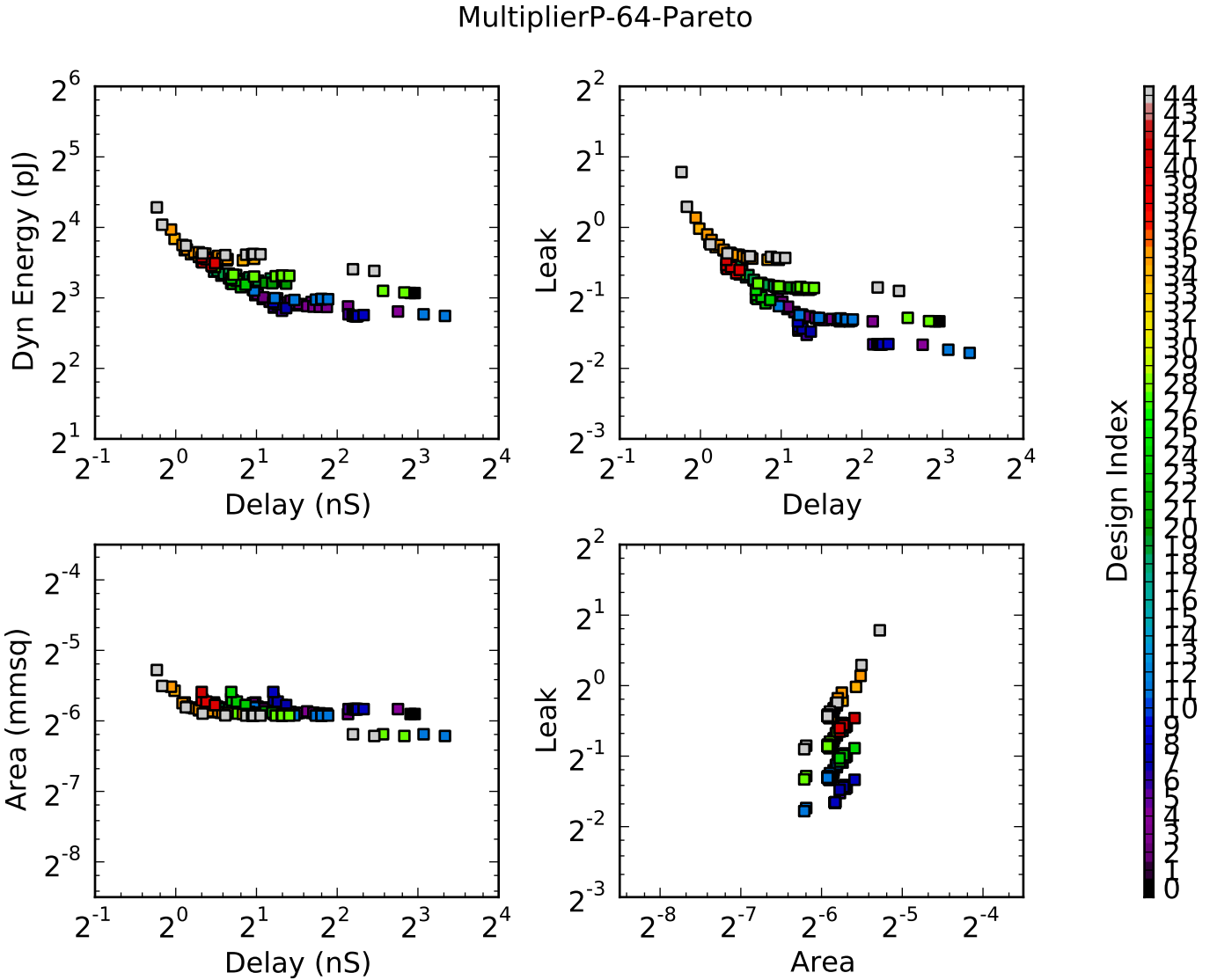
INFO_Pareto	0	1	0	0	
TOP.DesignIndex	28	29	30	31	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	0.9	0.9	1.0	1.0	
TOP.top_MultiplierP.MultiplierP.BoothType	4	NA	1	1	
TOP.top_MultiplierP.MultiplierP.Designware_MODE	OFF	ON	OFF	OFF	
TOP.top_MultiplierP.MultiplierP.TreeType	Wallace	NA	Array	OS1	
TOP.top_MultiplierP.MultiplierP.Width	64	64	64	64	

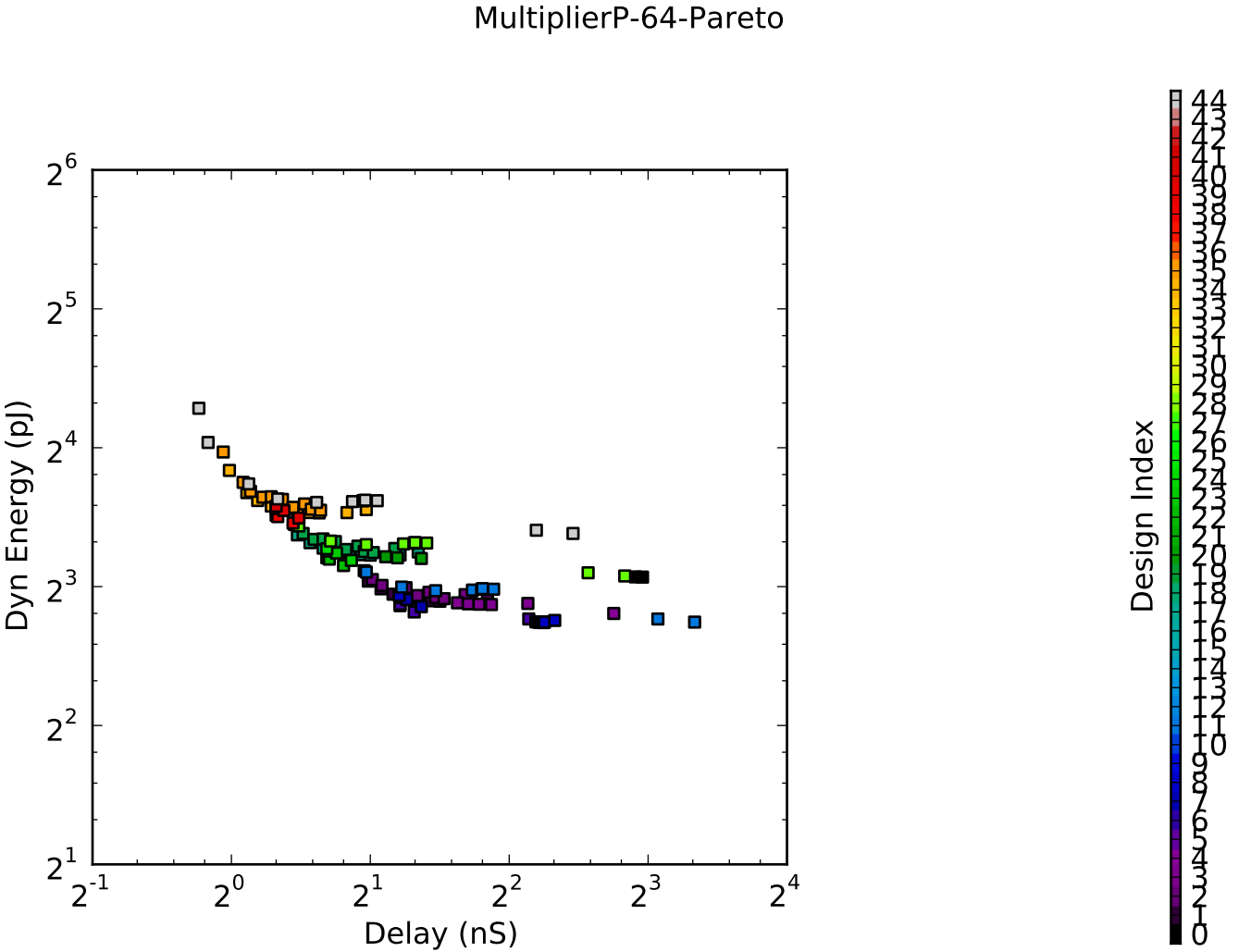
INFO_Pareto	0	0	0	1	
TOP.DesignIndex	32	33	34	35	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	1.0	1.0	1.0	1.0	
TOP.top_MultiplierP.MultiplierP.BoothType	1	1	2	2	
TOP.top_MultiplierP.MultiplierP.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_MultiplierP.MultiplierP.TreeType	Wallace	ZM	Array	OS1	
TOP.top_MultiplierP.MultiplierP.Width	64	64	64	64	

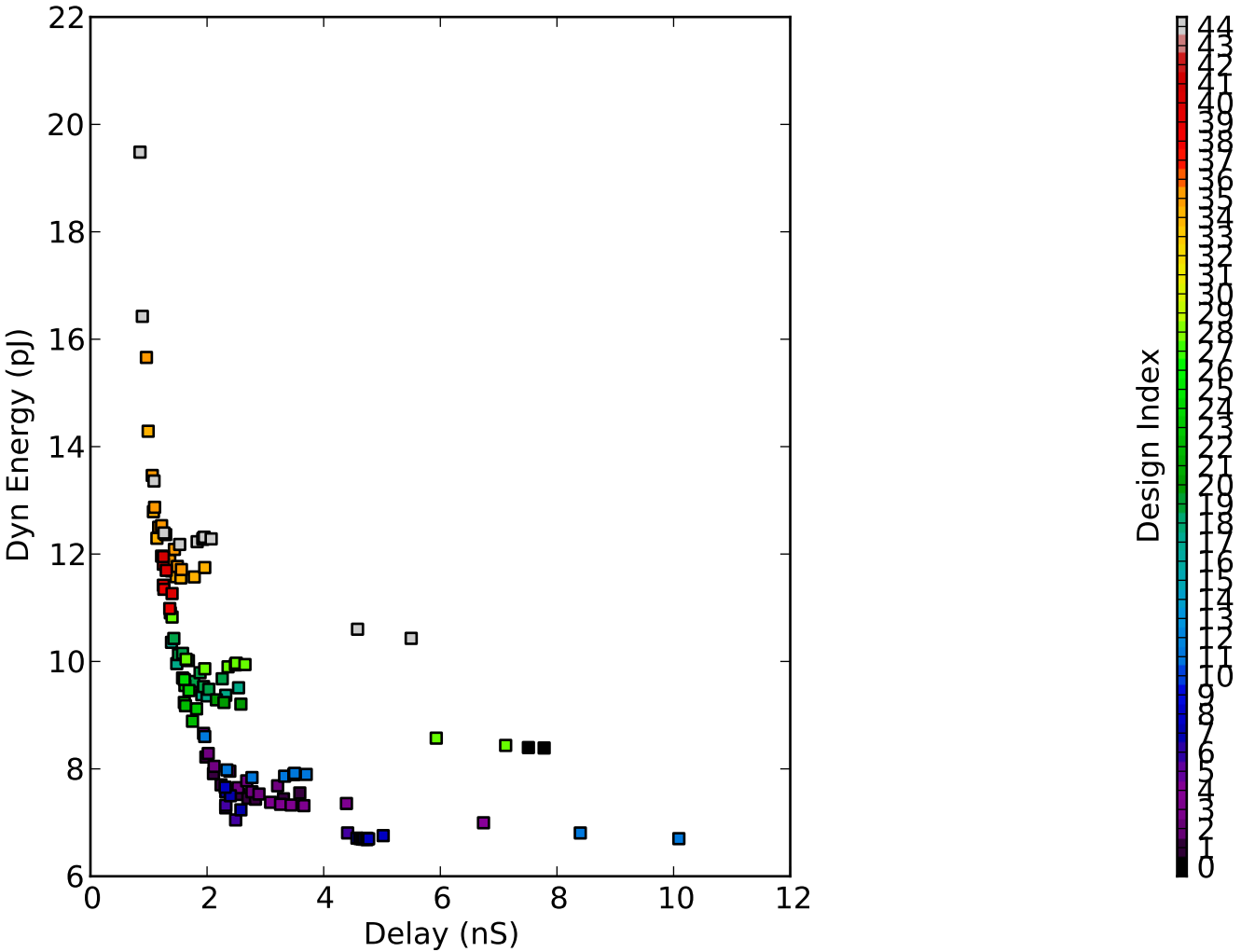
INFO_Pareto	1	0	0	1	
TOP.DesignIndex	36	37	38	39	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	1.0	1.0	1.0	1.0	
TOP.top_MultiplierP.MultiplierP.BoothType	2	2	3	3	
TOP.top_MultiplierP.MultiplierP.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_MultiplierP.MultiplierP.TreeType	Wallace	ZM	Array	OS1	
TOP.top_MultiplierP.MultiplierP.Width	64	64	64	64	

INFO_Pareto	1	1	0	0	
TOP.DesignIndex	40	41	42	43	
TOP.VT	svt	svt	svt	svt	
TOP.Voltage	1.0	1.0	1.0	1.0	
TOP.top_MultiplierP.MultiplierP.BoothType	3	3	4	4	
TOP.top_MultiplierP.MultiplierP.Designware_MODE	OFF	OFF	OFF	OFF	
TOP.top_MultiplierP.MultiplierP.TreeType	Wallace	ZM	Array	Wallace	
TOP.top_MultiplierP.MultiplierP.Width	64	64	64	64	

INFO_Pareto	1	
TOP.DesignIndex	44	
TOP.VT	svt	
TOP.Voltage	1.0	
TOP.top_MultiplierP.MultiplierP.BoothType	NA	
TOP.top_MultiplierP.MultiplierP.Designware_MODE	ON	
TOP.top_MultiplierP.MultiplierP.TreeType	NA	
TOP.top_MultiplierP.MultiplierP.Width	64	


MultiplierP-64-Pareto
Dyn Energy (pJ)
Delay (nS)
Leak
Delay
Area (mmsq)
Delay (nS)
Leak
Area
Design Index


MultiplierP-64-Pareto
Dyn Energy (pJ)
Delay (nS)
Design Index


MultiplierP-64-Pareto
Dyn Energy (pJ)
Delay (nS)
Design Index
22
20
18
16
14
12
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8
6
0
2
4
6
8
10
12
44
43
42
41
40
39
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36
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17
16
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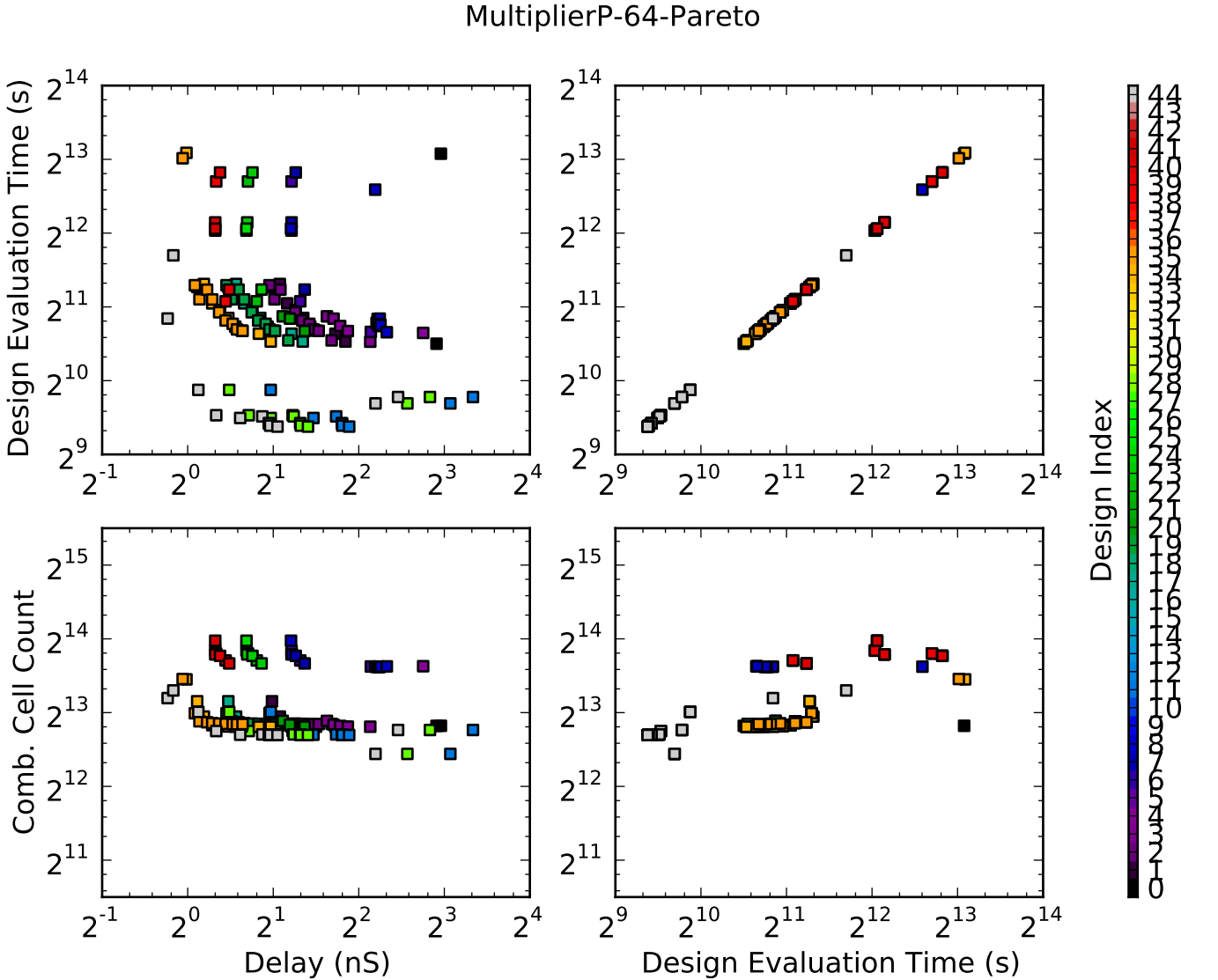
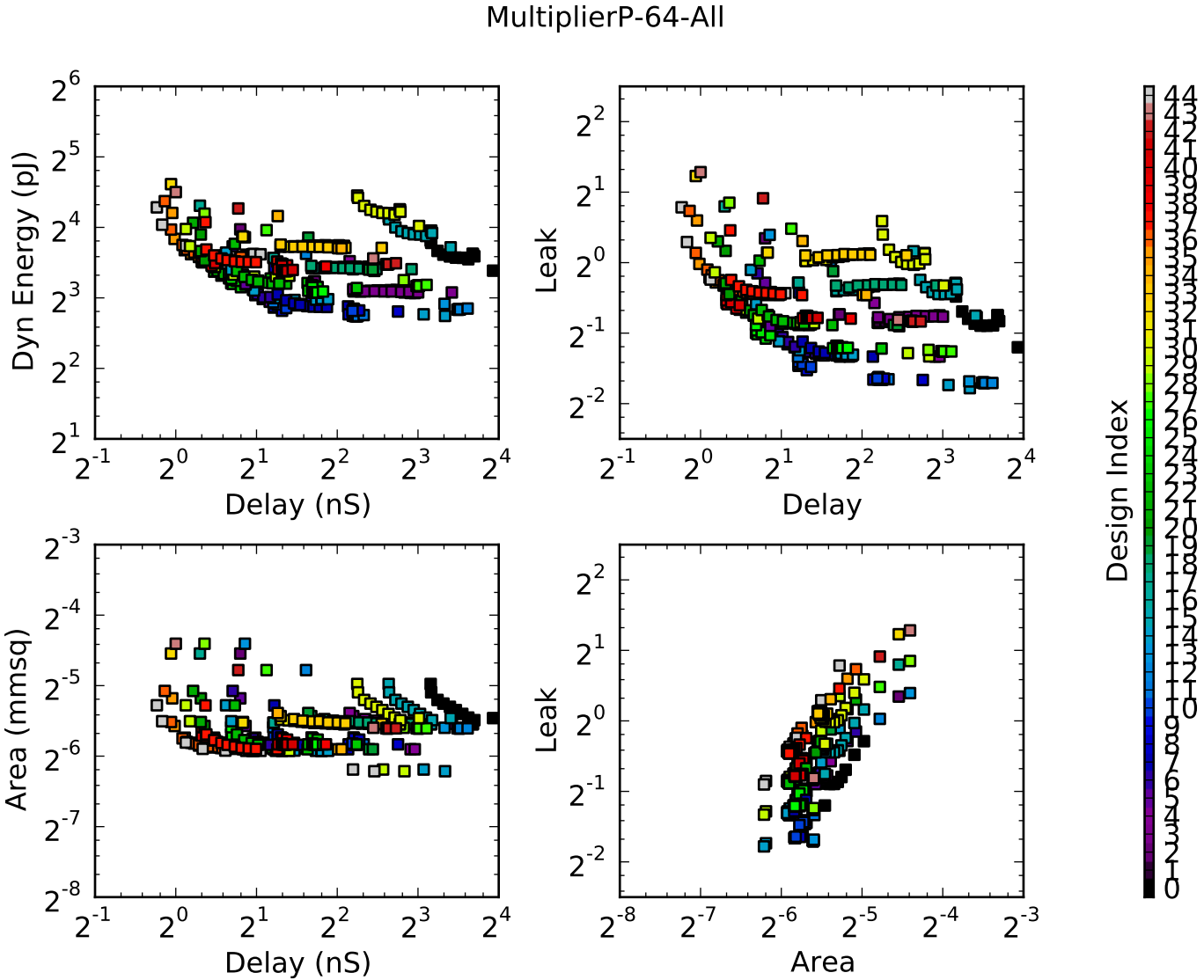
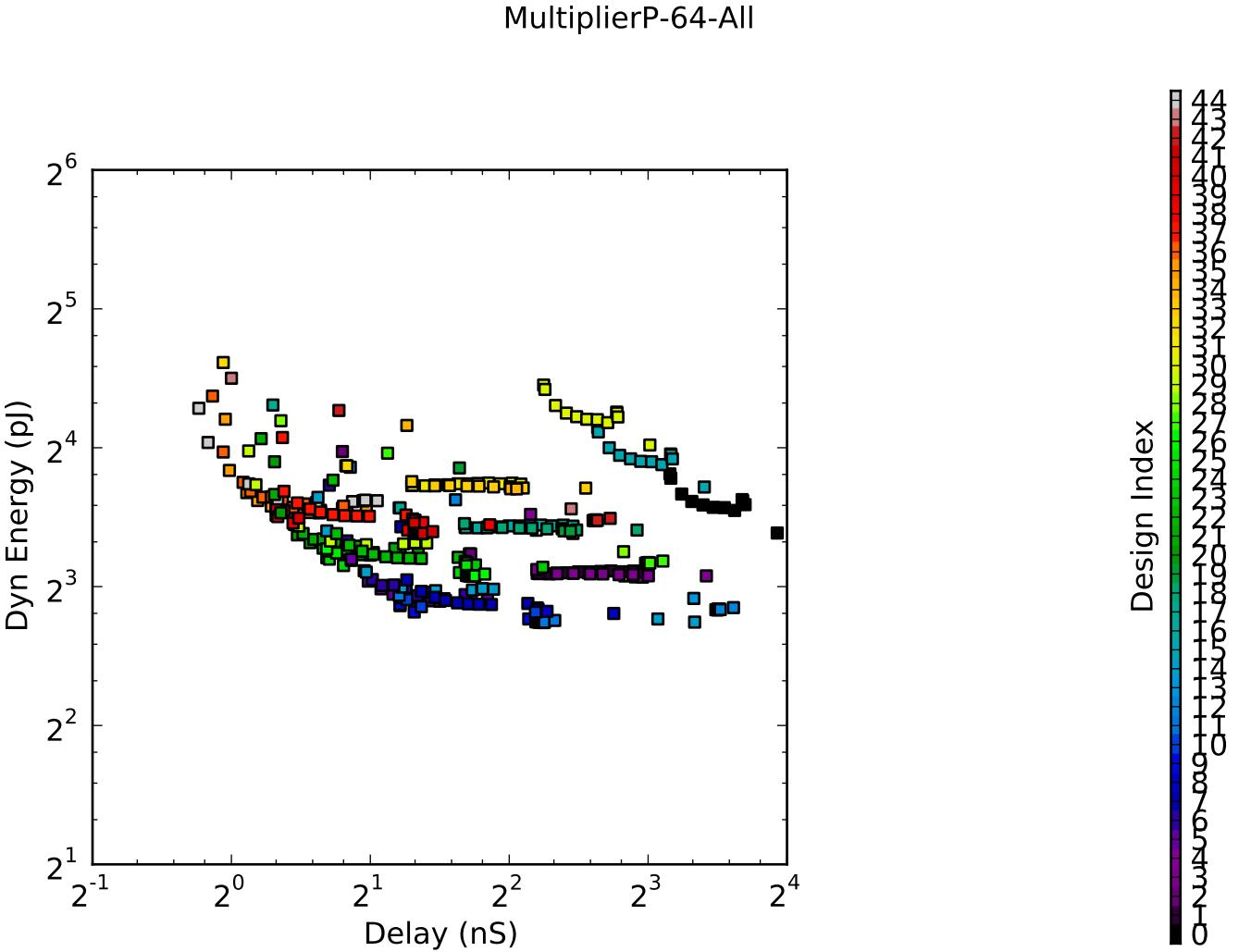
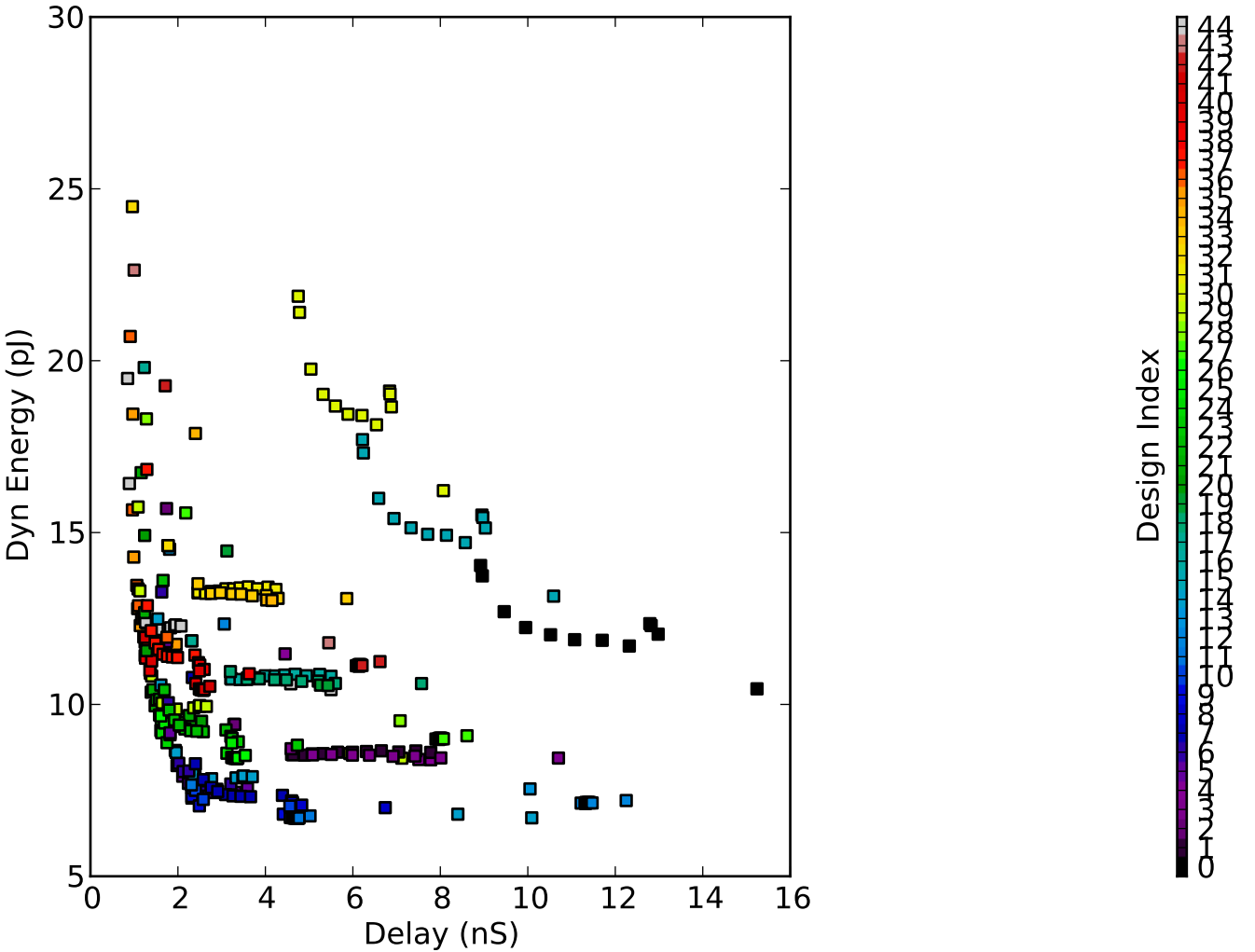
6
5
4
3
2
1
0


MultiplierP-64-Pareto
Design Evaluation Time (s)
Comb. Cell Count
Delay (nS)
Design Evaluation Time (s)
Design Index


MultiplierP-64-All
Dyn Energy (pJ)
Delay (nS)
Leak
Delay
Area (mmsq)
Delay (nS)
Leak
Area
Design Index


MultiplierP-64-All
Dyn Energy (pJ)
Delay (nS)
Design Index


MultiplierP-64-All
Dyn Energy (pJ)
Delay (nS)
Design Index
30
25
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16
44
43
42
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16
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12
11
10
9
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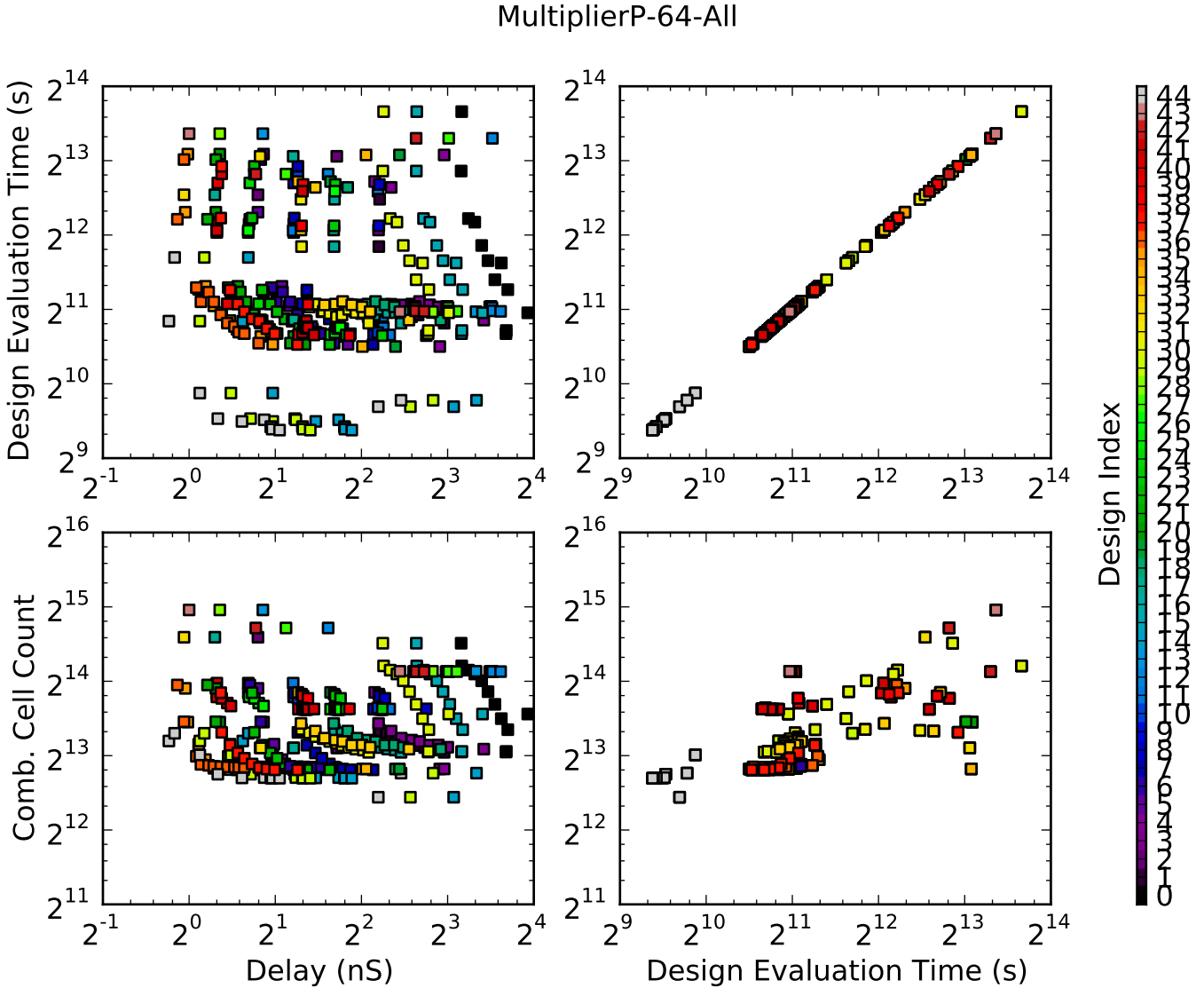
6
5
4
3
2
1
0


MultiplierP-64-All
Design Evaluation Time (s)
Comb. Cell Count
Delay (nS)
Design Evaluation Time (s)
Design Index