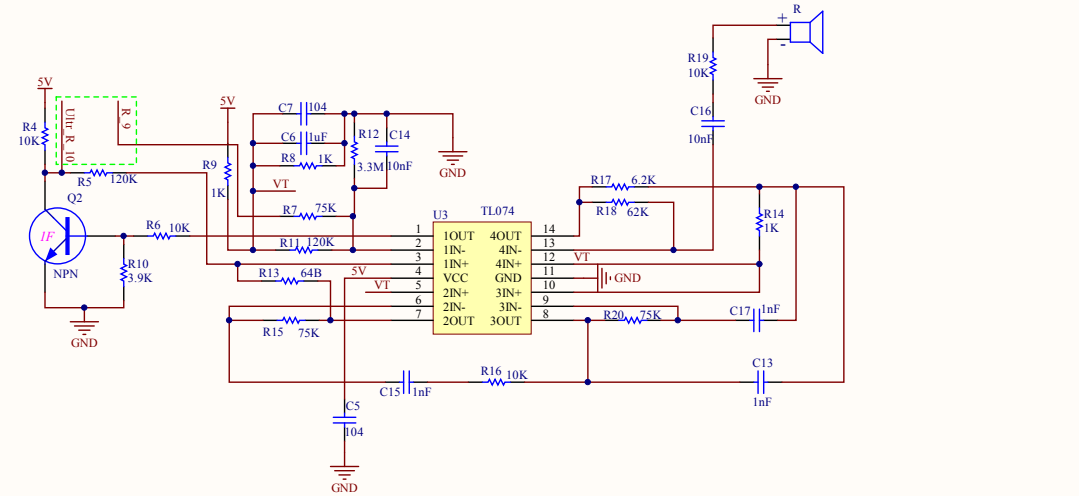


## D



D

The circuit diagram shows a 5V NPN transistor amplifier. The input signal is applied to the base of the NPN transistor (Q2) through a 10kΩ resistor (R4). The base is biased by a 5V supply through a 10kΩ resistor (R5) and a 10kΩ resistor (R6). The emitter is connected to ground through a 3.9kΩ resistor (R10). The collector is connected to a 5V supply through a 10kΩ resistor (R9) and a 10kΩ resistor (R11). The output of the transistor is connected to the non-inverting input (pin 1) of the TL074 op-amp. The op-amp is configured as a voltage follower, with its output (pin 14) connected to its non-inverting input (pin 1). The op-amp is powered by a 5V supply (pin 4) and ground (pin 11). The output of the op-amp is connected to a 10kΩ resistor (R16) and a 10kΩ resistor (R17). The output is also connected to a 10kΩ resistor (R19) and a 10kΩ resistor (R20). The output is connected to a speaker through a 10kΩ resistor (R19) and a 10kΩ resistor (R20).

[illegible][illegible]