Functional Electronic Circuits Lab6

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My variant:

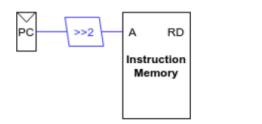
_	1
1.6	cuh
U	Sub

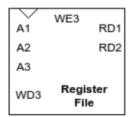
It means Subtract rs1 from rs2 and write to rd.

31	30	25 24	21 2	0 19 1	5 14	12 11	8 7	6)
0100	0000		rs2	rsl	000		rd	0110011	sub

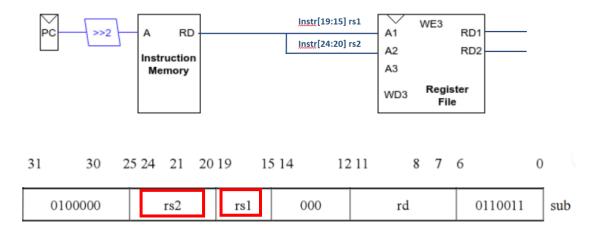
The pictures and explanation of the provided instruction execution:

1. PC moves two bits to the right.

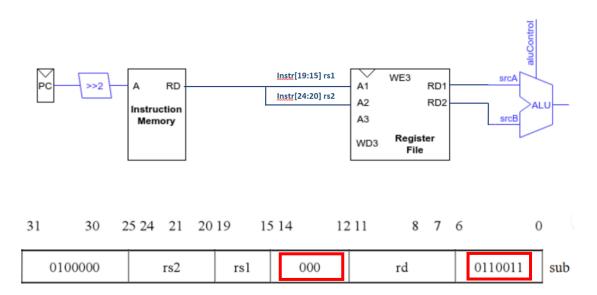




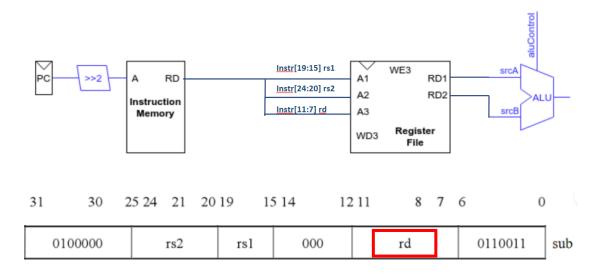
2. Input rs1 and rs2 from instruction memory into A1 and A2 in register file respectively.



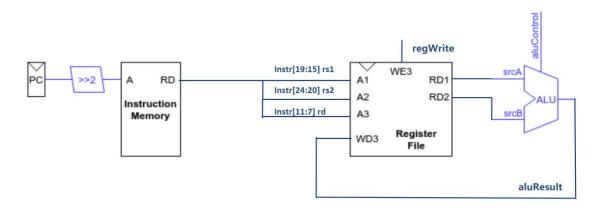
3. Input srcA and srcB from RD1 and RD2 into ALU for computation.



4. Input rd from instruction memory to A3 in register file.



5. Input the result of ALU into WD3 in register file and rewrite it.



31	30	25 24	21	20	19 1	15	14	12	11	8	7	6		0	
0100	000		rs2		rsl		000			rd			0110011		sub

6. PC plus 4 and then go to the next instruction.

