



Timing diagram for a 14-day delay element. The diagram shows an input signal 'In' and an output signal 'Out'. The input signal is a pulse that starts at time t_1 , rises to a high level, and returns to a low level at time $t_1 + 14 \text{ days}$. The output signal is a pulse that starts at time $t_1 + 14 \text{ days}$, rises to a high level, and returns to a low level at time $t_1 + 28 \text{ days}$. The output signal is delayed by 14 days relative to the input signal.





