

A

A

B

B

C

C

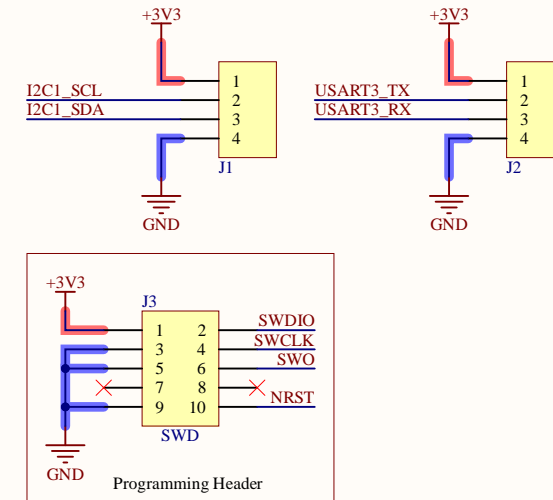
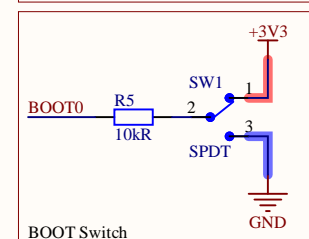
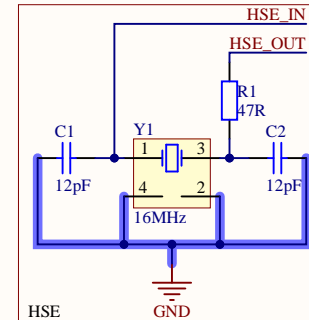
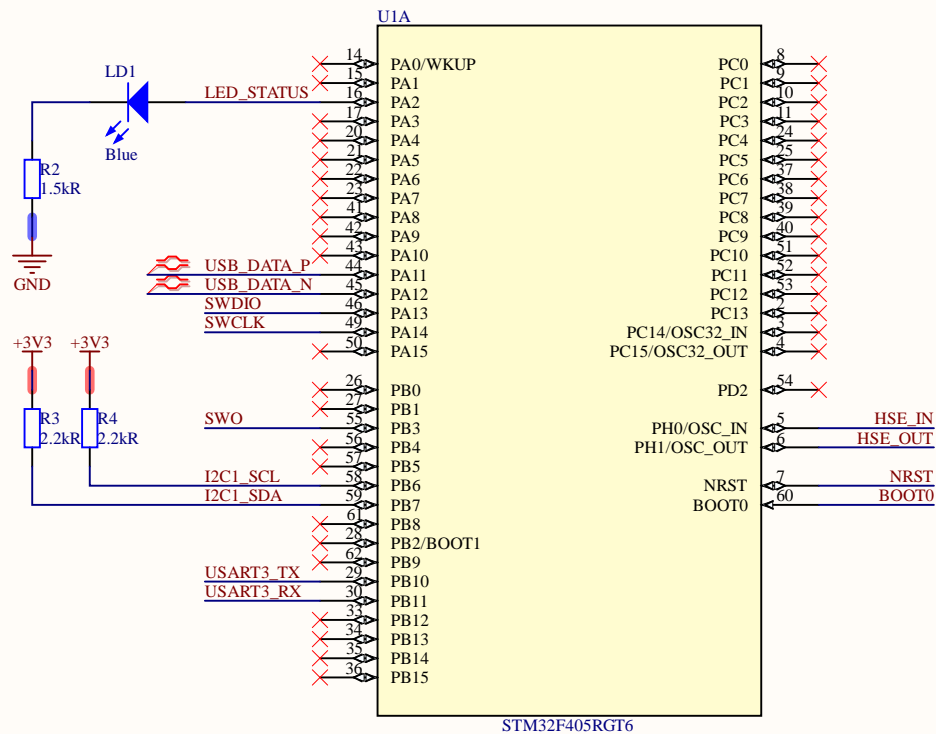
D

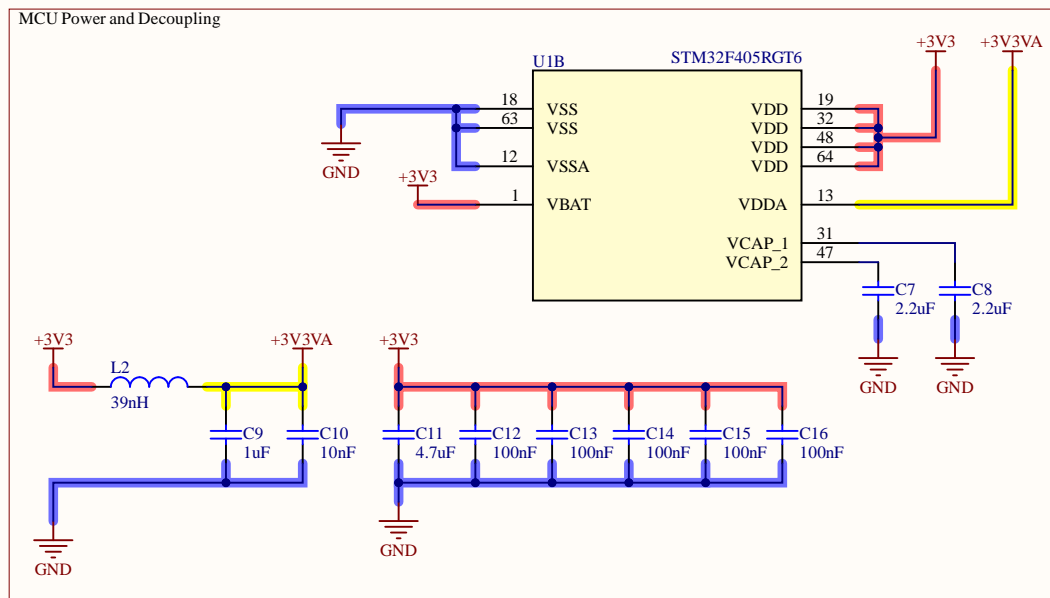
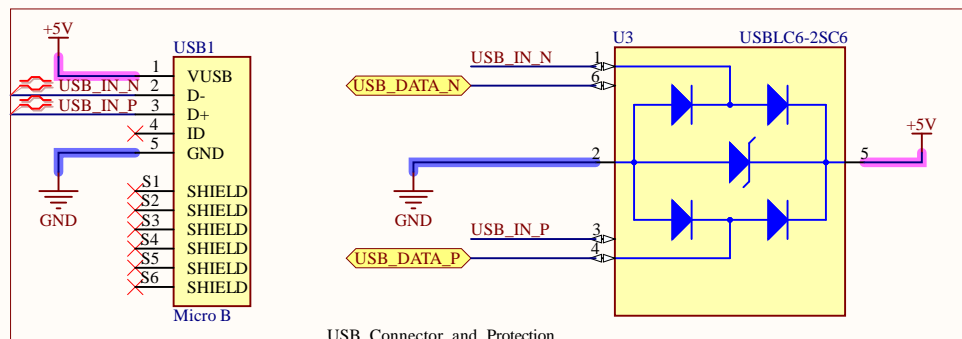
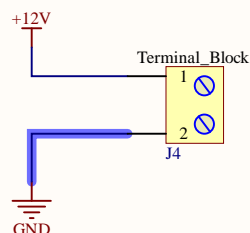
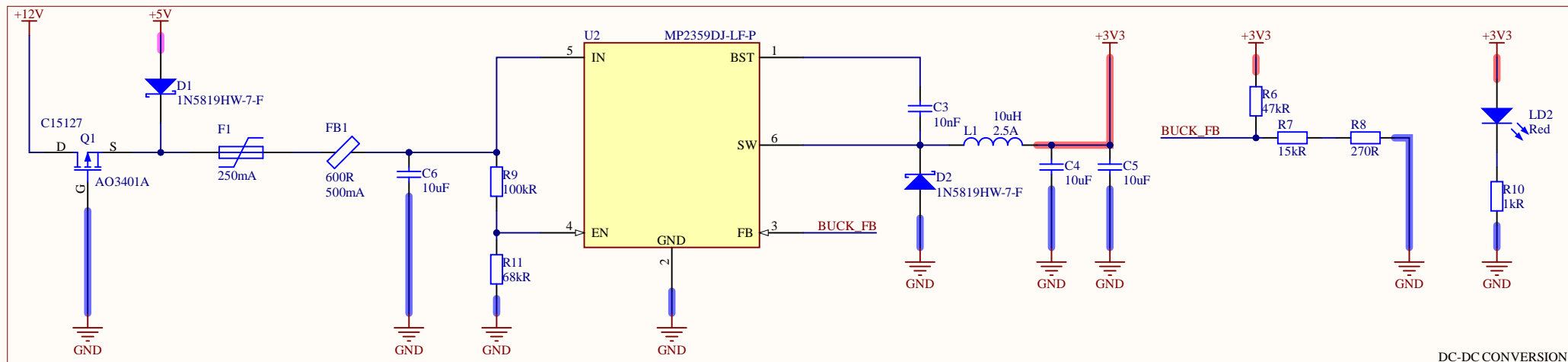
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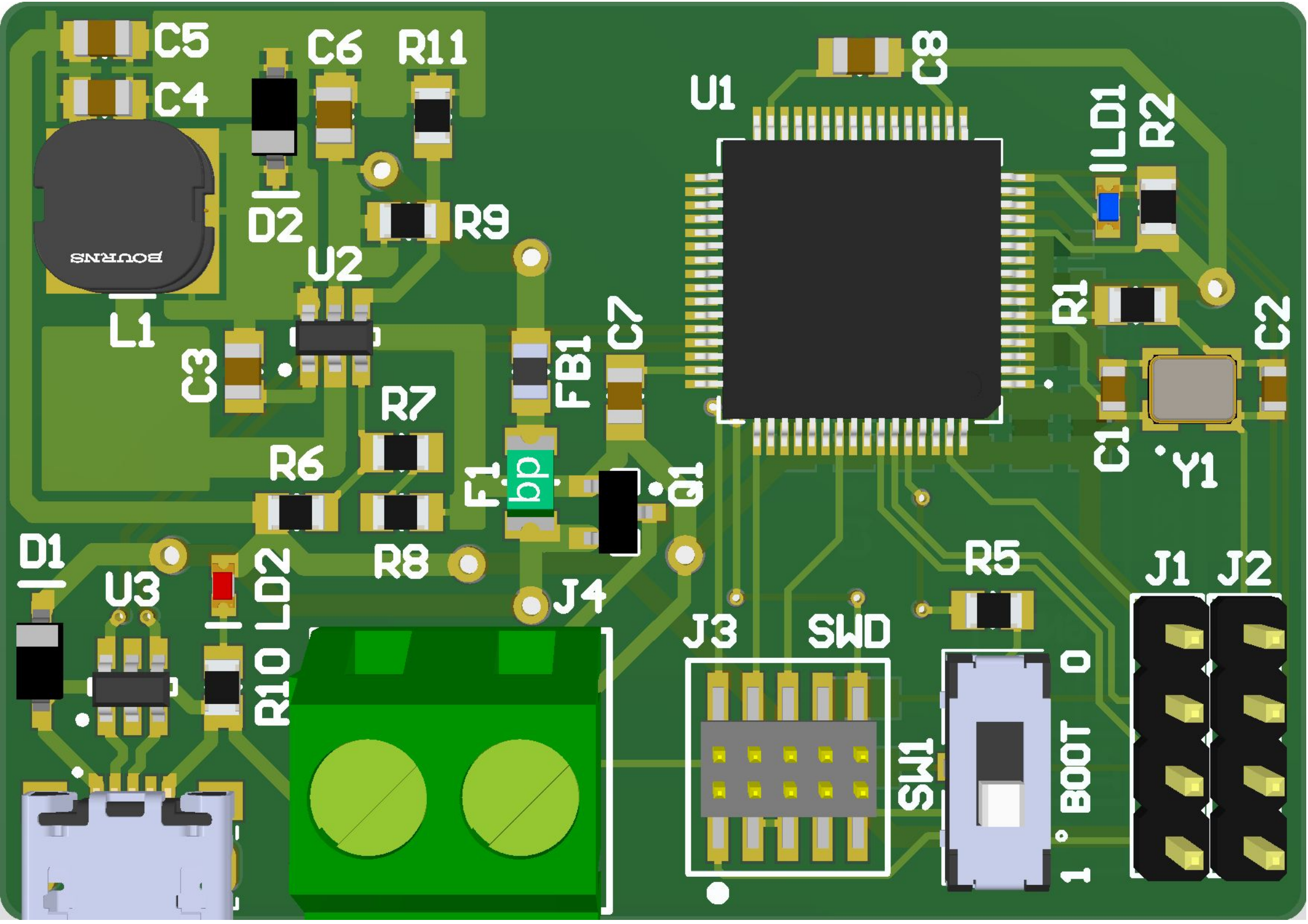
STM32F\_Board\_Schematic\_PowerAndUSB

USB\_DATA\_P  
USB\_DATA\_N

USB\_DATA\_N  
USB\_DATA\_P









				USART
				I2C
				GND
				Rx
				SDA
				Tx
				SCL
				+3V3

Ass  
 Prog  
 QCpass

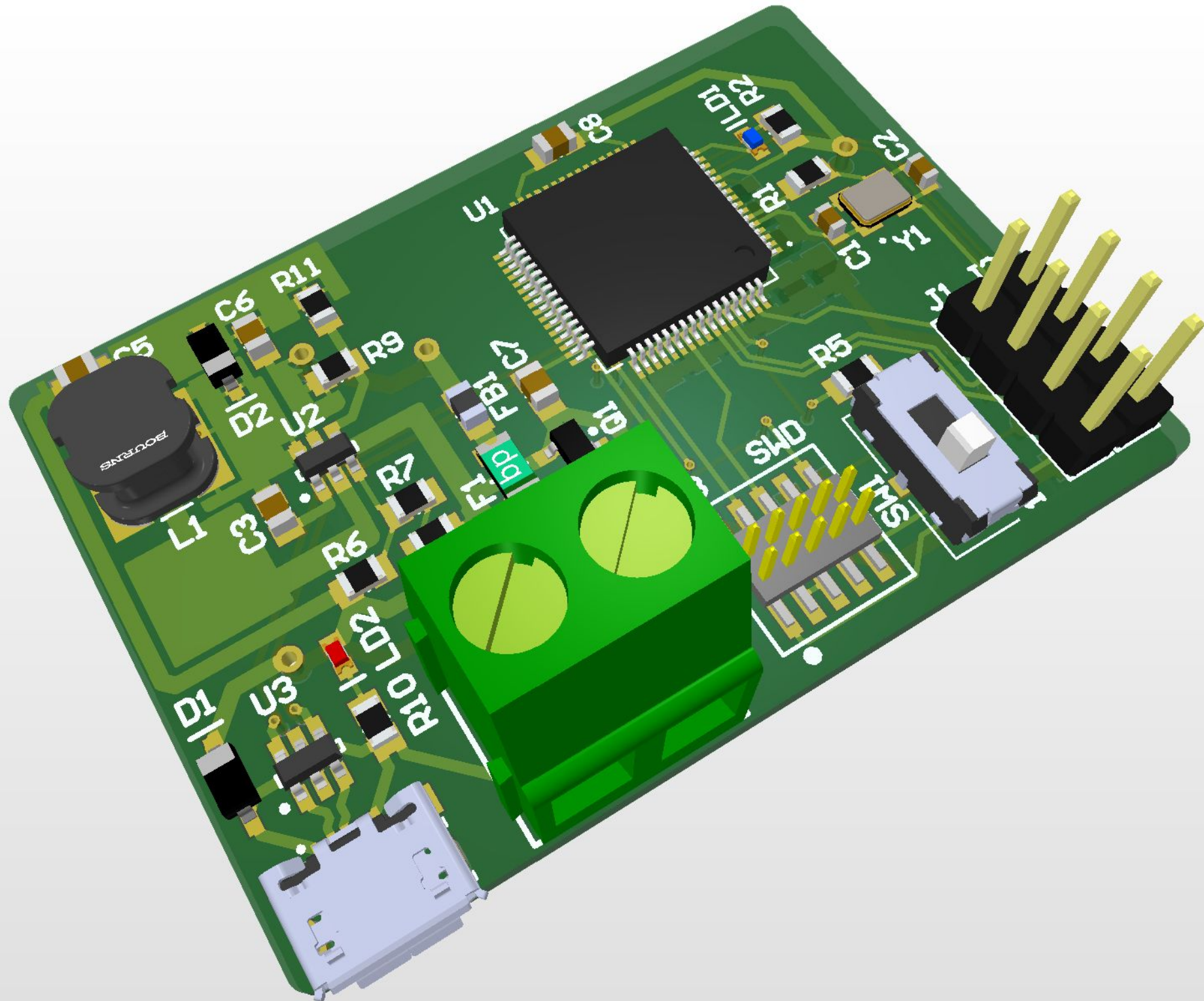
C9 C10  
 C12 L2  
 C14  
 C13  
 C15  
 C16  
 C11  
 R4 R3

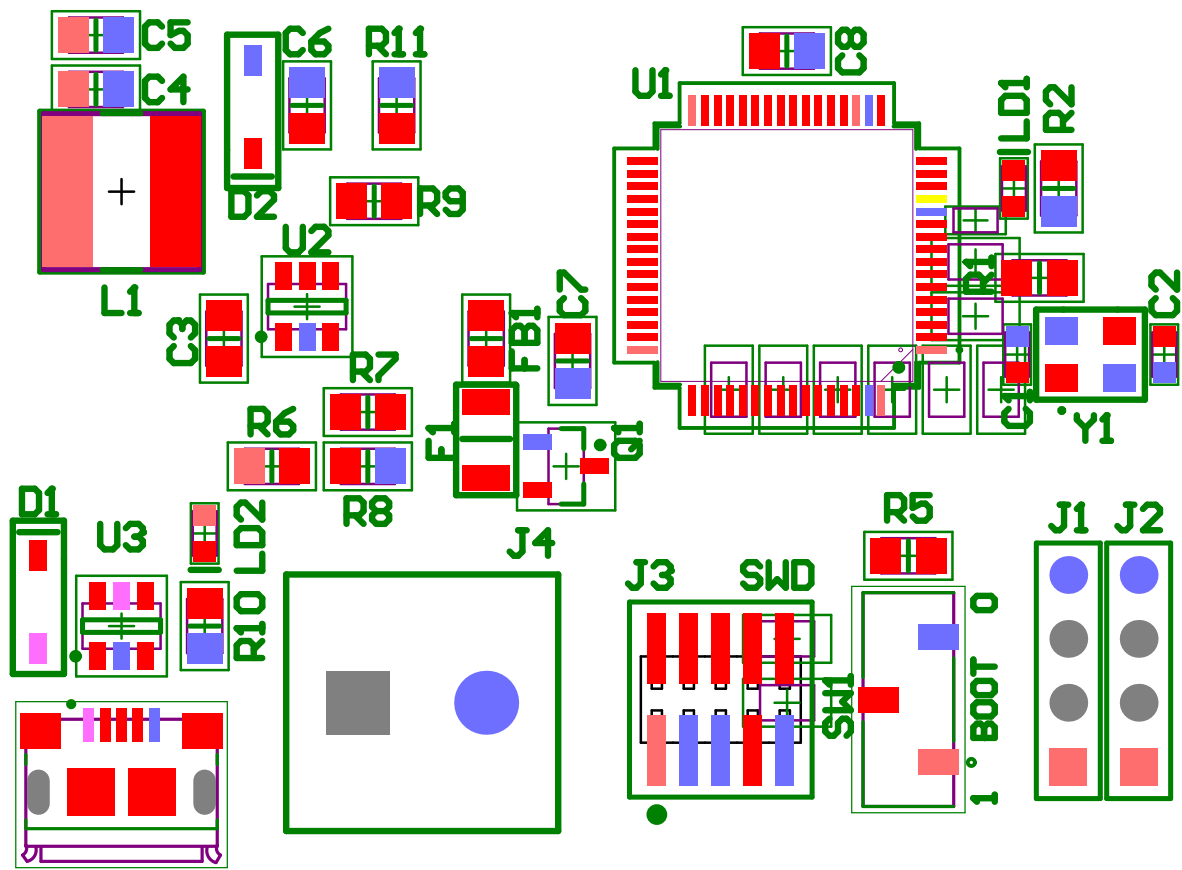
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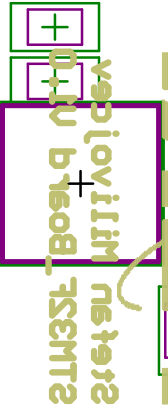
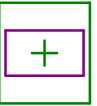
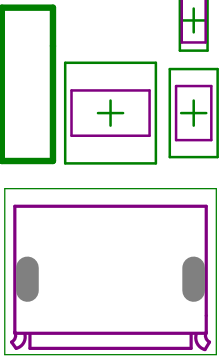
+12V

# FERRI

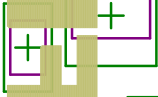
Stefan Milivojcev  
 STM32F\_Board V1.0







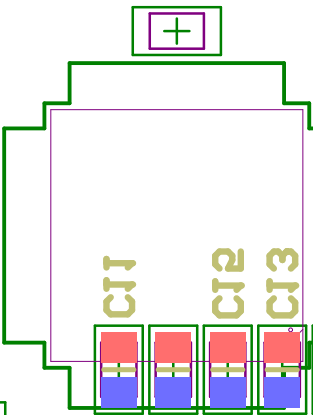
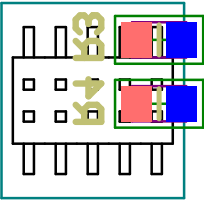
21135L - B0919 1110  
21919 111019



END



+15V



C11

C1e

C12

C13

C14

C15



END

SDA

SCL

+3V3

CS1

TRASP

Q100

LS

22A  
p019  
226Q00