**Cond 000000 A S Rd Rn Rs 1001 Rm**

1010 000000 0 1 1110 0111 0000 1001 1000

**Cond 01 I P U 00 L Rn Rd Offset**

1010 01 0 0 0 00 1 1110 0111 000001111000 instr5

**Cond 00 I Opc S Rn Rd Operand2**

1010 00 0 0000 1 1110 0111 000001111000 instr4

1010 00 0 0000 1 1110 0111 000001101000 instr3

1010 00 1 0000 1 1110 0111 000001111000 instr2

1010 00 0 0110 1 1110 0111 000001111000 instr1