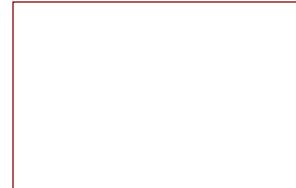


Clock and Reset

File: Clock_and_Reset.kicad_sch

Program Counter

File: Program_Counter.kicad_sch

Address Register

File: Adress_Register.kicad_sch

Stack Pointer

File: Stack_Pointer.kicad_sch

A

A

Registers and DB Driver

File: Registers_and_DB_Driver.kicad_sch

Micro Code Sequencer

File: Micro_Code_Sequencer.kicad_sch

Instruction Decoder

File: Instruction_Decoder.kicad_sch

Exception Handling

File: Exception_Handling.kicad_sch

B

B

ALU

File: ALU.kicad_sch

Status Register

File: Status_Register.kicad_sch

Special Conditions

File: Special_Conditions.kicad_sch

Processor Port

File: Processor_Port.kicad_sch

C

C

External Memory

File: External_Memory.kicad_sch

Miscellaneous

File: Miscellaneous.kicad_sch

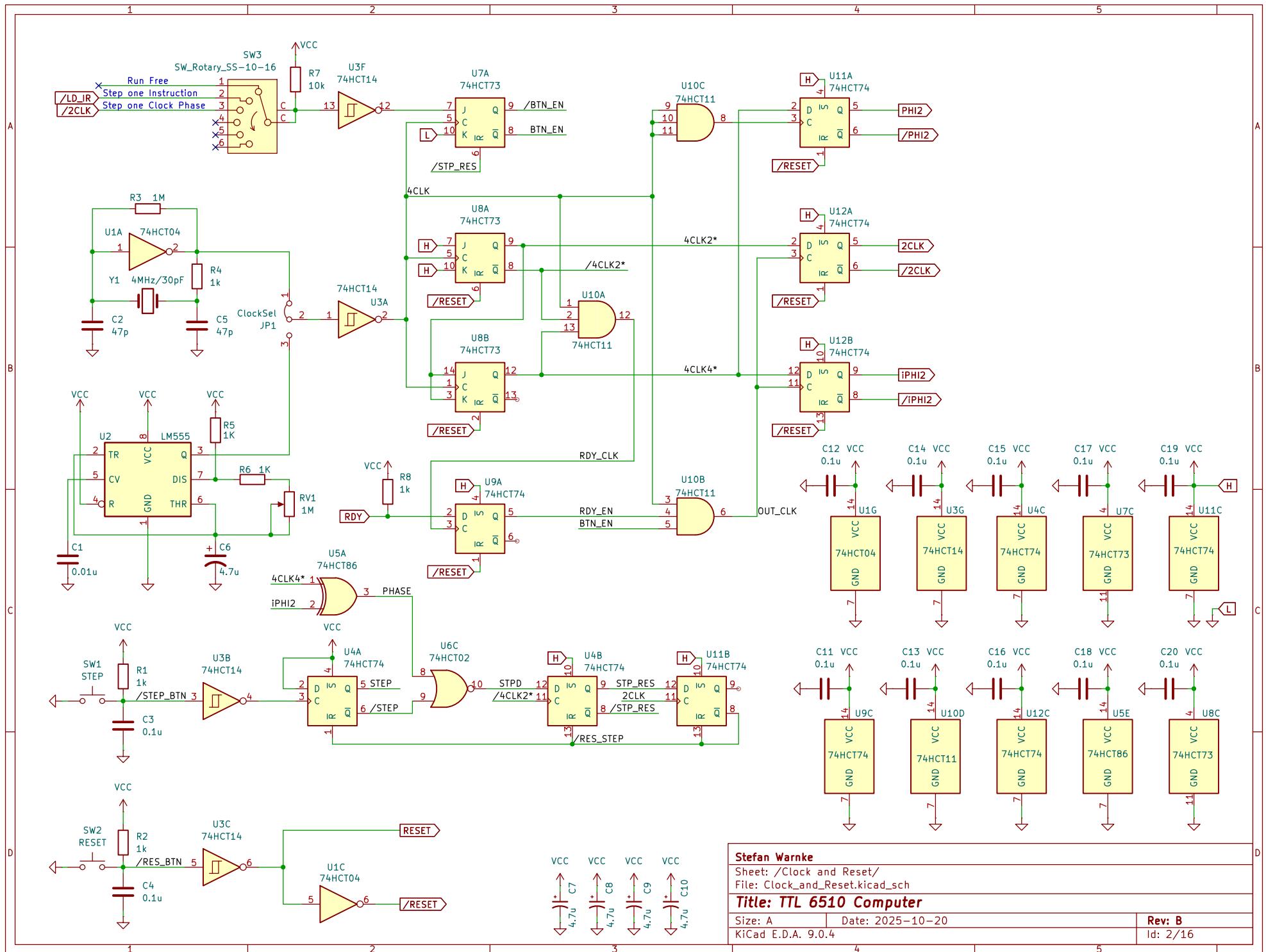
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D

SimTTL_Regs

File: SimTTL_Regs.kicad_sch

Stefan WarnkeSheet: /
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KiCad E.D.A. 9.0.4Rev: B
Id: 1/16



Stefan Warnke

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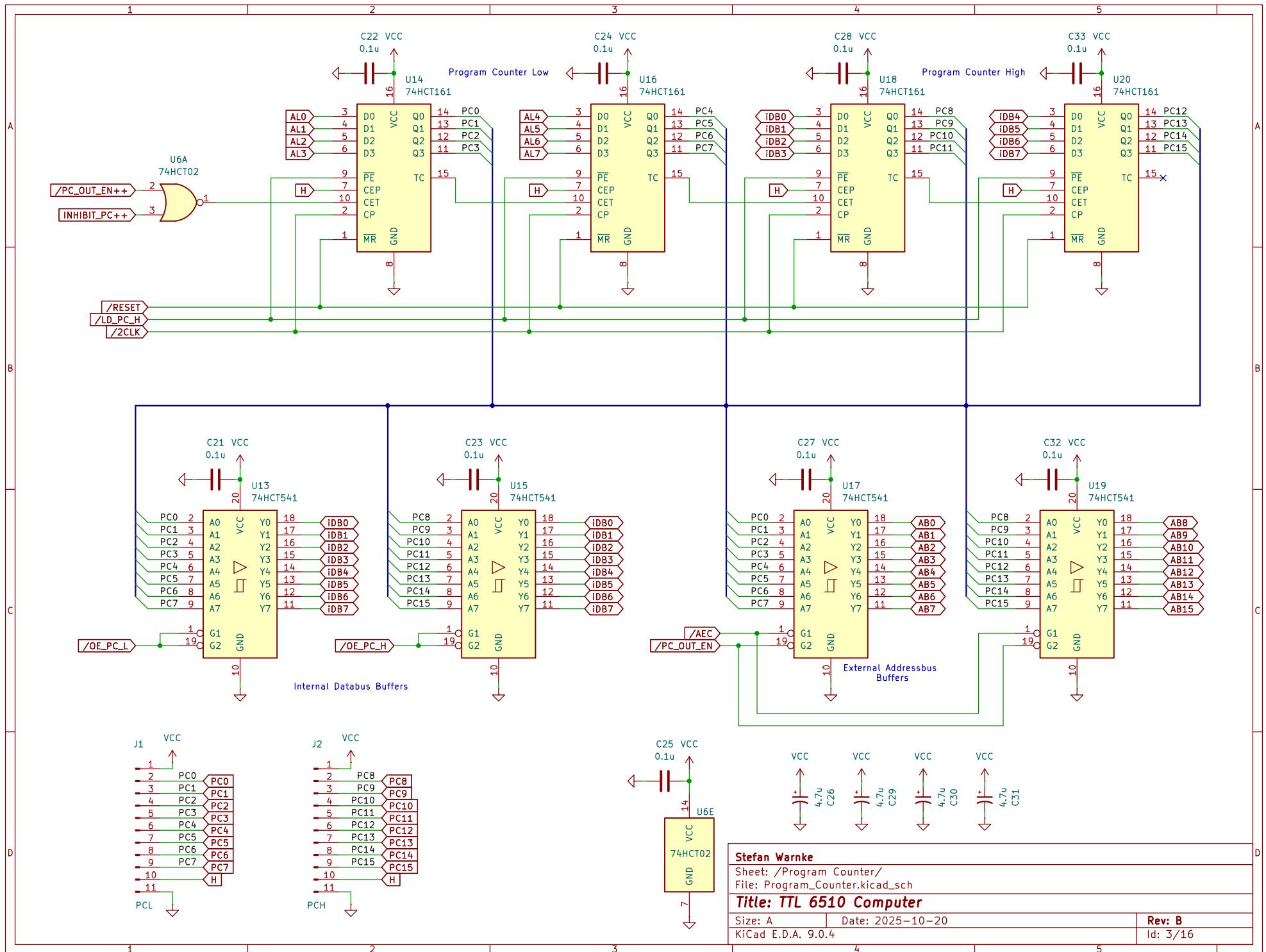
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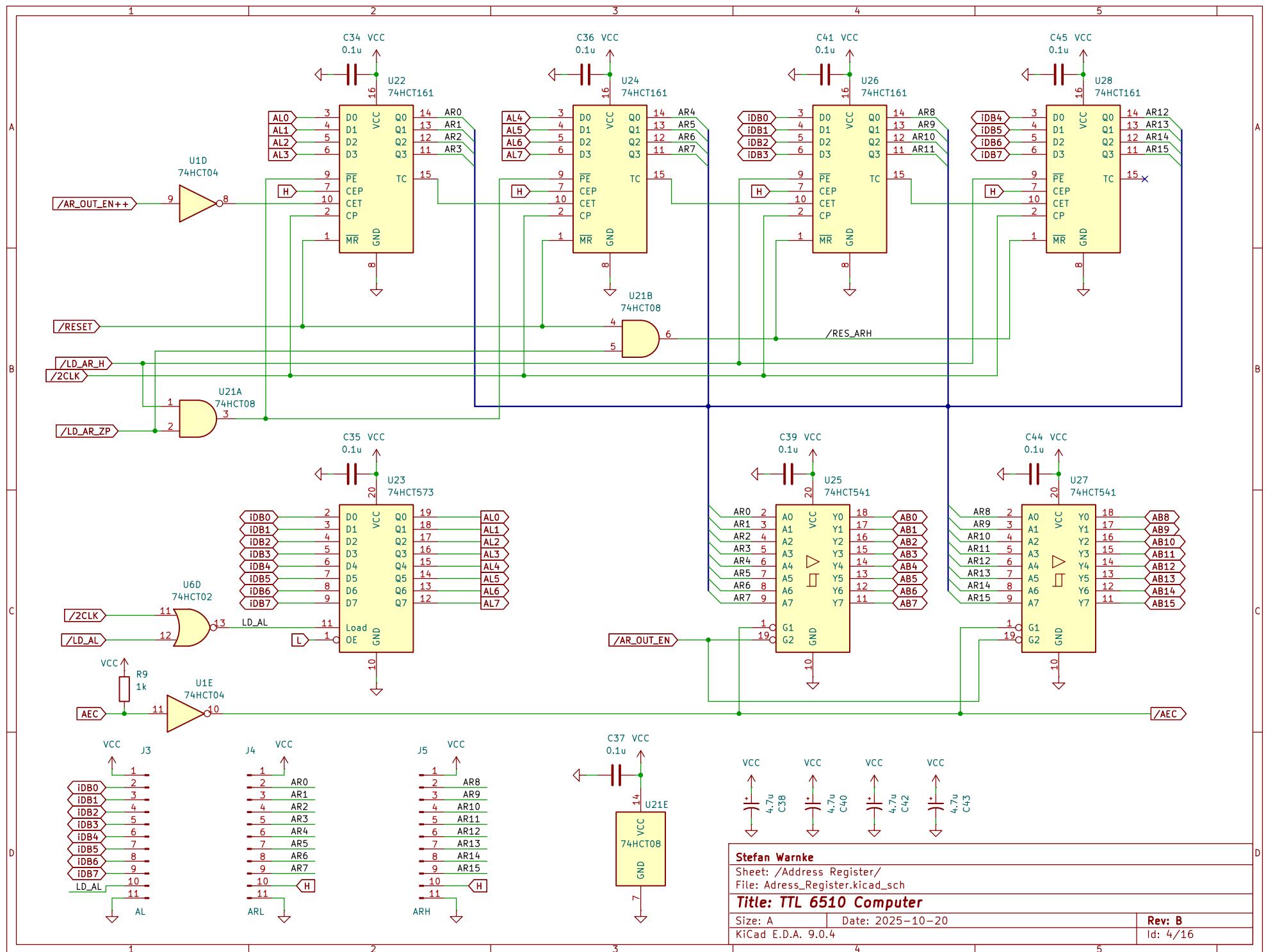
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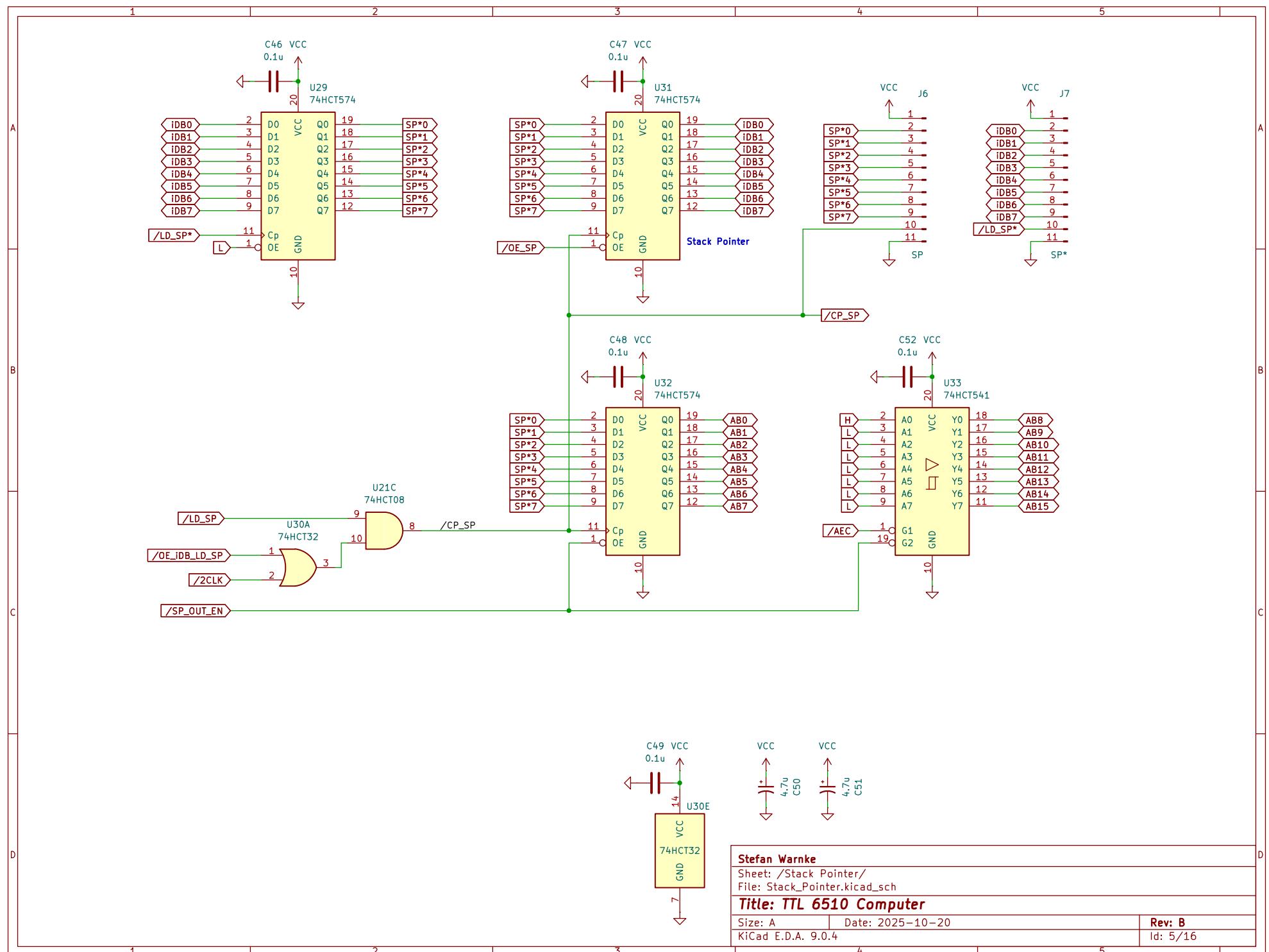
KiCad E.D.A. 9.0.4

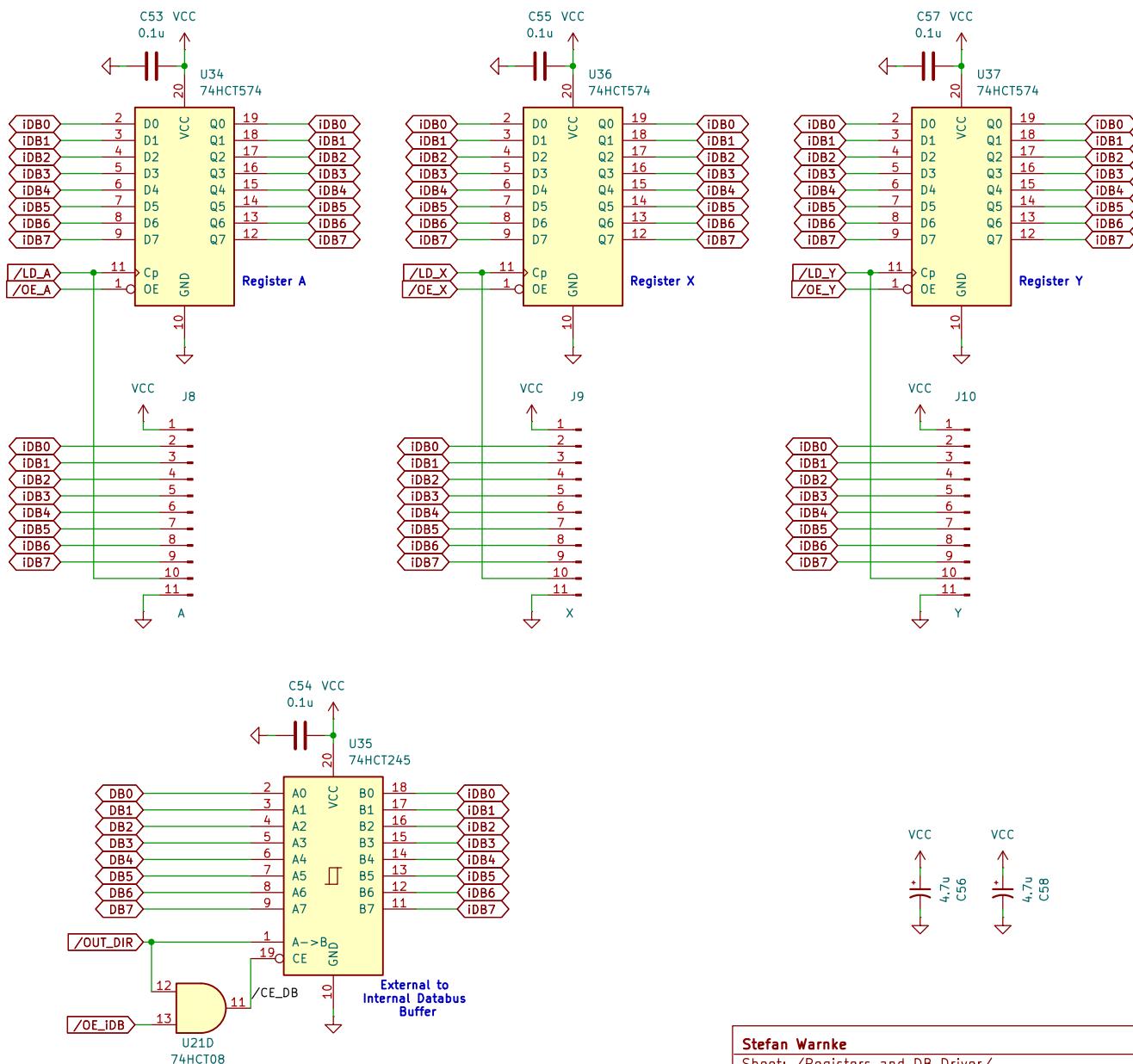
Rev: B

Id: 2/16









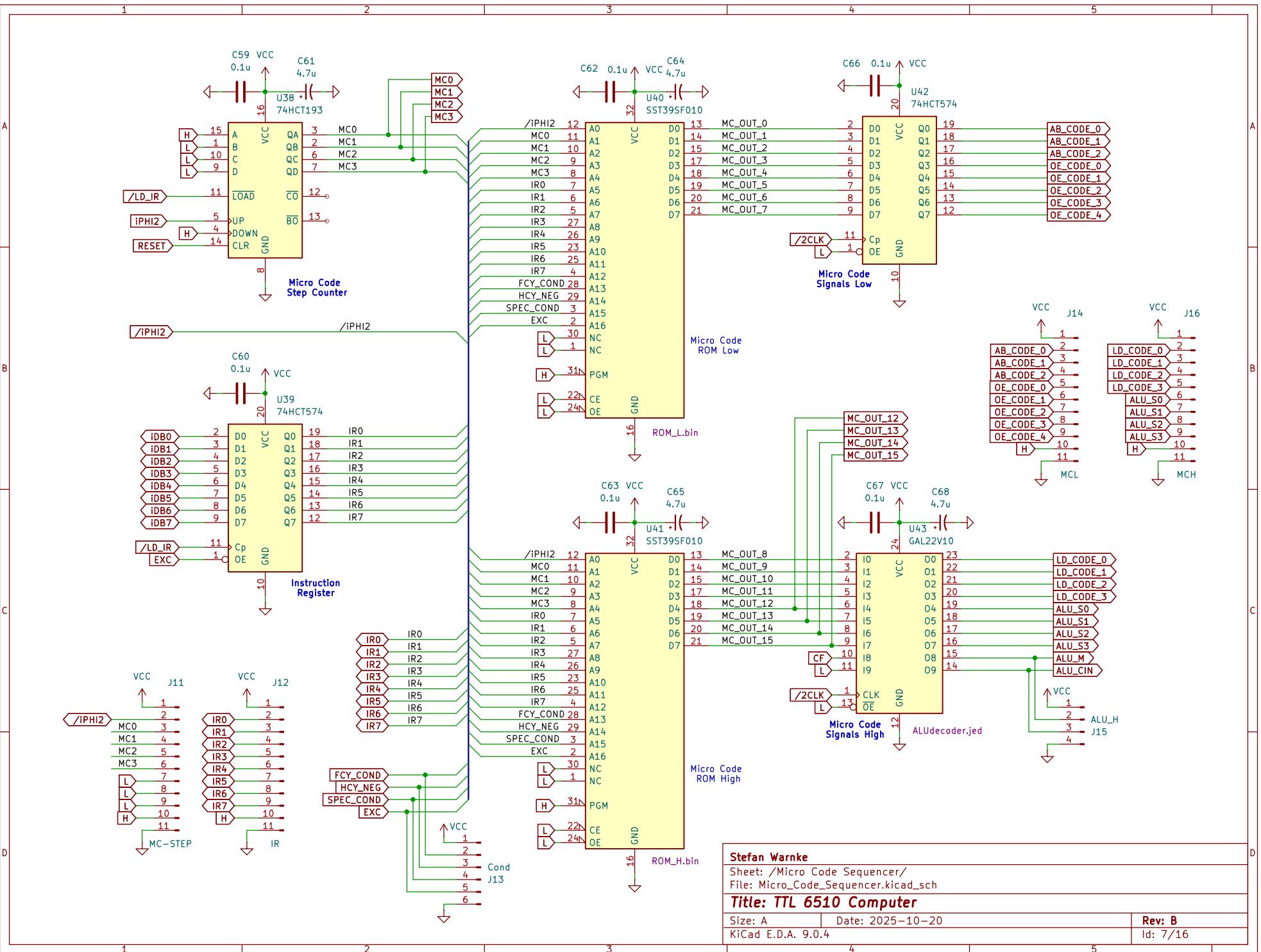
Stefan Warnke

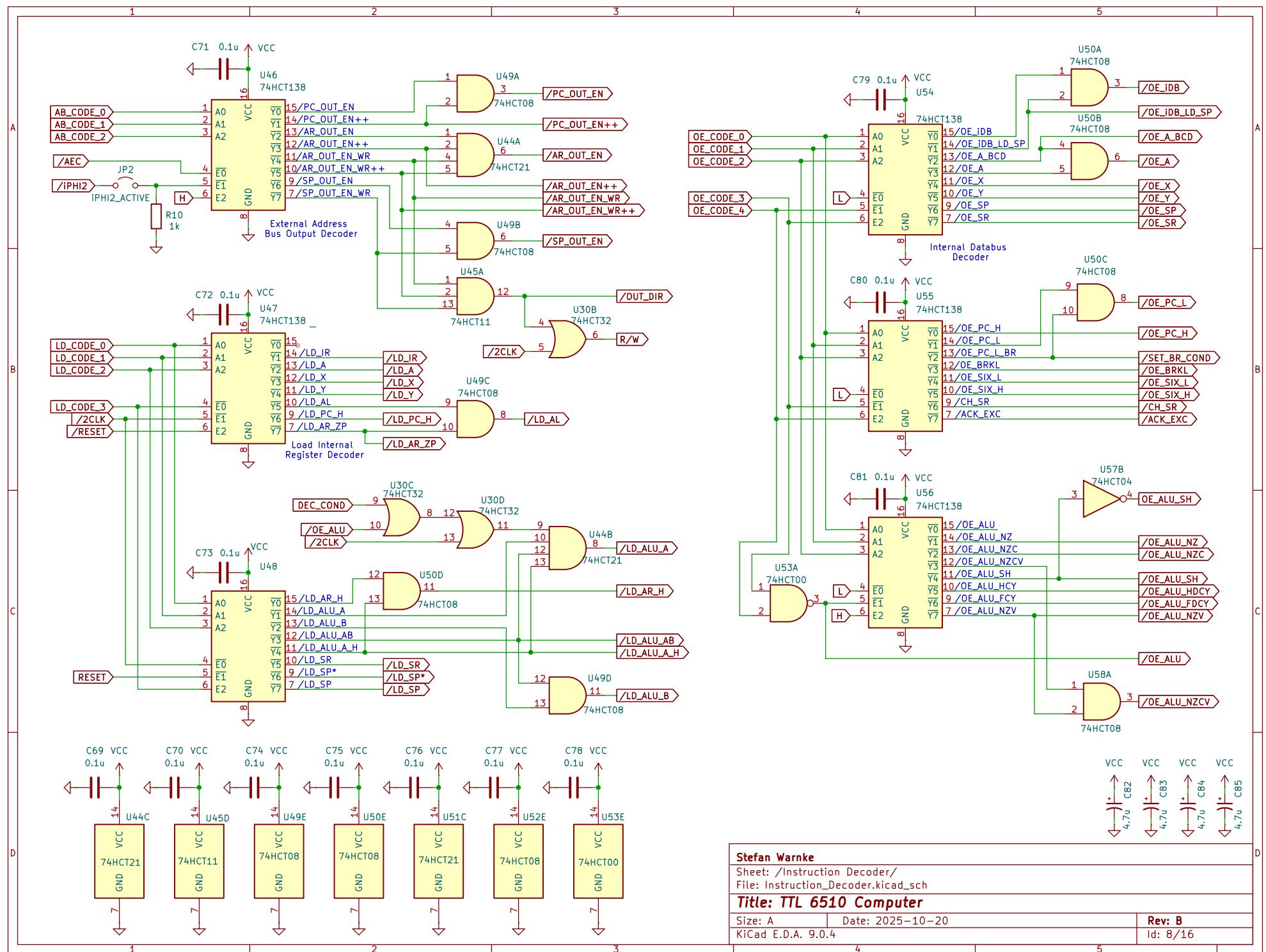
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Size: A | Date: 2025-10-20
KiCad E.D.A. 9.0.4

Rev: B
Id: 6/16





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File: Instruction_Decoder.kicad_sch

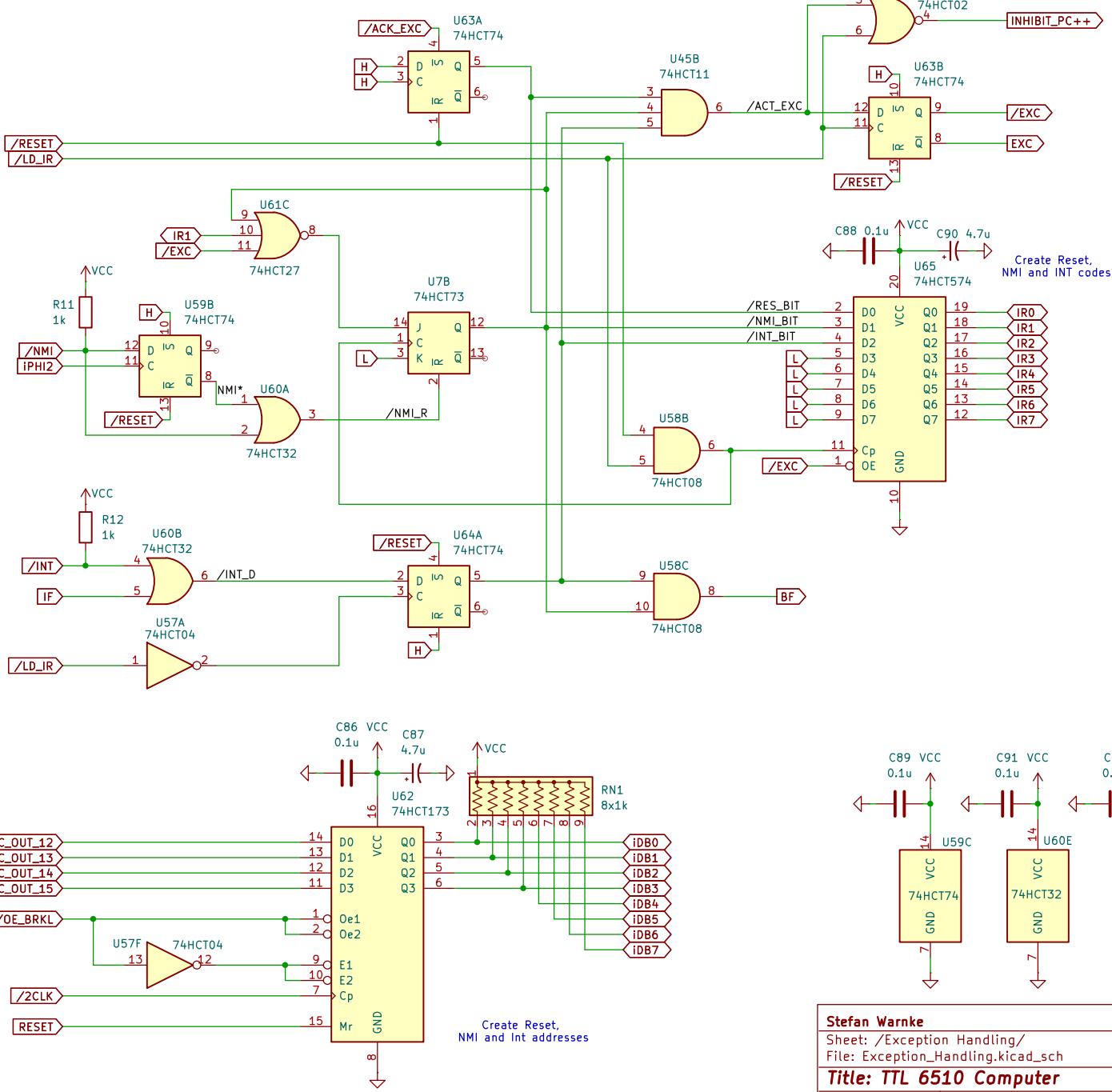
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KiCad E.D.A. 9.0.4

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Id: 8/16



Stefan Warnke

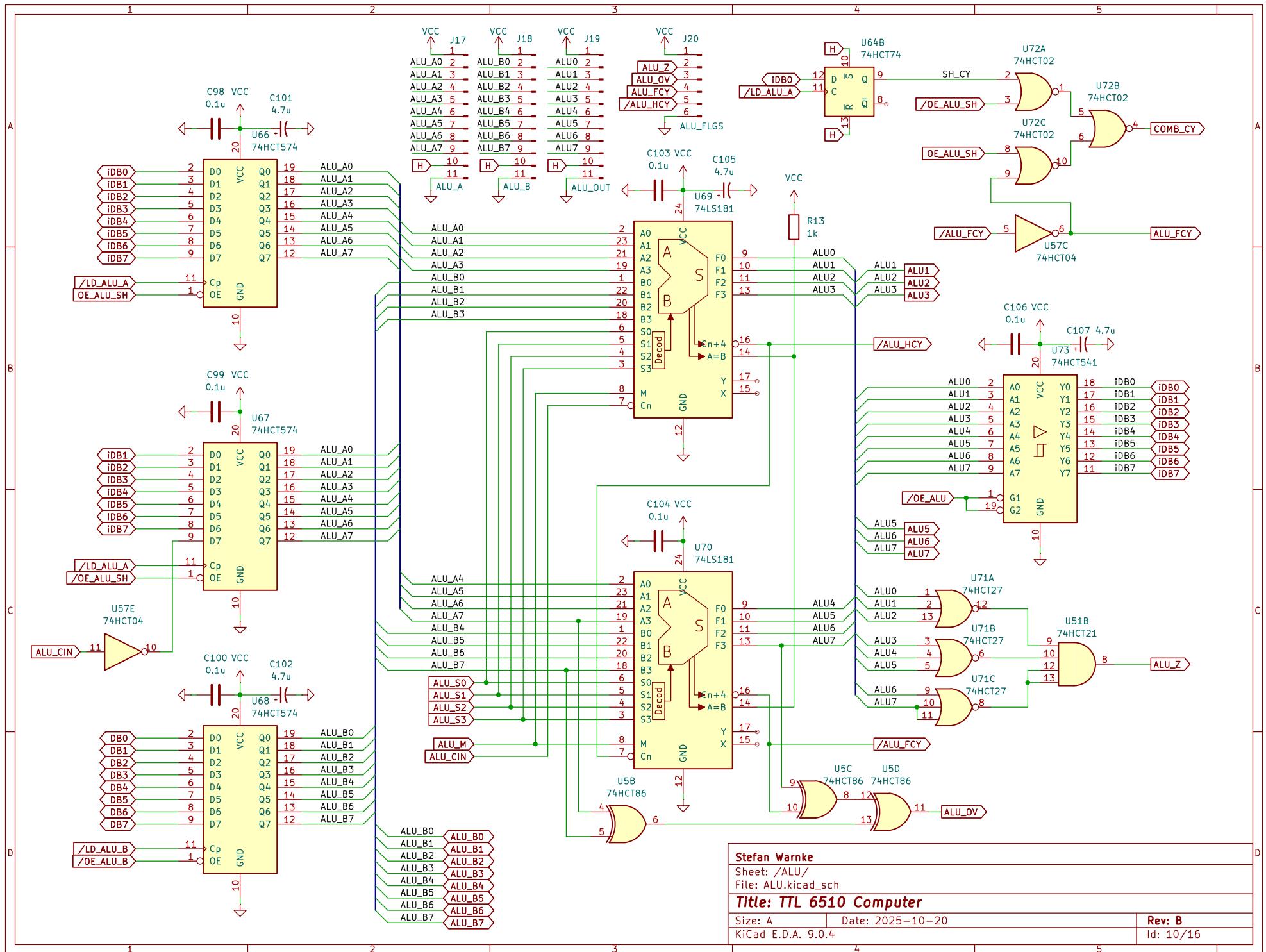
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Title: TTL 6510 Computer

Size: A Date: 2025-10-20

KiCad E.D.A. 9.0.4

Rev: B
Id: 9/16



Stefan Warnke

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File: ALU.kicad_sch

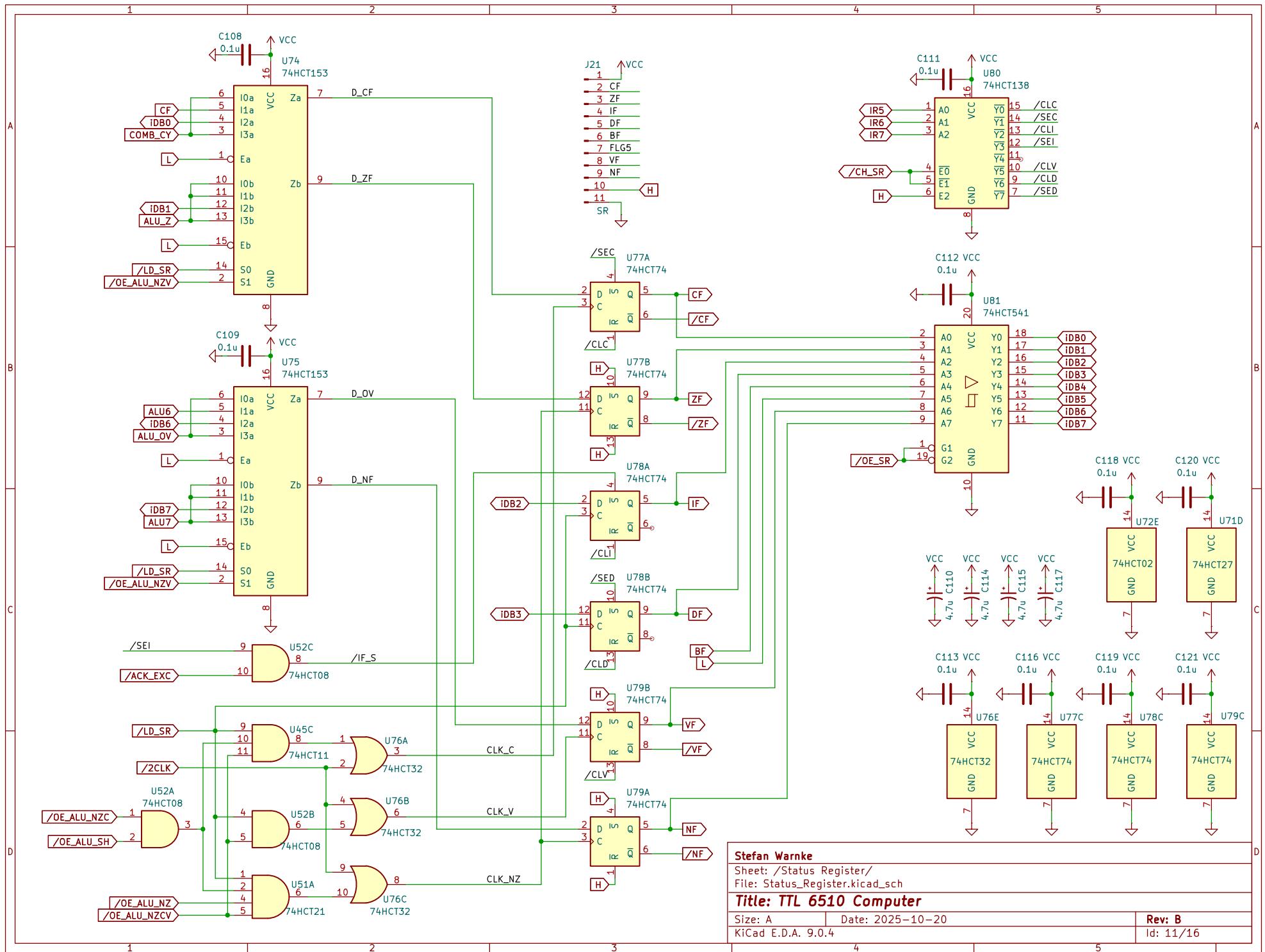
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Size: A Date: 2025-10-20

KiCad E.D.A. 9.0.4

Rev: B

Id: 10/16



Stefan Warnke

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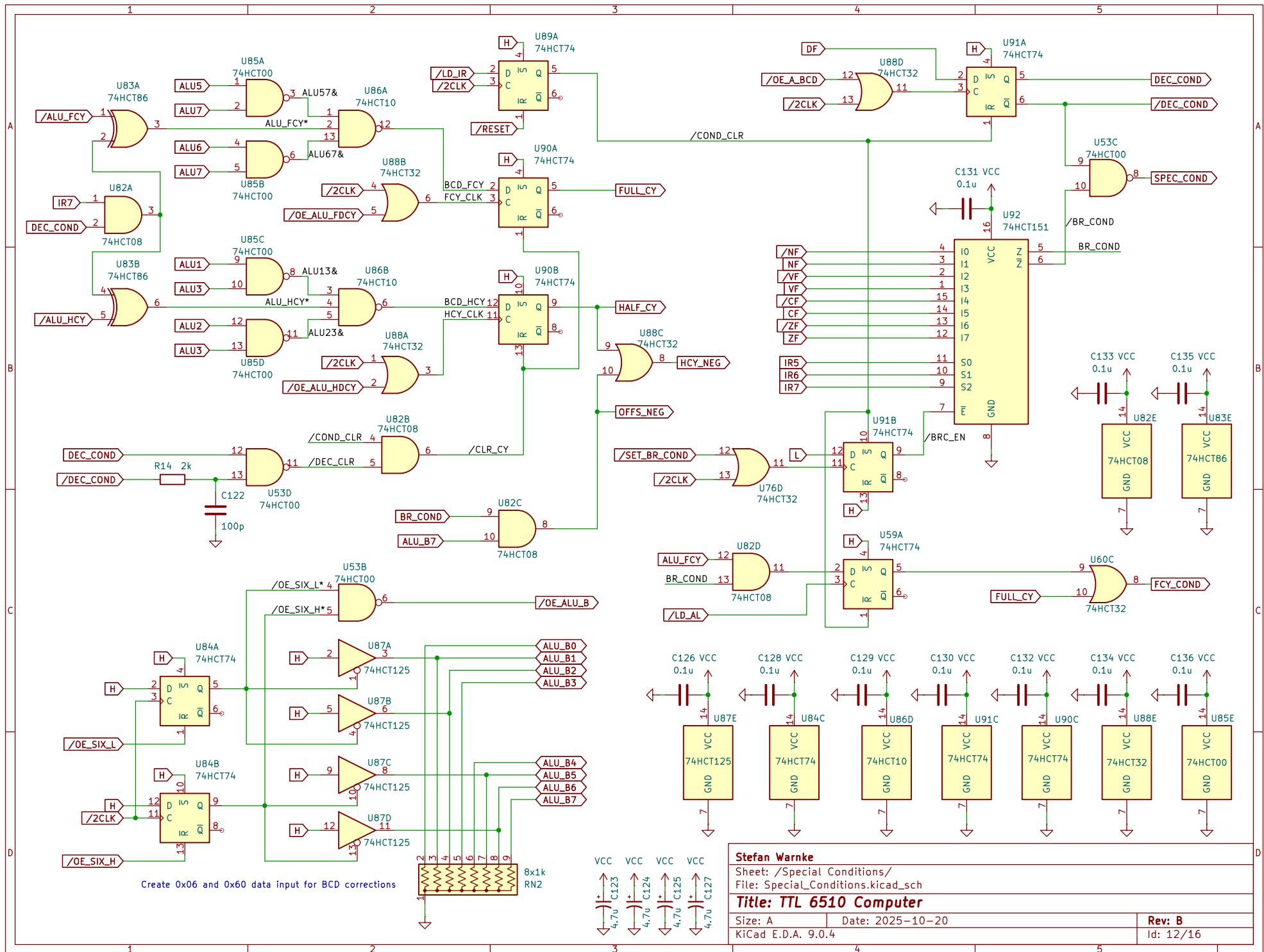
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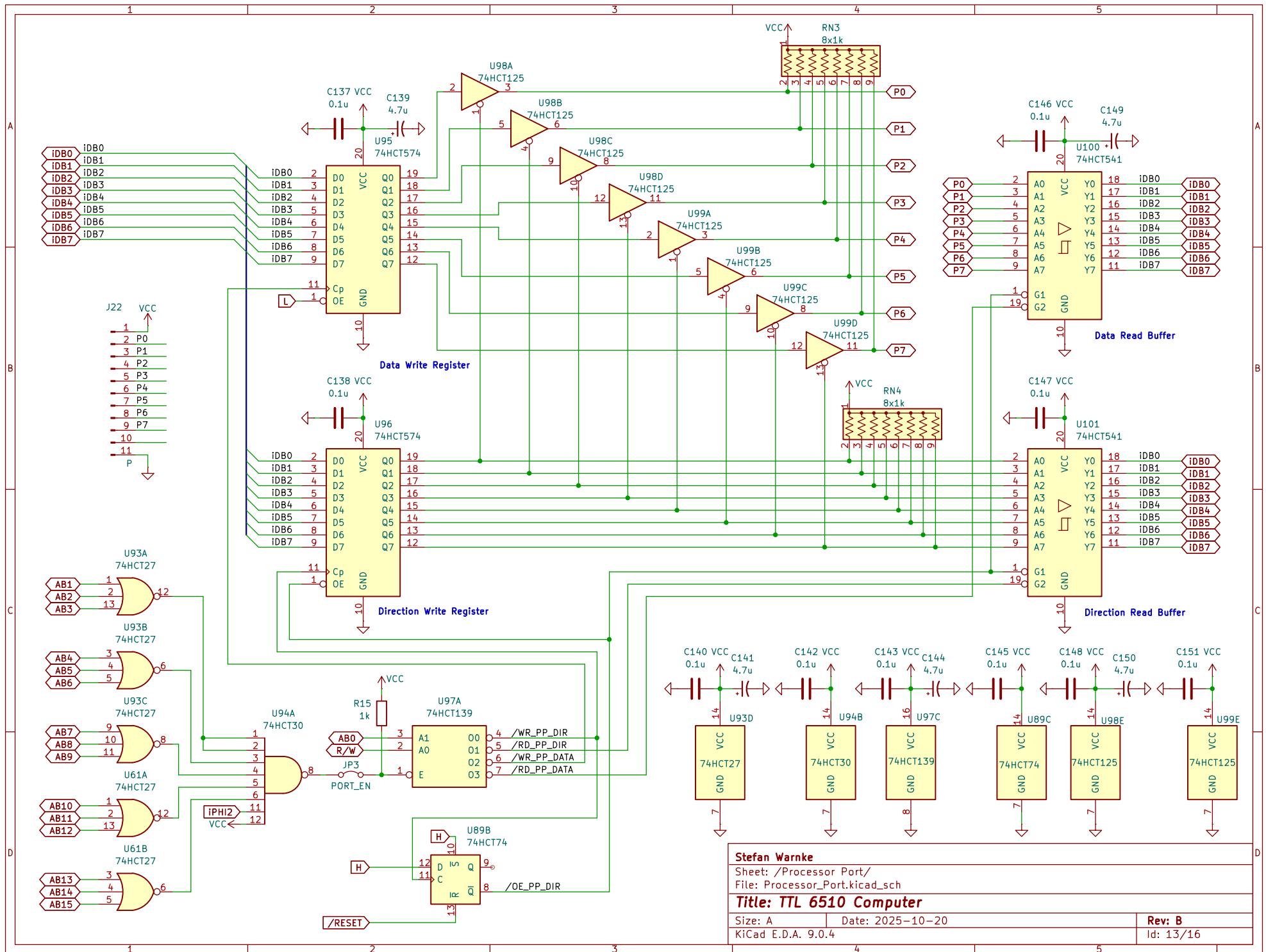
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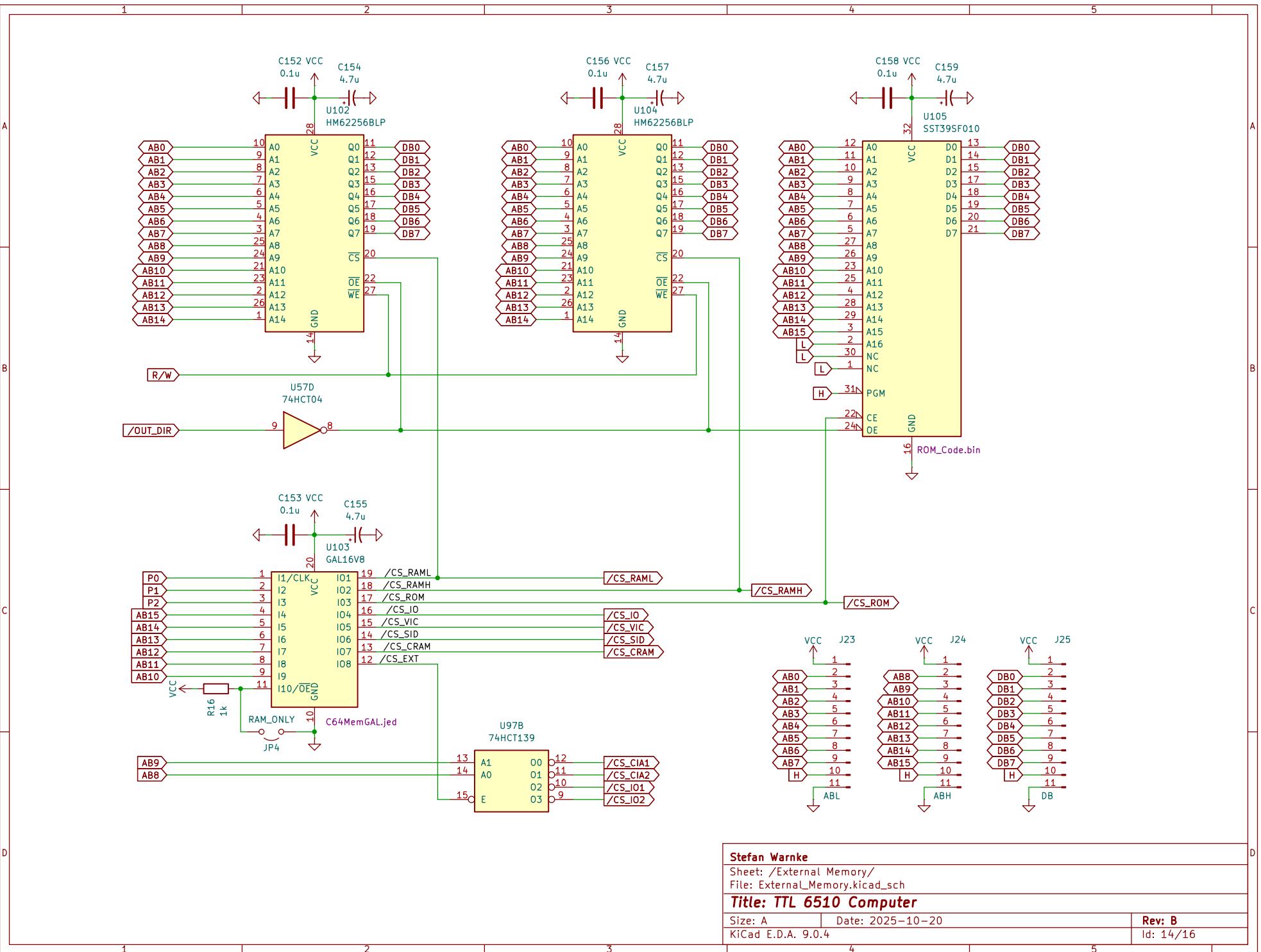
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Rev: B

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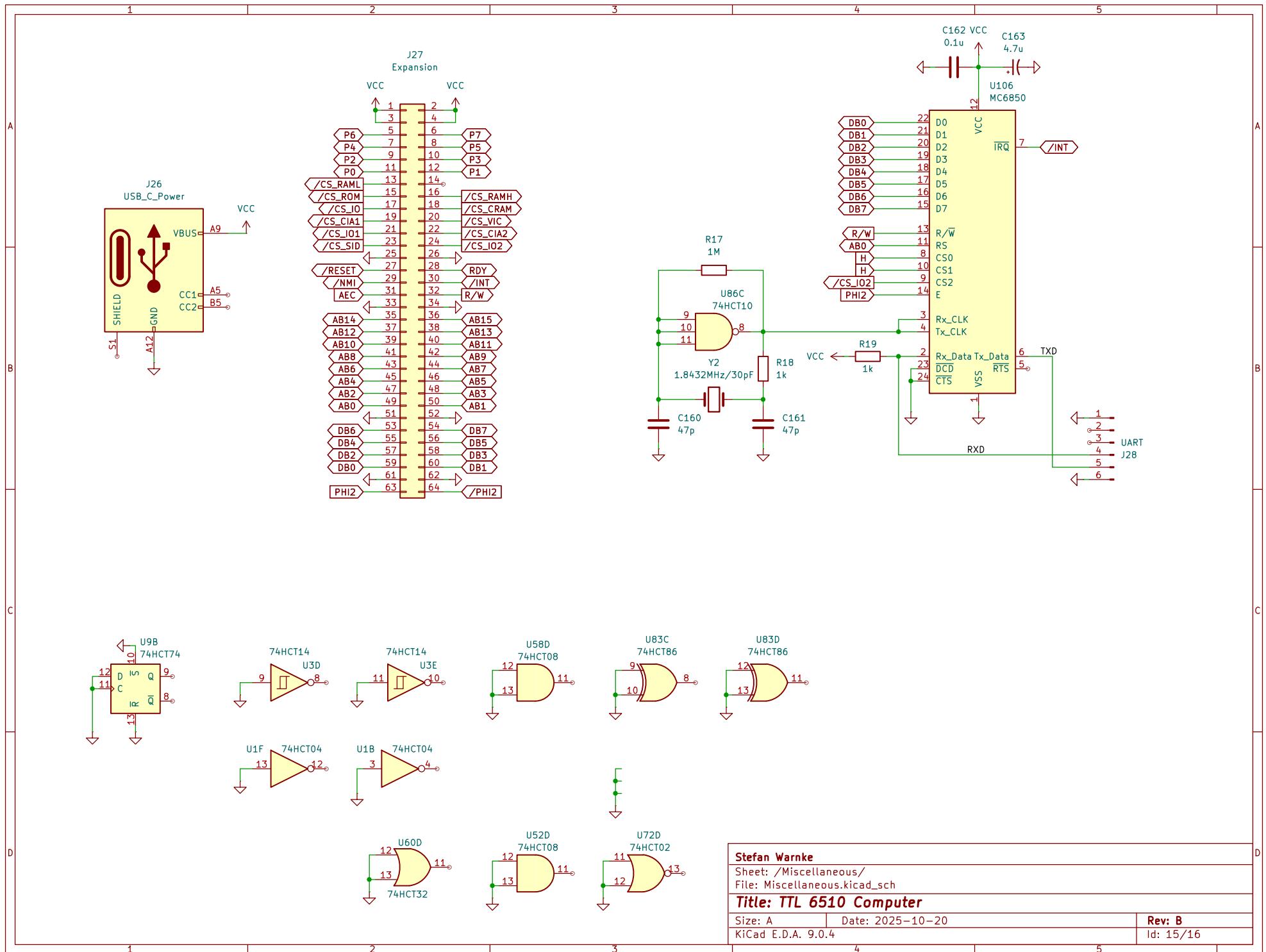
Stefan Warnke

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Title: TTL 6510 Computer

Size: A Date: 2025-10-20
KiCad E.D.A. 9.0.4

Rev: B
Id: 14/16



Stefan Warnke

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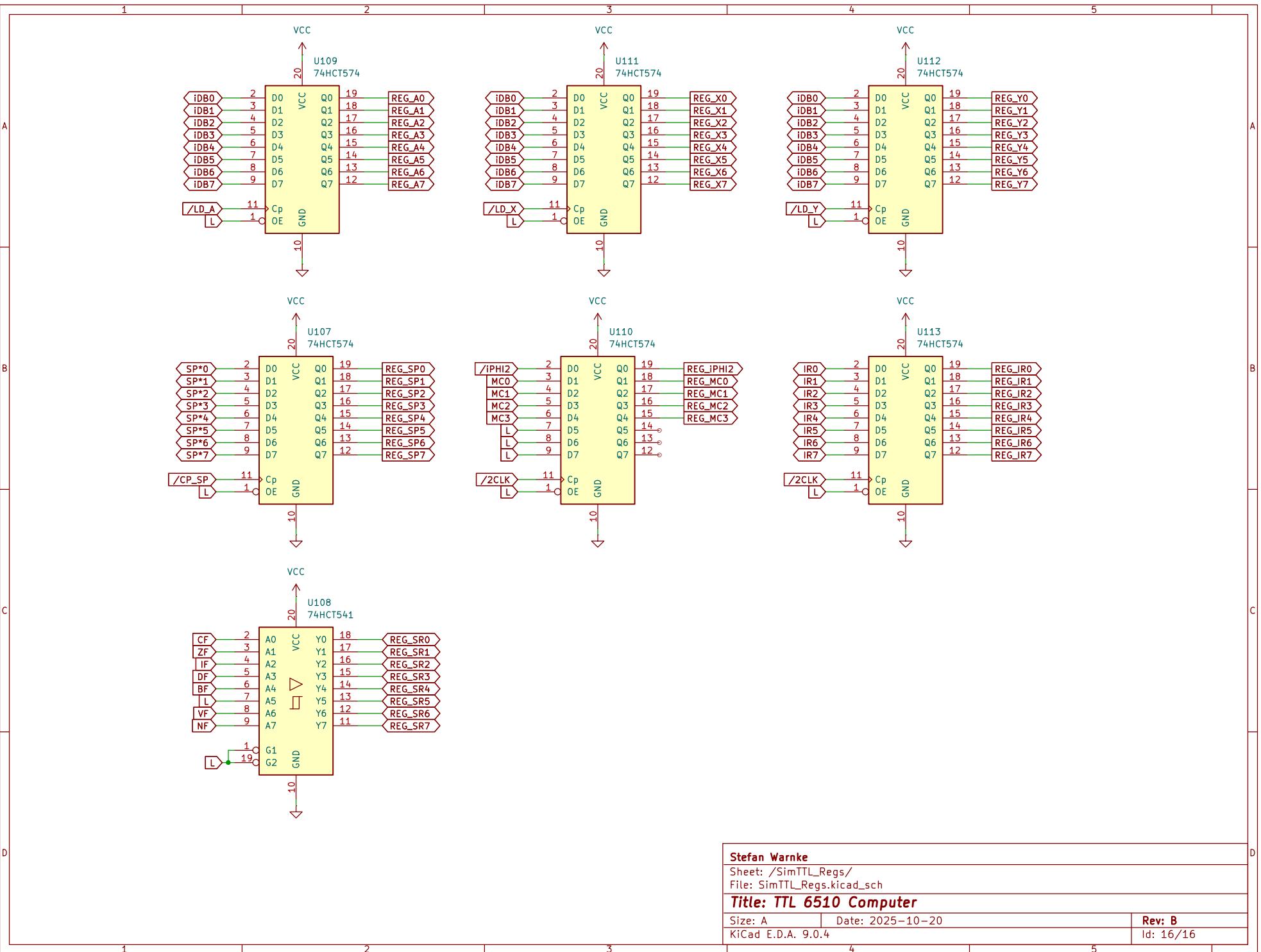
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KiCad E.D.A. 9.0.4

Rev: B

Id: 15/16



Stefan Warnke

Sheet: /SimTTL_Regs/
File: SimTTL_Regs.kicad_sch

Title: TTL 6510 Computer

Size: A Date: 2025-10-20
KiCad E.D.A. 9.0.4

Rev: B
Id: 16/16