

TTL 6510 Computer Stefan Warnke 2025 Rev B

Expansion

Special Conditions

Status Register

Instruction Decoder

Micro Code Sequencer

Cond

MC-STEP

Clock & Reset

Processor Port

External Memory

Address Register

Program Counter

Stack Pointer

ALU_A

ALU_OUT

ALU_B

ALU

MCH

ALU_H

MCL

IR

MC-STEP

Clock & Reset

ARL

PCL

AL

SP*

A

X Registers

Y

SW1 STEP

SW2 RESET

RV1 1M

U2 LM555

U0 74HCT11

U5 74HCT86

U9 74HCT74

U6 74HCT02

U7 74HCT73

U1 74HCT04

U11 74HCT74

U3 74HCT10

U13 74HCT75

U8 74HCT75

U4 74HCT08

U55 74HCT138

U47 74HCT138

U44 74HCT21

U50 74HCT08

U75 74HCT138

U54 74HCT138

U46 74HCT138

U79 74HCT74

U77 74HCT74

U76 74HCT32

U74 74HCT32

U73 74HCT54

U72 74HCT02

U51 74HCT21

U58 74HCT08

U60 74HCT32

U62 74HCT173

U64 74HCT74

U66 74HCT574

U67 74HCT574

U68 74HCT574

U69 74LS181

U70 74LS181

U71 74HCT27

U72 74HCT104

U73 74HCT125

U74 74HCT125

U75 74HCT153

U76 74HCT74

U77 74HCT74

U78 74HCT08

U79 74HCT74

U80 74HCT138

U81 74HCT574

U82 74HCT08

U84 74HCT74

U86 74HCT10

U88 74HCT08

U90 74HCT74

U92 74HCT154

U94 74HCT30

U96 74HCT574

U98 74HCT125

U99 74HCT125

U100 74HCT541

U102 74HCT541

U104 74HCT541

U105 74HCT541

U106 74HCT541

U107 74HCT541

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U2