-	1	2	3	4	5
	Clock and Reset	Program Counter	Address Register	Stack Pointer	
A					
	File: Clock_and_Reset.kicad_sch	File: Program_Counter.kicad_sch	File: Adress_Register.kicad_sch	File: Stack_Pointer.kicad_sch	
	Registers and DB Driver	Micro Code Sequencer	Instruction Decoder	Exception Handling	
3					
	File: Registers_and_DB_Driver.kicad_sch	File: Micro_Code_Sequencer.kicad_sch	File: Instruction_Decoder.kicad_sch	File: Exception_Handling.kicad_sch	
	ALU	Status Register	Special Conditions	Processor Port	
	File: ALU.kicad_sch	File: Status_Register.kicad_sch	File: Special_Conditions.kicad_sch	File: Processor_Port.kicad_sch	
		External Memory	Miscellaneous		
-					
		File: External_Memory.kicad_sch	File: Miscellaneous.kicad_sch		
		SimTTL_Regs			
		 File: SimTTL_Regs.kicad_sc	Stefan Warnke		
			Sheet: / File: TTL 6510 Comp	uter.kicad_sch	
			Title: TTL 6510	Title: TTL 6510 Computer	
			Size: A D KiCad E.D.A. 9.0.4	ate: 2025-02-04	Rev: B
	1	3	NICAU E.D.A. 9.0.4	<i>h</i>	10: 1/10





























