# Corona Proof Classroom

TODO : add Diagram, when finished

TODO: Overall information about t this project: What and Why ?

## Window node

This node informs the system if the window is open or close. As this module should be placed near every window, there is a requirement for an easy mount. This module is entirely wireless to fulfill this requirement. It means that the module communicates via BLE (see subsection [Communication](#_Communication)), and it is powered independently (see subsection [Power harvesting](#_Power_harvesting)).

This module uses the nRF52840 as a CPU. This CPU was chosen because it has Bluetooth Low Energy (BLE) on-chip. The window status is detected by the reed switch, despite we first discuss using a hall sensor because the hall sensor consumes more power. (For example, the [SS461R.](https://cz.farnell.com/honeywell/ss461r/hall-effect-sensor-bipolar-to/dp/1735770?st=hall%20sensor)  consume typically 3.5 mA).

The device should be in power save state most of the time. When someone opens or closes the window, the node should be woked up and report this change. To ensure that the energy supply is sufficient, the device is allowed to send the information only three times per hour. If this limit is reached, it will send the last state at the end of this period. To detect if all devices work properly, the window node will send every 24 hours one message.

### Power harvesting

As there could be many Windows node modules in each room, it is essential to make the maintenance as simple as possible. Thus this module harvest energy instead of using batteries, which have to be replaced or recharged after some period.

As the window node is placed on the window (as its name says), the best energy to harvest is from the sun. To make the solar panel as small as possible, it is crucial to optimize power consumption.

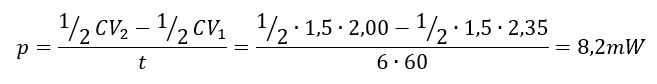
The only source of power we use MIKROE-651 (70.00mm x 65.00mm) solar panel. It could give us 100mA at 4V, meaning that the maximum power is 400mW. As the sensors will be most probably located indoor and can be hidden in the shadow, we do not expect to reach this maximum power.

The solar panel is connected to the J2 screw terminal of the [bq25570EVM-206](https://www.ti.com/lit/ug/sluuaa7a/sluuaa7a.pdf?ts=1606856067193&ref_url=https%253A%252F%252Fwww.ti.com%252Ftool%252FBQ25570EVM-206%253Futm_source%253Dgoogle%2526utm_medium%253Dcpc%2526utm_campaign%253Dapp-null-null-GPN_EN_EVM-cpc-evm-google-wwe%2526utm_content%253DBQ25570EVM206%2526ds_k%253DBQ25570EVM-206%2526DCM%253Dyes%2526gclid%253DCjwKCAiA8Jf-BRB-EiwAWDtEGso9PlQg4hH1DbfHmVWag5CSS1KGx1ErZqPrdqqDqYDGDrUgbuuwxhoCmHsQAvD_BwE%2526gclsrc%253Daw.ds) evaluation board, which is well-suited for high impedance power direct current sources. The maximum output power point(MPP) of those power sources varies with ambient conditions. In our case, the solar panel's MPP varies with the amount of light and the temperature. The bq25570 automatically track the MPP to maximize the amount of harvested power.  We set the MPP is located at 80% of open-circuit voltage by connecting JP4 to 80%. Only one of JP4 and JP1 can have a shunt installed at the same time.

This power management module charges a supercapacitor or Li-pol battery, which ensures the availability of power. This storage element can have significant power leakage. We decide to use the supercapacitor 1.5F instead of a battery, as it is less expensive. The supercapacitor is connected to J8. The power management module can charge the supercapacitor up to 4.2V. On our development board, the maximum is set to 3.2V via resistor divider (R1 and R2). I measure maximum voltage 2,4V (Maybe my voltmeter mistake?). There is internally programmed undervoltage protection of 2.0 V to protect the Li-pol battery.

Once the battery voltage is above undervoltage level, it provides 1.8 V to its output (J11). This voltage is set via resistors R9 and R10. The output is enabled when a shunt is installed on JP2 (connect \EN and GND). The output bug converter can be disabled using JP3 and JP6. We connect VOUT\_EN and VSTOR on JP3 and discontent JP6. In this configuration, the device is always powered on when the storage element is not undervolted.

We measured the power of the solar panel in a real environment. The whole setup was connected, as described earlier. The J11 was unconnected. We measured the voltage on the supercapacitor (V1). After six minute we measure it again (V2). Then we can compute the power as follow:



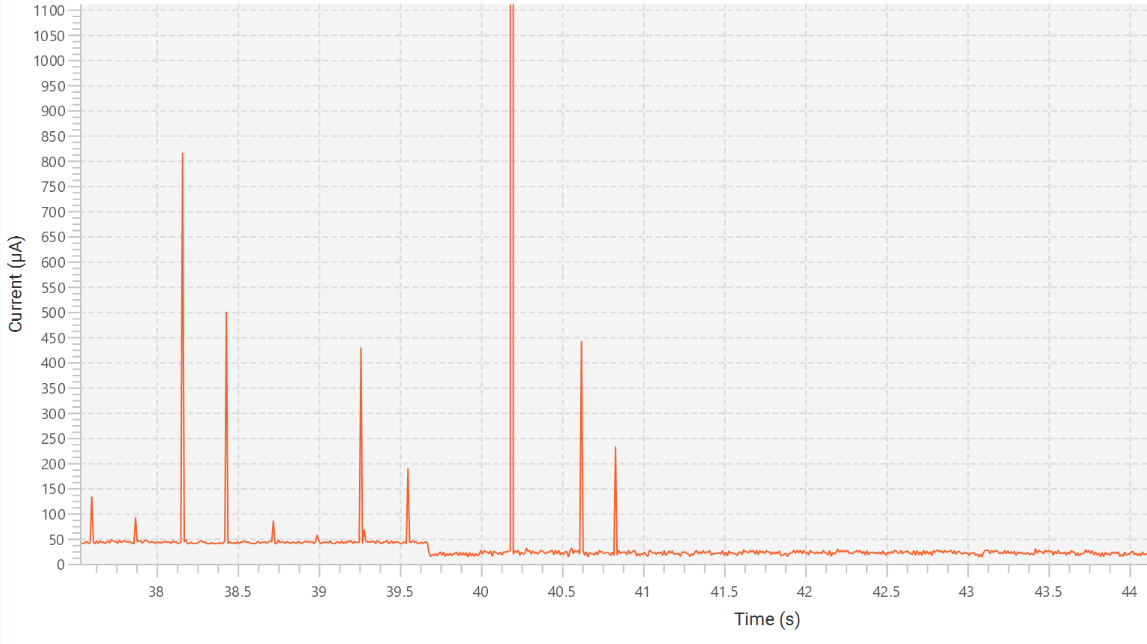
### Power savings

The power consumption was measured using STM X-NUCLEO-LPM01A. For the first measurement, the value from the screen was used because it is a single value, and it is easy to compare. For the final measurements, the current was the current-time profile was captured using STM32 CubeMonitor-Power.

For the first measurement device was connected to V\_in 3-5 pin on P20. In this scenario, the board consumes approximately 5mA. When changing the SW6 to "nRF only," the power consumption drop to 2.095 mA when the magnet is present to the reed switch and 2.408 mA when not present. This significant difference is caused by the pull-up resistor. In this scenario, the internal pull-up was used. The best way to solve this problem is to use a different reed switch, which will switch between power and ground (For example, [DRS-DTH-50-80](https://cz.mouser.com/datasheet/2/240/littelfuse_reed_switches_DRS_DTH_datasheet_pdf-1372468.pdf)), so there will be no need for a pull-up resistor. The second possible solution, which we used, is to use an external pull up with higher resistance. We use R = 180 kΩ, which causes a 17 µA leak when the power voltage is 3 V and 10 µA when the power supply goes to 1,8 V. This may increase the risk of radio-frequency interference. For this reason, it important to have the wire as short as possible.

A second significant power consumption drop was done by using P21 to power it up. In this scenario, the current drops to 50 µA. Disable the UART logging to save some more power and proper initialization of timers. Rather than using the wait function, we managed to decrease the power consumption to 20 µA when there is a magnet and 40 µA without the magnet. As you can see, there is a current loss of 20 µA when a magnet is present, from one half the resistor causes it, and the second half can be caused by the current going into the PIN (gate of the MOS structure and protection diodes).

The result is shown in this picture. Firstly, there was a magnet present (the window is closed), and after some time, the window is open. The device waits for 0,5s (debouncing) and then start the advertisement. The advertisement is the biggest peak. The whole system is powered by 1,8 V.



### Communication

The window node communicate with the OCTA using BLE addvertisment. It display complete loca name, which is window and all useful informations are in the manufacturer data. There is no company (0xFFFF); ascii code of W(0x75); than 8-bit long ID; zero byte (to make it the same length as chair node) and 1 if window is close or 0 if it is open.

## Chair node

## Octa – gateway

## Backend

## Visualization

<https://lucid.app/lucidchart/43eb870d-9356-41da-9697-4ace1b9dea2e/edit?page=0_0#?folder_id=home&browser=icon>

<https://imgbb.com/>

https://ibb.co/D5fCVMq

## Costs

## Conclusion