

# NUMA Quiz 2

Given a NUMA multiprocessor system with 4 nodes, each node with one processor, 64 KB of private cache and 8 GB of main memory. Coherence among nodes is kept with a directory-based MSU protocol, using the transactions explained in class.

Assume that the home node for variable *var* is NUMANode0 and that at a given moment there exist clean copies of that variable in the cache memories in NUMANode1 and NUMANode2, but no copies in NUMANode0 and NUMANode3. If the processor in NUMANode1 wants to update (write to) variable *var*, which of the following coherence actions do NOT occur?

Trieu-ne una o més:

- ☐ The processor in the local node (NUMANode1) issues PrWr.
- ☐ The local node sends a UpgrReq command to the home node (NUMANode0).
- ☐ Since there is another copy of the line in the cache of a remote node (NUMANode2), the home node sends an Invalidate command to it.
- ☐ Since there is another copy of the line in the cache of a remote node (NUMANode2), the local node sends an Invalidate command to it.

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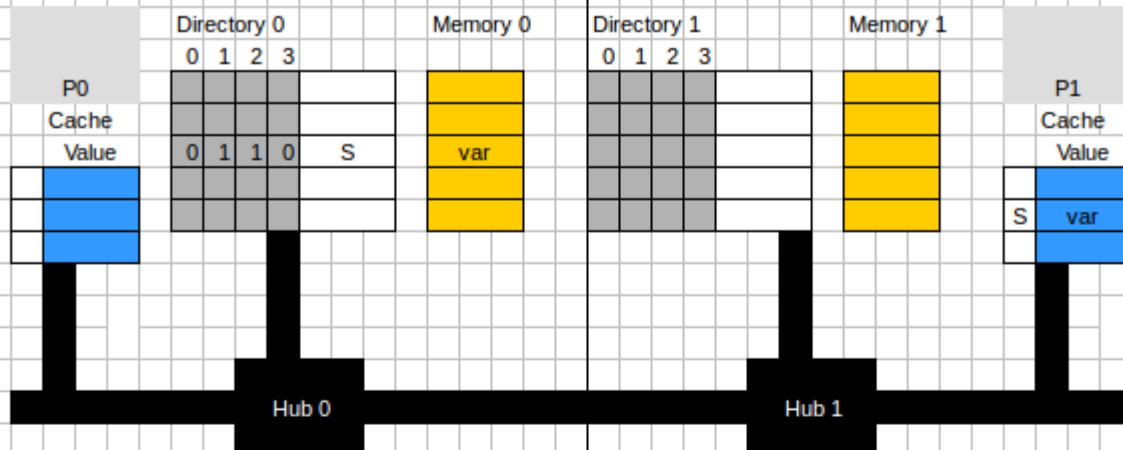
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NUMA node 0

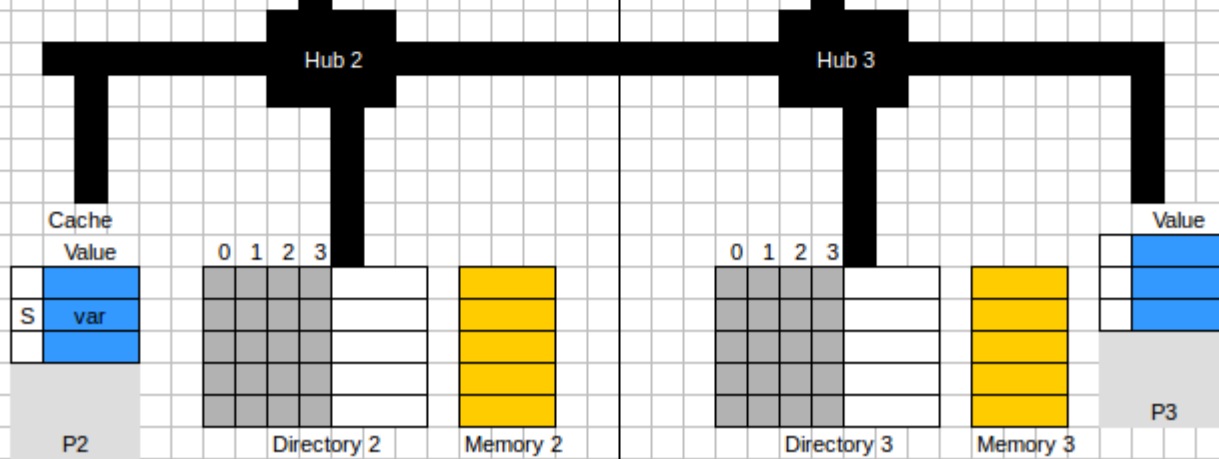
NUMA node 1



PrWr1

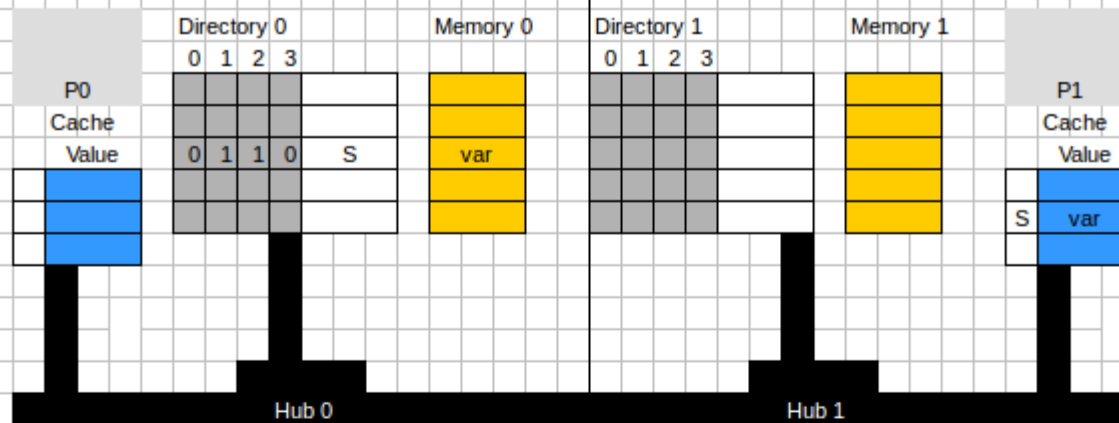
NUMA node 2

NUMA node 3



NUMAnode 0

NUMAnode 1

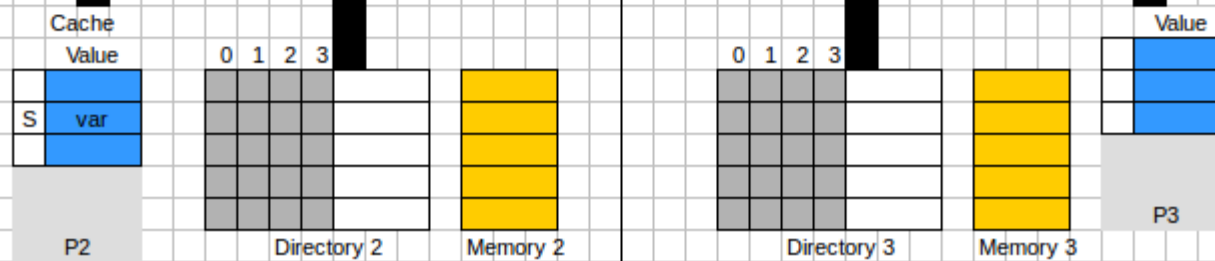


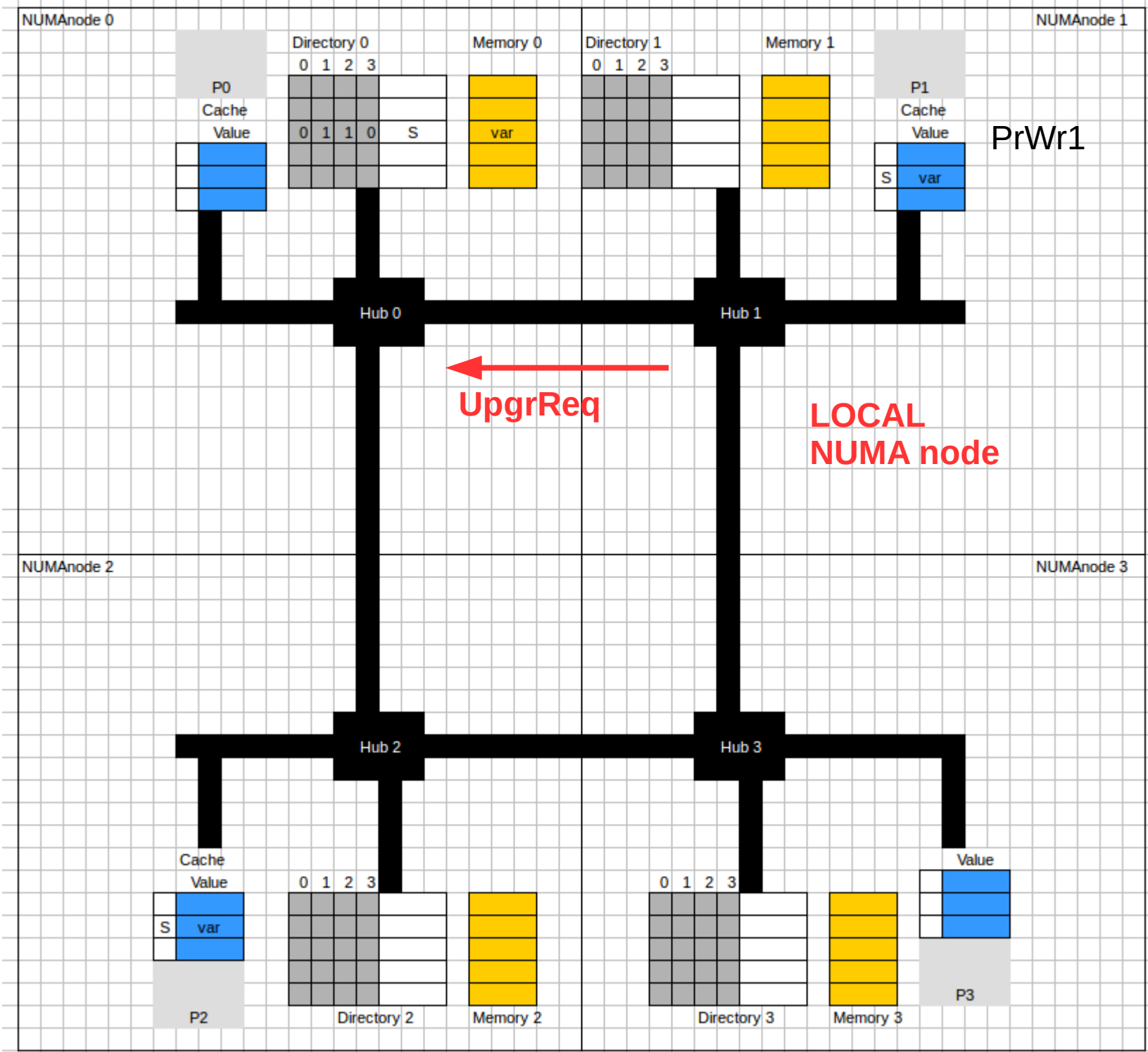
PrWr1

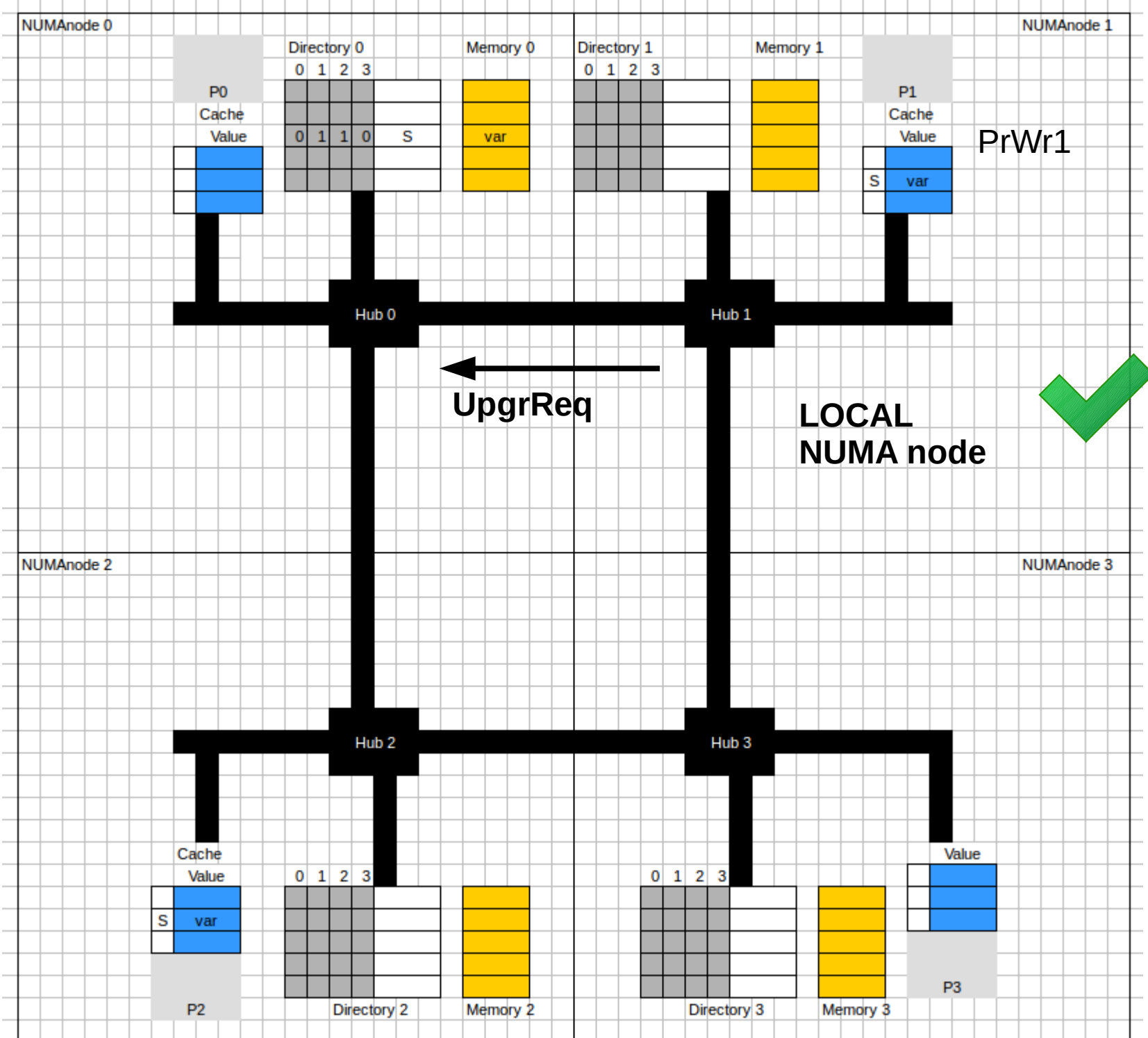


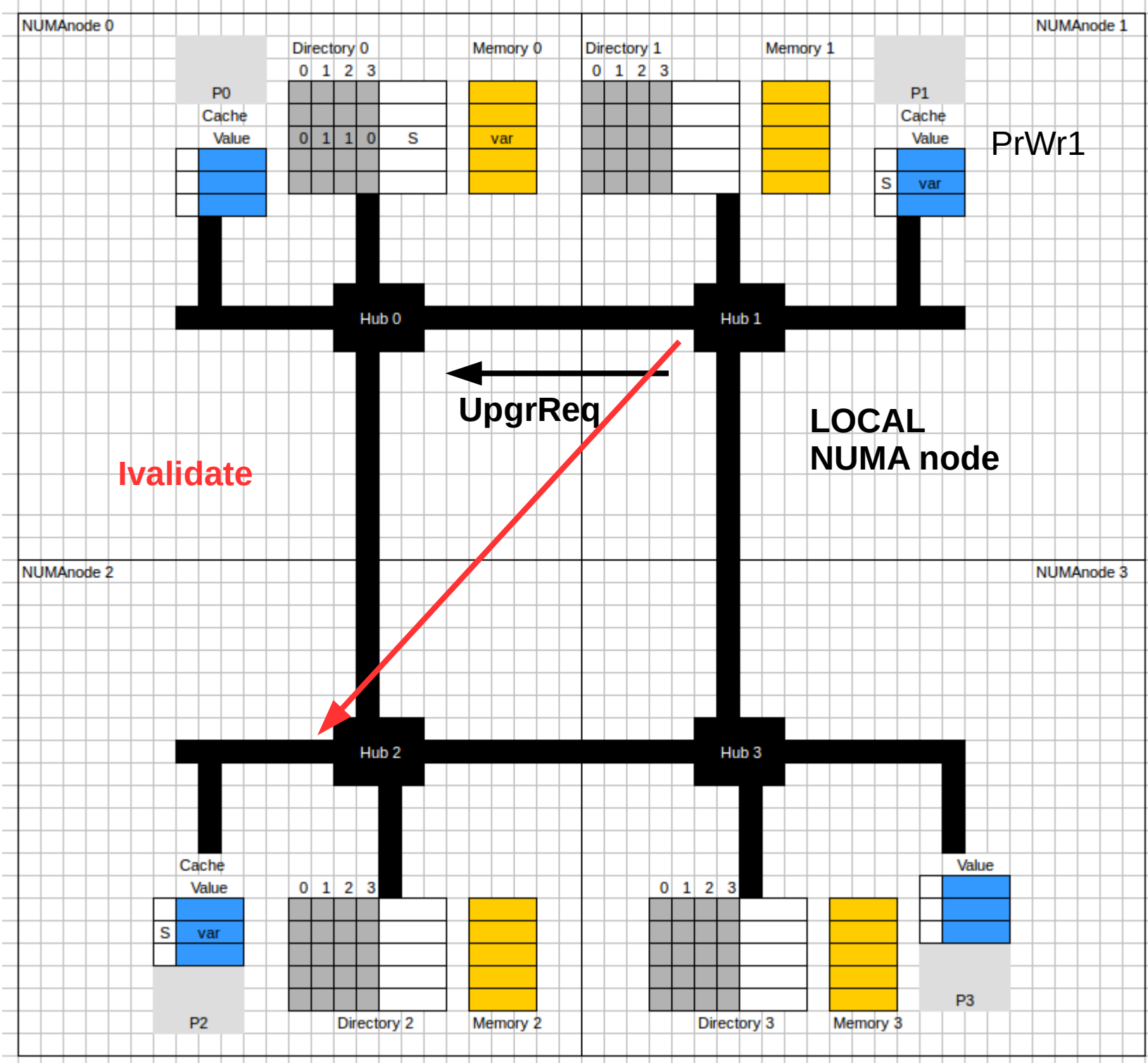
NUMAnode 2

NUMAnode 3







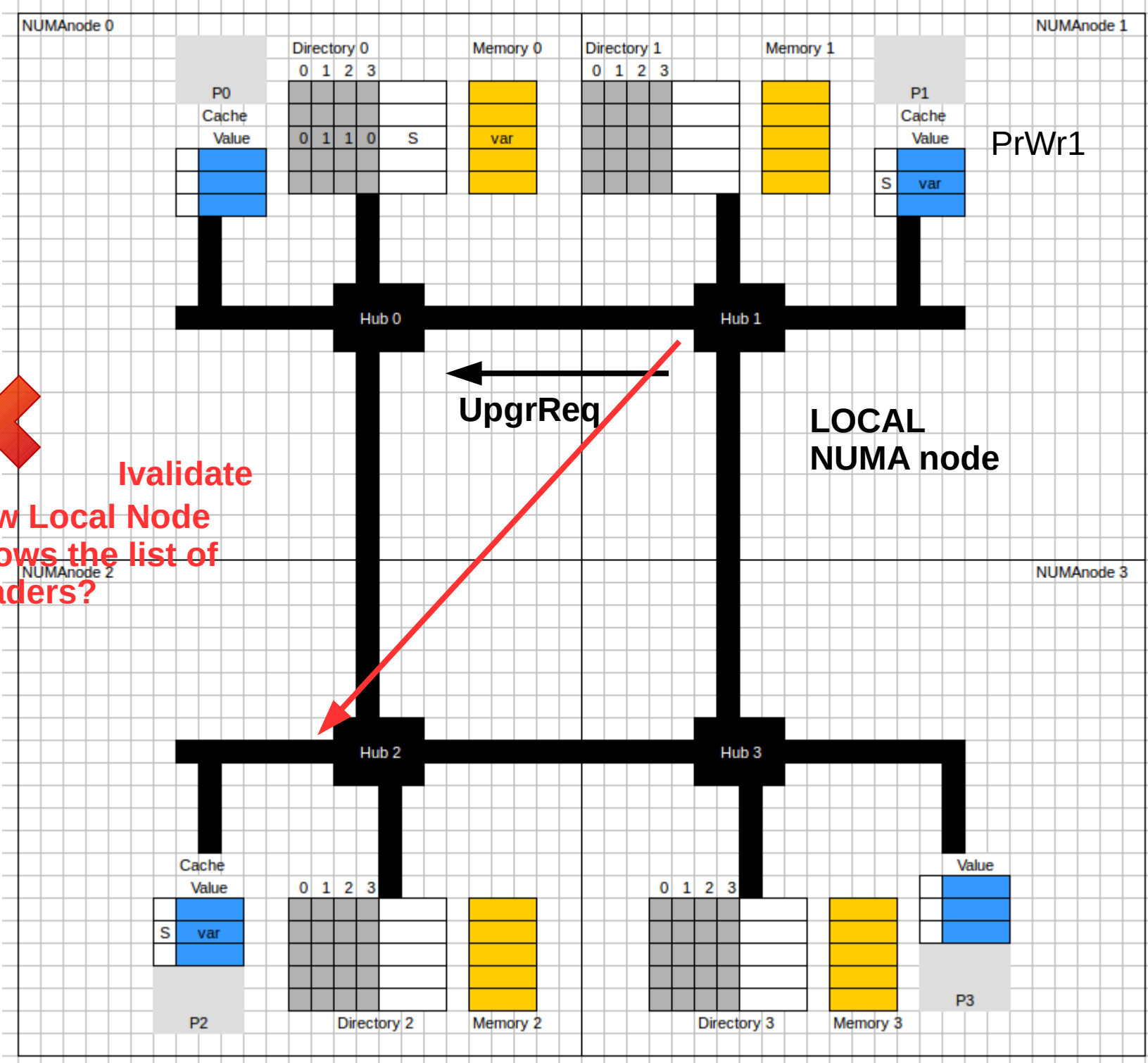






How Local Node  
Knows the list of  
shaders?

Ivalidate



PrWr1

LOCAL  
NUMA node

UpgrReq

Cache  
Value

0 1 2 3

S var

P2

Directory 2

Memory 2

0 1 2 3

Value

Value

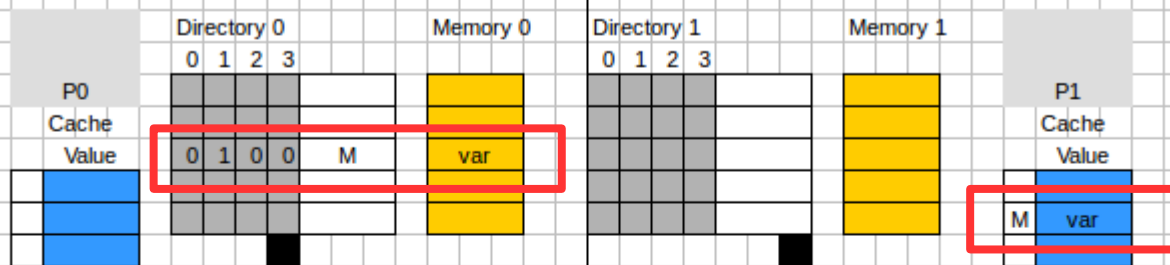
P3

Directory 3

Memory 3

NUMANode 0

NUMANode 1



PrWr1

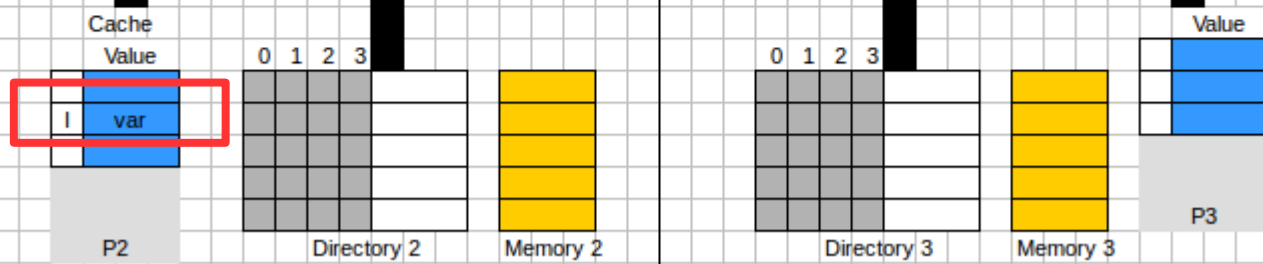
UpgrReq

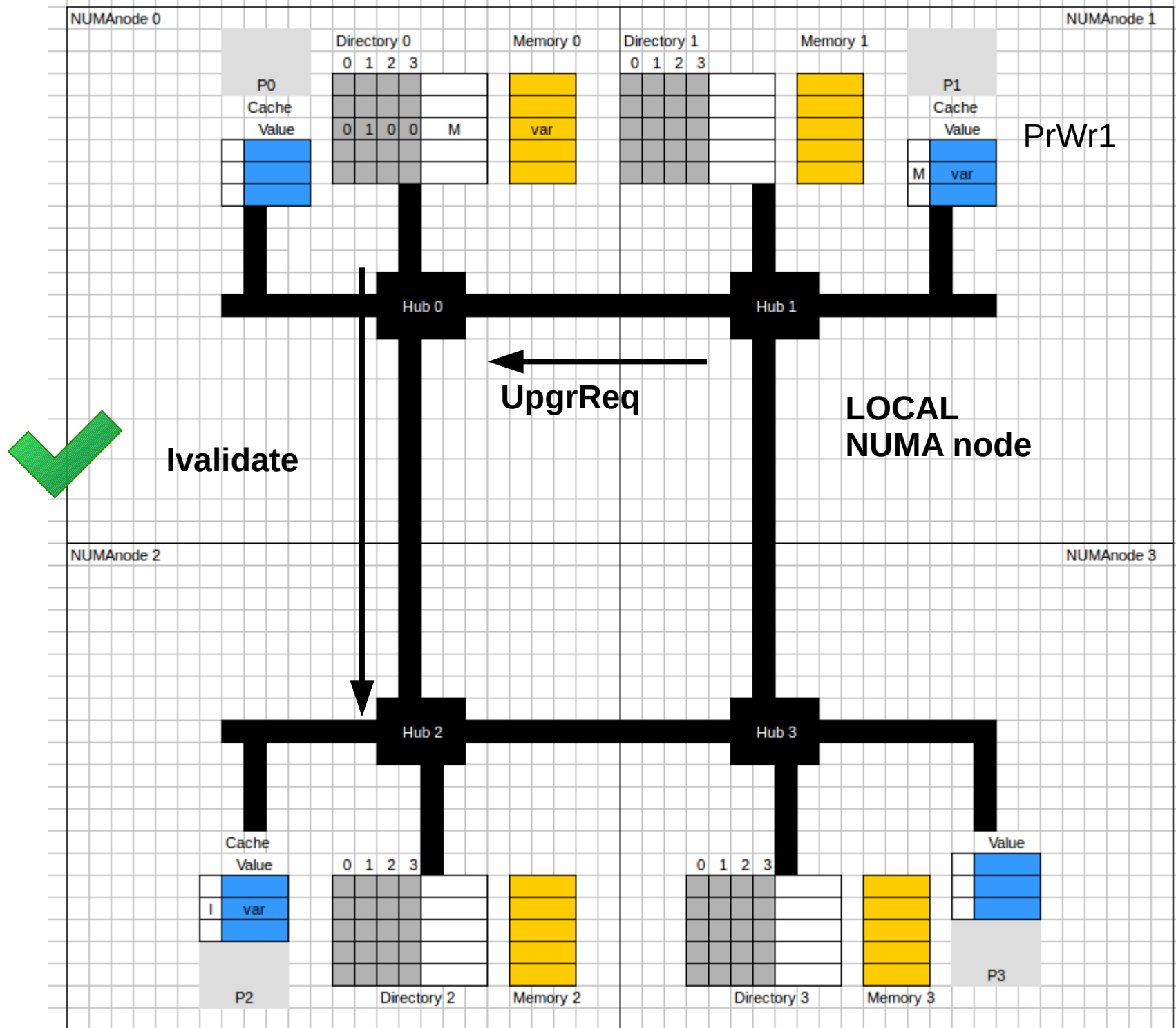
Ivalidate

LOCAL  
NUMA node

NUMANode 2

NUMANode 3







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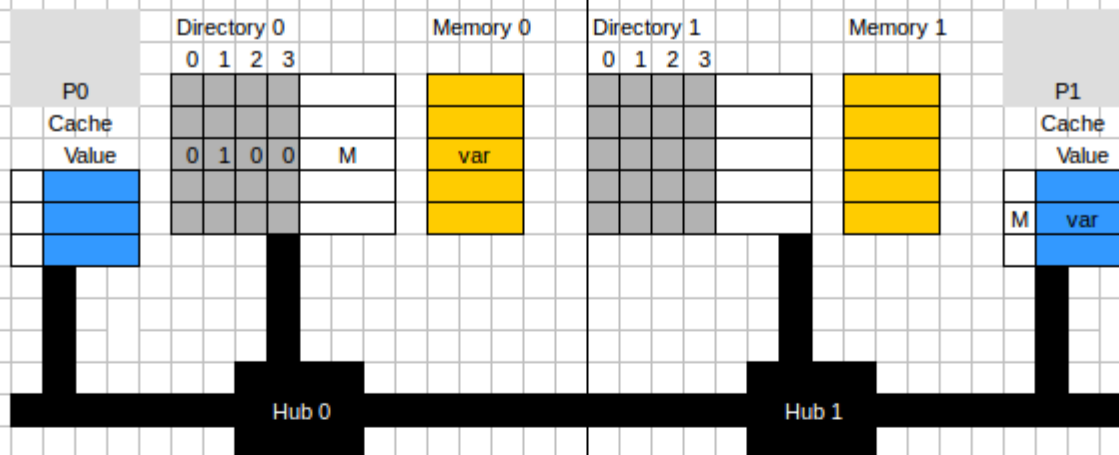
After the previous access, the processor in NUMA node3 reads the same variable *var*. Which of the following coherence actions do NOT occur?

Triu-ne una o més:

- ☐ The processor in the local node (NUMA node3) issues PrRd.
- ☐ The local node issues a Fetch command to the home node (NUMA node0) to obtain the line containing *var*.
- ☐ The home node then issues a Fetch command to the remote NUMA node1 in order to get the most up-to-date copy of the requested memory line.
- ☐ The remote node issues a Dreply command with the only valid copy of the line in the whole system.

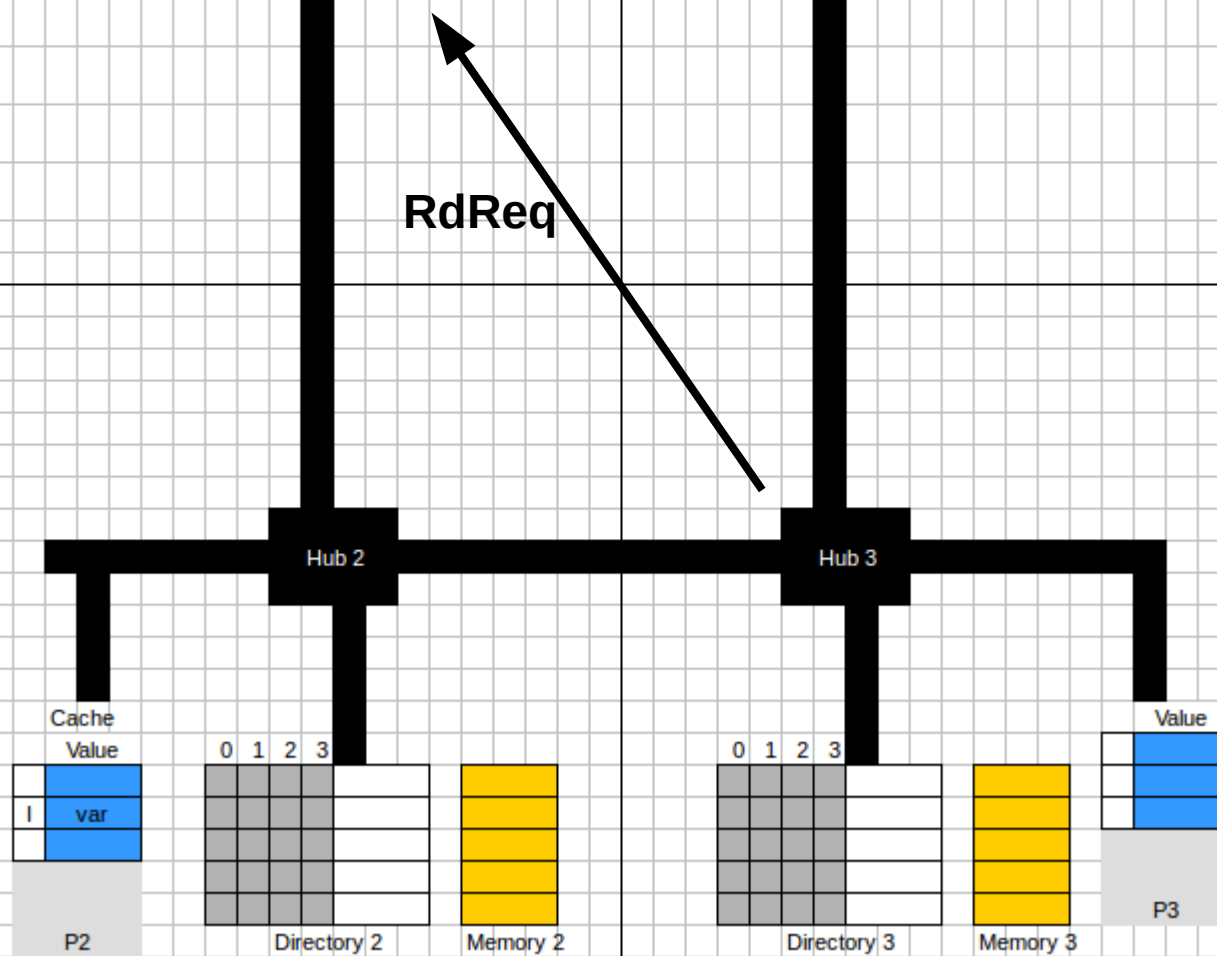
NUMAnode 0

NUMAnode 1



NUMAnode 2

NUMAnode 3

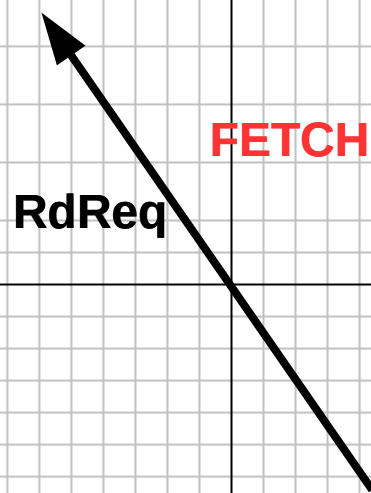
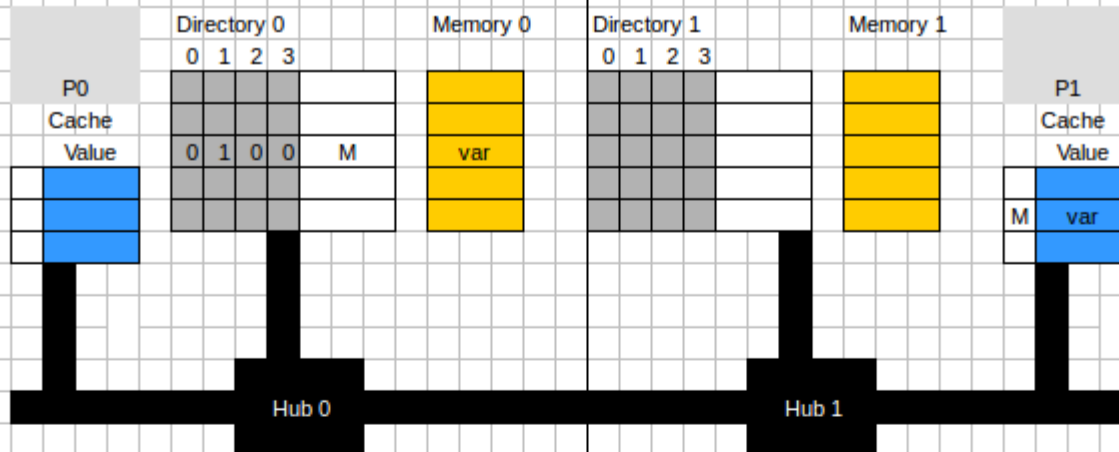


PrRd



NUMAnode 0

NUMAnode 1



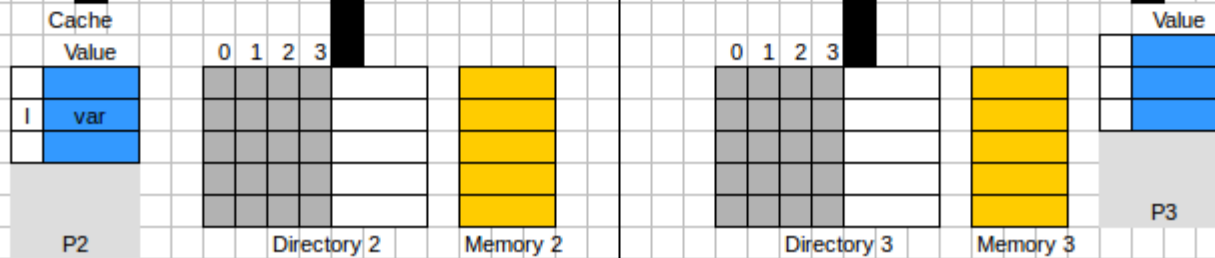
FETCH

Fetch asks  
remote (owner)  
node for a copy  
of line???



NUMAnode 2

NUMAnode 3

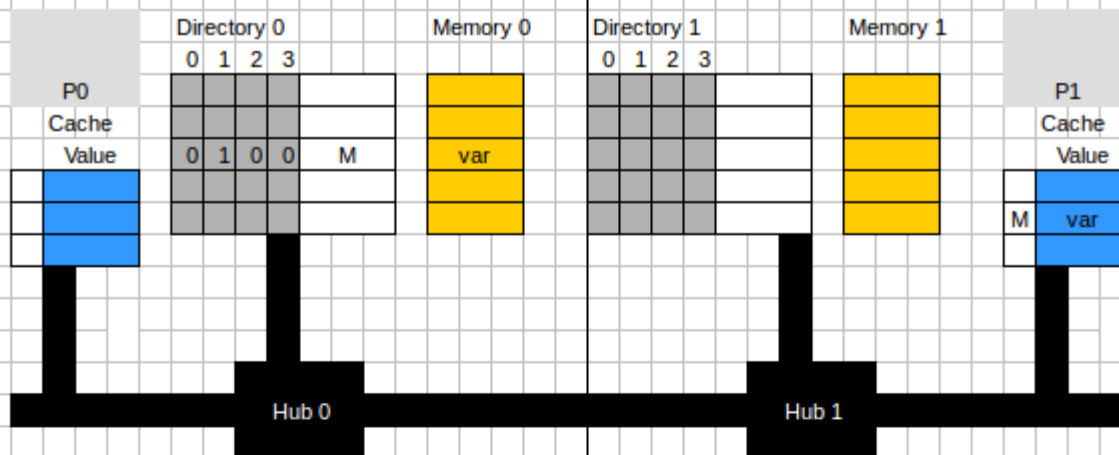


PrRd



NUMAnode 0

NUMAnode 1



Hub 0

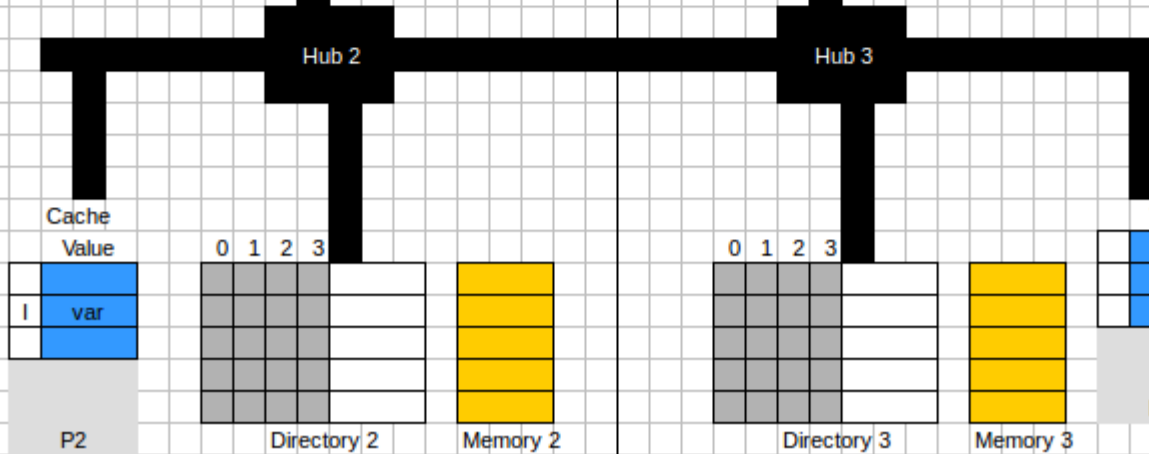
Hub 1

Fetch  
RdReq



NUMAnode 2

NUMAnode 3



Hub 2

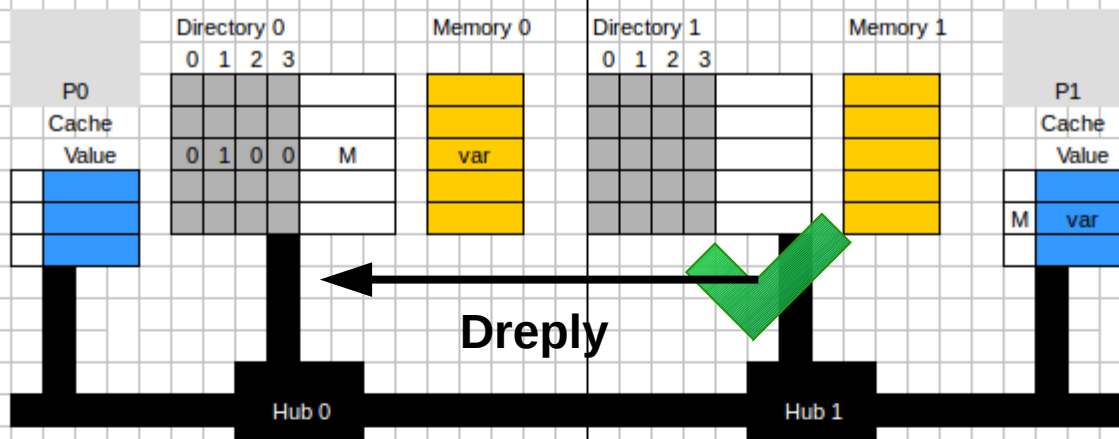
Hub 3

PrRd



NUMAnode 0

NUMAnode 1



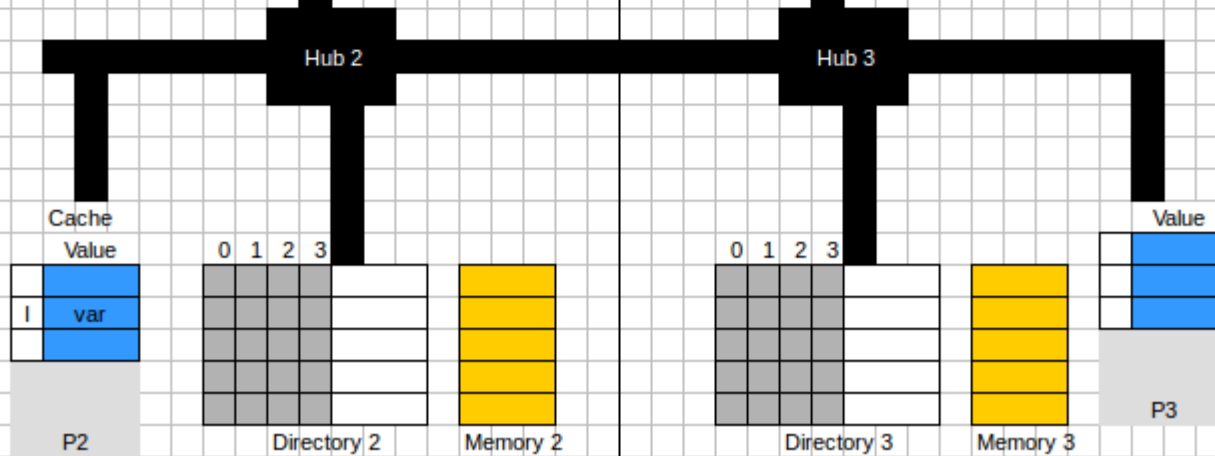
Dreply

Fetch

RdReq

NUMAnode 2


NUMAnode 3



PrRd

After the previous access, the processor in NUMA node 3 reads the same variable *var*. Which of the following coherence actions do NOT occur?

True or false:

- ☐ The processor in the local node (NUMA node 3) issues PrRd.
-  ☐ The local node issues a Fetch command to the home node (NUMA node 0) to obtain the line containing *var*.
- ☐ The home node then issues a Fetch command to the remote NUMA node 1 in order to get the most up-to-date copy of the requested memory line.
- ☐ The remote node issues a Dreply command with the only valid copy of the line in the whole system.