4/4/25, 1:43 PM main.c

src\main.c

```
1 /*
 2
    * HelloWorld.c
 3
 4
    * Created: 3/27/2025 20:13:27 AM
 5
    * Author : Magnus Trip
    */
 6
 7
 8
    #include <stdio.h>
 9
    #include <avr/io.h>
    #include <util/delay.h>
10
11
12
    #include <avr/eeprom.h>
13
   #include "usart.h"
14
15
   #include "i2cmaster.h"
16
    #include "lm75.h"
17
    #include "lcd.h"
18
19
    void delay_ms(uint16_t milliseconds);
    void delay_hs(uint16_t hundred_milliseconds);
20
21
22
    int main(void) {
23
24
     // i2c_init();
25
26
27
      uart_init(); // open the communication to the microcontroller
      io_redirect(); // redirect input and output to the communication
28
29
30
31
32
      // Setting Data direction register for port D as well as enabling pullups
33
      DDRD = 0xff;
34
      DDRD &= \sim(1<<DDD4);
      PORTD |= (1<<PORTD4);
35
36
37
      //Turn on the counter0 to count on external rise on PortD4
38
      TCCR0B = (1 << CS00) | (1 << CS01) | (1 << CS02);
39
40
       //Infinite While loop
       while(1) {
41
42
        //Delay function 1s + a counter counting external rising edges on PD4
43
        delay_hs(10);
44
        printf("%d\n",TCNT0);
45
     /*
46
47
        //500ms Delay + PB5 LED Flashing
48
        PORTB=0x20;
49
        delay_ms(500);
50
        PORTB=0x00;
        delay_ms(500); */
51
```

```
52
 53
 54
         //1s Delay + PB5 LED Flashing
 55
         PORTB=0x20;
         delay_hs(10);
 56
         PORTB=0x00;
 57
 58
         delay_hs(10); */
 59
 60
 61
         //2s Delay + PB5 LED Flashing
         PORTB=0x20;
 62
         delay hs(20);
 63
 64
         PORTB=0x00;
 65
         delay_hs(20); */
 66
 67
        }
 68
        return 0;
 69
      }
 70
     //8-bit Clock for millisecond delay using timer register 0
71
72
     void delay_ms(uint16_t milliseconds){
73
         //Set Mode to CTC
74
         TCCR0A = (1 < WGM01);
75
76
         //Top - set to 1 ms with this: Since 16MHz / 64 = 250 \text{ KHz} : 1/250\text{KHz} = 4\mu\text{s} : 4\mu\text{s} *
     250 ticks = 1ms : max size of 8bit is 255
77
         OCR0A = 249;
78
 79
         // Setting Prescaler to 64
         TCCR0B |=(1<<CS01)|(1<<CS00);
 80
 81
       for (uint16_t i = 0; i < milliseconds; i++)</pre>
 82
 83
       //Delay Start + waiting for Overflow flag
 84
 85
       while ((TIFR0 & (1<<OCF0A)) == 0)
 86
       {
 87
       //Flag Reset at Overflow
 88
 89
       TIFR0 = (1 << OCF0A);
90
91
    }
92
93
    //16 bit Counter for Hundred millisecond delay using timer register
    void delay_hs(uint16_t hundred_milliseconds){
96
       //Set Mode to CTC
97
       TCCR1B =(1<<WGM12);
98
       //Top - Determines the delay time to 100 ms - it should be 100 since the timer resoltion
99
     is 16MHz/64 = 250KHz = 1/250KHz = 4\mu s and 4\mu s * 25000 ticks = 100 ms : Max size of 16bit is
     65535
100
       OCR1A = 24999;
101
102
       // Setting Prescaler to 64
```

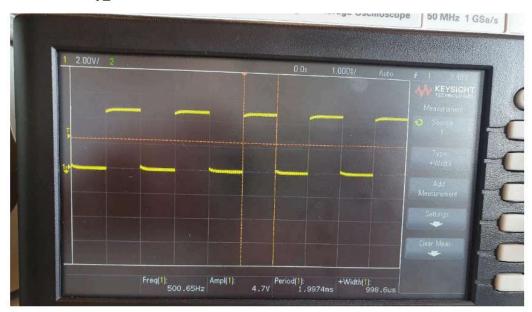
4/4/25, 1:43 PM main.c TCCR1B |=(1<<CS11)|(1<<CS10); 103 104 for (uint16_t i = 0; i < hundred_milliseconds; i++)</pre> 105

```
//Delay Start waiting for Overflow flag
106
107
         while ((TIFR1 & (1<<OCF1A)) == 0)
108
         }
109
         //Flag Reset at Overflow
110
111
         TIFR1 = (1 << OCF1A);
112
       }
113 }
```

Assignment 5 - Timers and counters

Oscilloscope pictures from electrical lab: Using Keysight EDUX1002A

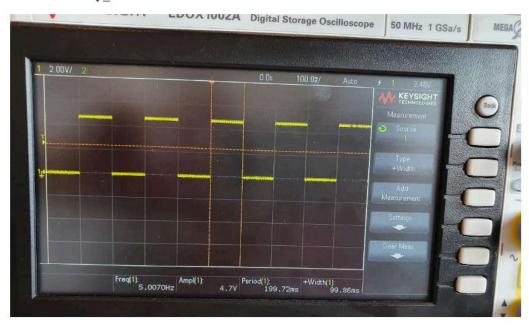
Function: delay ms: 1ms



The delay for is 998,6µs which means that the delay is 99,86% accurate.

$$\frac{998,6 \, ms}{1000 \, ms} \cdot 100 = 99,86\%$$

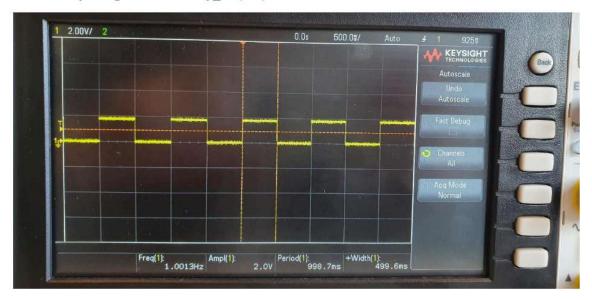
Function: delay_hs: 100ms



The delay is 99,86ms which means that the delay is 99,86% accurate.

$$\frac{99,86 \, ms}{100 \, ms} \cdot 100 = 99,86\%$$

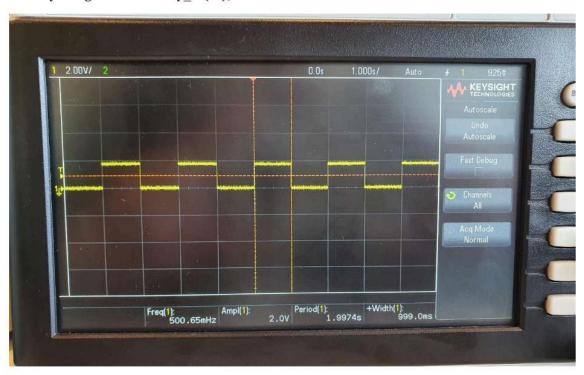
500ms delay using function: delay_ms(500);



The delay is 499,6ms which means that the delay is 99,9% accurate.

$$\frac{499,6}{500} \cdot 100 \approx 99,92 \%$$

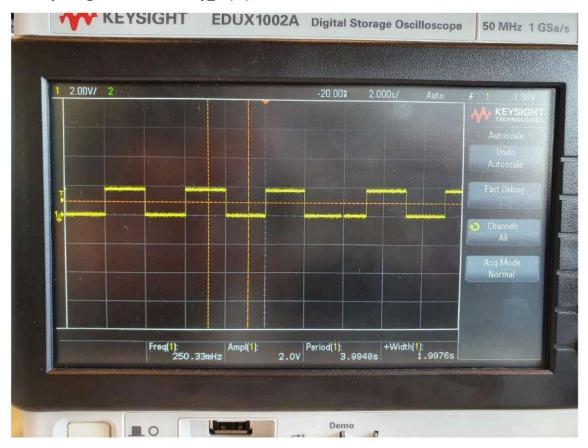
1s delay using function: delay_hs(10);



The delay is 999ms which is 99,9% accurate.

$$\frac{999}{1000} \cdot 100 = 99,9\%$$

2s delay using the function: delay_hs(20);



The delay is 1997,6ms which mean that the delay is roughly 99,9% accurate.

$$\frac{1997,6}{2000} \cdot 100 = 99,88\%$$

Testing the external edge detection of PD4

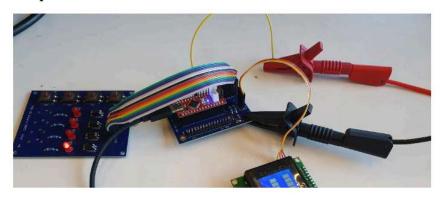
Arbitrary Function Generator: GWINSTEK AFG-2125



Results from rising edge detection on PD4:



Setup:



The red clip is connected to PD4 meanwhile the black clip is connected to ground.