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towboot a Multiboot-compatible bootloader for EFI-based x86 systems

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Introduction: Contents



1 Introduction Goals Motivation

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Introduction



Goals

- UEFI application
- Multiboot-compatible bootloader

Motivation

- firmware of current devices follows the UEFI standard
- Multiboot as the interface between kernel and bootloader
- hhuOS
 - Multiboot-compatible
 - licensing reasons
 - whole operating system

Background: Contents



2 Background

Technologies used

UEFI

Rust

Multiboot

x86

ELF

TOML

Alternatives

Technologies used: UEFI



- Unified Extensible Firmware Interface¹
- Tianocore²
- Open Virtual Machine Firmware³

https://github.com/tianocore/tianocore.github.io/wiki/OVMF (visited on 01/01/2021).

¹ Unified Extensible Firmware Interface (UEFI) Specification, Version 2.8 Errata B, May 2020. UEFI Forum, 2020. URL:

https://uefi.org/sites/default/files/resources/UEFI%20Spec%202.8B%20May%202020.pdf (visited on 10/13/2020).

²tianocore/edk2. URL: https://github.com/tianocore/edk2 (visited on 01/01/2021).

³OVMF. tianocore/tianocore.github.io. URL:

UEFI: Boot Protocol



(without a Compatibility Support Module)

- relocatable PE⁴ files
- loaded by the firmware (or another application)
- same CPU mode as the firmware
- Bootxxxx variables
 - EFI System Partition
- removable media
 - \EFI\BOOT\BOOTarch.EFI

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⁴Portable Executable

UEFI: API



- efi_main function
- System Table
- Boot and Runtime Services
- opaque handles
- protocols with UUIDs

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UEFI: Protocols



- Loaded Image Protocol
- Simple File System Protocol / File Protocol
- Simple Text Input Protocol / Simple Text Output Protocol
- Graphics Output Protocol⁵
- many more possible

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⁵ Replacing VGA, GOP implementation for UEFI. UEFI Summer Plugfest. 2011. URL: https://uefi.org/sites/default/files/resources/UPFS11_P4_UEFI_GOP_AMD.pdf (visited on 10/25/2020).

UEFI: example



```
#![no std]
2 #![no main]
  use uefi::prelude::*;
  use uefi::proto::loaded_image::LoadedImage;
  #[entrv]
  fn efi main(image: Handle, mut systab: SystemTable < Boot >) -> Status {
       uefi services::init(&mut systab).expect("Failed to initialize utilit
       let loaded image = systab.boot services()
               .open protocol exclusive::<LoadedImage>(image)
               .expect("Failed to open loaded image protocol");
10
       let load options = loaded image.load options as cstr16()
11
               .expect("Failed to get load options");
12
       writeln!(systab.stdout(), "got load options: {}", load options);
13
       Status::SUCCESS
15
```

Technologies used: Rust⁶



- system programming language
- · low-level code, high-level abstractions
- type- and memory-safety (excluding unsafe)
- cargo

⁶ Rust Programming Language. URL: https://www.rust-lang.org/ (visited on 01/01/2021).

Rust and UEFI



- i686-unknown-uefi and x86_64-unknown-uefi targets⁷
 - Tier 2
 - no_std
- uefi crate⁸

https://doc.rust-lang.org/nightly/rustc/platform-support.html (visited on 06/23/2023).

**Gabriel Maieri, uefi, crates.jo, URL: https://crates.jo/crates/uefi (visited on 12/04/2020).

⁷ The rustc book. Chap. Platform Support. URL:

Technologies used: Multiboot¹²¹³



- standard for kernels and bootloaders
- from the GNU GRUB⁹ project
 - GNU kernel: gnumach¹⁰ (for GNU HURD¹¹)
- two major versions

⁹GNU GRUB. GNU. URL: https://www.gnu.org/software/grub/ (visited on 10/13/2020).

¹⁰ gnumach. GNU. URL: https://www.gnu.org/software/hurd/microkernel/mach/gnumach.html (visited on 12/05/2020).

¹¹ GNU Hurd. GNU. URL: https://www.gnu.org/software/hurd/(visited on 01/01/2021).

¹²Bryan Ford and Erich Stefan Boleyn. *Multiboot Specification version 0.6.96*. Free Software Foundation, Inc. URL: https://www.gnu.org/software/grub/manual/multiboot/multiboot.html (visited on 10/13/2020).

¹³Bryan Ford and Erich Stefan Boleyn. *Multiboot2 Specification version 2.0.* GNU. Free Software Foundation, Inc. URL: https://www.gnu.org/software/grub/manual/multiboot2/multiboot.html (visited on 10/13/2020).

Technologies used: Multiboot



- load kernel and modules (ELF or flat binary)
 - ELF or flat binary
 - header: static information
 - module alignment
 - require passing memory or video information
 - preferred video mode
 - binary loading and entry
 - additional flags

Technologies used: Multiboot



- pass information about the system
 - struct, EAX and EBX registers
 - memory (upper, lower, map)
 - kernel command line
 - modules (and command line)
 - svmbol
 - bootloader name
 - framebuffer
 - legacy stuff (boot device, drives, config table, APM, VBE)
 - on Multiboot 2: SMBIOS, ACPI, UEFI, network
 - machine state
 - 32-bit Protected Mode
 - no interrupts
 - no paging
 - no PAE (in practice)
 - Multiboot 2: also native UEFI mode

Technologies used: Multiboot



- multiboot crate¹⁴. PR¹⁵
- multiboot2 crate¹⁶. PR¹⁷

¹⁴ Gerd Zellweger. *multiboot*. crates.io. URL: https://crates.io/crates/multiboot (visited on 01/04/2021).

¹⁵Niklas Sombert. *Add some functionality for using this in bootloaders (Pull Request 8)*. gz/rust-multiboot. URL: https://github.com/gz/rust-multiboot/pull/8 (visited on 01/04/2021).

¹⁶Philipp Oppermann. *multiboot2*. crates.io. URL: https://crates.io/crates/multiboot2 (visited on 06/23/2023).

¹⁷Niklas Sombert. *Add a builder to multiboot2 (Pull Request 133)*. rust-osdev/multiboot2. URL: https://github.com/rust-osdev/multiboot2/pull/133 (visited on 06/23/2023).

Technologies used: x86



- i686 (32-bit) and x86_64 (64-bit)
- multiple modes¹⁸
 - Long Mode
 - 64-Bit Mode
 - Compatibility Mode (32-Bit)
 - Legacy Mode
 - Protected Mode (32-Bit or 16-Bit)
 - Virtual-8086 Mode (16-Bit)
 - Real Mode (16-Bit)
- Alternatives:
 - MIPS (Multiboot 2)
 - Itanium, ARM, RISC-V (UEFI)

¹⁸ AMD64 Architecture Programmer's Manual. 2020. Chap. 1.2 Modes of Operation. URL: https://www.amd.com/system/files/TechDocs/40332.pdf (visited on 01/04/2021).

Technologies used: x86



- Multiboot machine state requires many x86-specifics
 - native mode to 32-bit Protected Mode
 - may have to switch through Compatibility Mode
- memory
 - Real Mode (8086): 16-bit pointers, 24-bit address bus, 1MB, memory hole, lower 640KB
 - 286: 24-bit address bus: 1MB to approx 10MB
 - i686: 4GB virtually, physically (more with PAE)
 - x86_64: physically 256TB memory
 - segmentation

Technologies used: x86: segmentation on the 8086



• in Real Mode, segment registers are offsets

```
mov [ax], 0xab; writes to *(ds * 16 + ax)
mov es:[ax], 0xab; similar, but with es
jmp 0xabc; actually jumps to cs * 16 + 0xabc ("near-jump")
```

• they're settable, usually

```
mov ds, 0x12
jmp 0xde:0xabc; cs can only be set with a "far-jump"
```

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Technologies used: x86: segmentation on the 286



- in Protected Mode, segment registers are indices into the Global Descriptor Table
- for a very simple OS with just one application it might look like this:

index	content
0	NULL
1	system code
2	system data
3	application code
4	application data

- information contained in these Segment Descriptors:
 - base, limit, access, flags
 - not layed out sequentially!
- x86 crate¹⁹ has a builder

¹⁹Gerd Zellweger. x86. crates.io. URL: https://crates.io/crates/x86 (visited on 06/30/2023).

Technologies used: x86: code (1/3)



```
let code_segment_builder = SegmentDescriptorBuilder::code_descriptor(
      0, u32::MAX, CodeSegmentType::ExecuteRead,
  );
  let code segment: Descriptor = code segment builder
       .present().limit granularity 4kb().db().finish();
  let data segment builder = SegmentDescriptorBuilder::data descriptor(
      0, u32::MAX, DataSegmentType::ReadWrite,
  );
  let data segment: Descriptor = data segment builder
       .present().limit granularity 4kb().db().finish();
10
  let gdt = DescriptorTablePointer::new_from_slice(
       &[Descriptor::NULL, code_segment, data_segment]
```

Technologies used: x86: code (2/3)



```
unsafe { x86::irg::disable();
       x86::dtables::lqdt(&qdt);
       x86::dtables::lidt(&DescriptorTablePointer::default()); // invalid
       asm! ("push 0x08", // code segment
           "lea rbx, [2f]",
           "push rbx".
           "retfq", // overwrite CS
           "2:", // compatibility mode, yay.
           ".code32".
           "mov eax, 0x10", // data segment
           "mov ds, eax",
           "mov es, eax",
           "mov fs, eax",
13
           "mov qs, eax",
           "mov ss, eax",
15
```

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Technologies used: x86: code (3/3)



```
"mov ecx, cr0",
           "and ecx, \sim (1 << 31)", // disable paging
           "or ecx, 1", // enable protected mode
           "mov cr0, ecx",
           "mov ecx, cr4",
           "and ecx, \sim (1 << 5)", // disable PAE
           "mov cr4, ecx",
           "mov ecx, 0xC0000080",
           "rdmsr",
           "and eax, \sim (1 << 8)", // disable long mode
           "wrmsr",
13
       );
```

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Technologies used: ELF



- Executable and Linkable Format²⁰
- support needed to be Multiboot-compliant
- header
- Program Header
- Section Header
- goblin crate²¹

²⁰Wikipedia contributors. Executable and Linkable Format — Wikipedia, The Free Encyclopedia. 2020. URL:

^{//}en.wikipedia.org/w/index.php?title=Executable_and_Linkable_Format&oldid=992446183 (visited on 01/04/2021).

²¹m4b et al. *qoblin*. crates.io. URL: https://crates.io/crates/goblin (visited on 01/04/2021).

Technologies used: TOML



- Tom's Obvious Minimal Language²²
- format for configuration files
- simple, yet type-safe and well-defined
- toml crate²³, PR²⁴

²²Tom Preston-Werner. *TOML: Tom's Obvious Minimal Language*. URL: https://toml.io/(visited on 10/14/2020).

²³Eric Huss and Alex Crichton. *toml.* crates.io. URL: https://crates.io/crates/toml (visited on 01/04/2021).

²⁴Thom Chiovoloni. *Added nostd support (Pull Request 429)*. alexcrichton/toml-rs. URL: https://github.com/alexcrichton/toml-rs/pull/429 (visited on 04/14/2022).

Alternatives



- GNU GRUB²⁵
 - widely used bootloader
 - supports various platforms and operating systems
 - menu, can edit entries interactively
 - complex scripting language
 - de-facto reference implementation of Multiboot
- Syslinux²⁶
 - very lightweight bootloader
 - supports multiple platforms and some operating systems
 - current release 2014
- Limine²⁷
 - supports various platforms and operating systems
 - also has an own boot protocol

²⁵ GNU GRUB.

²⁶ The Syslinux Project. URL: https://www.syslinux.org/ (visited on 01/02/2021).

²⁷Limine. URL: https://limine-bootloader.org/(visited on 06/08/2023).

Implementation and Usage: Contents



3 Implementation and Usage

Boot process

Configuration

NVRAM

Command line parameters

Configuration file

Memory Management

Stack

Heap

Whole pages

Workarounds

Changes made to other software

multiboot crates

hhuOS

miniarg

multiboot12

Boot process



- initialize the bootloader
- 2 determine what to boot
 - 1 parse the command line parameters
 - 2 parse the configuration file
 - 3 display a menu
- 3 boot the operating system
 - load the kernel
 - 2 load the modules
 - 3 configure the graphics output
 - 4 prepare the information to pass to the kernel
 - 5 deinitialize most parts of the bootloader
 - 6 jump to the kernel's entry point

Configuration: NVRAM



- binary encoding: CBOR²⁸ or JSON²⁹ (for instance)
- EFI variable
- requires tooling
- bound to a system
- firmware might "forget" configuration

²⁸Carsten Bormann. *Concise Binary Object Representation*. URL: https://cbor.io (visited on 10/14/2020). ²⁹Douglas Crockford. *Introducing JSON*. URL: https://www.ison.org/ (visited on 10/14/2020).

Configuration: Command line parameters



```
FSO:\> towboot.efi -help
Usage:
-config Load the specified configuration file instead of the default one.
-kernel Don't load a configuration file, instead boot the specified kernel.
-logLevel Set the log level. (This only applies if '-kernel' is specified.)
-module Load a module with the given args. Can be specified multiple times.
-quirk Enable a specific quirk. (Only applies when loading a kernel.)
-help Displays all available options and how to use them.
-version Displays the version of towboot
```

- towboot.efi -kernel "kernel.elf quiet" -module "module1.bin -verbose" -module "module2.bin -quiet"
- towboot.efi -config config.toml
- file names or arguments may contain spaces or quotes
- Bootxxxx variables
- shell or from another bootloader
- boot manager in the firmware usually not very user-friendly
- firmware might "forget" configuration

Configuration: Configuration file



```
default = "entry1"
                                      12
   timeout = 10
   # log level defaults to 'info'
                                      15
   [entries]
                                      16
     [entries.entry1]
                                      17
       name = "Operating System"
       image = "kernel.elf"
       argv = "quiet"
                                      20
       quirks = ["KeepResolution"] 21
11
```

```
[[entries.entry1.modules]]
  image = "module1.bin"
  argv = "--verbose"

[[entries.entry1.modules]]
  image = "module2.bin"
  argv = "--quiet"
```

- easy to read and modify by hand
- most bootloaders do this
- towboot.toml, placed in top-level directory of the ESP

Memory Management: Stack



- local variables
- works always
- size needs to be known at compile time
- used for most runtime data, for example MultibootInfo
- tracked by rustc at compile time

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Memory Management: Heap



- alloc::alloc::alloc is bound to allocate_pool
- tracked both by rustc at compile time and by the firmware at runtime
- used for everything with a dynamic size and no further requirements

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Memory Management: Whole pages



- can be allocated to specific address (used for the kernel)
- custom mem::Allocation wrapper (impl Drop)
- tracked by the firmware at runtime
- also used for modules

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Workarounds



- stack probes³⁰ broken with Rust 1.49 (fixed with 1.51, at least)
- initially no support for floating point operations (now just for x86_64)
- hacks module

³⁰probestack.rs. rust-lang/compiler-builtins. URL: https://github.com/rust-lang/compiler-builtins/blob/63ccaf11f08fb5d0b39cc33884c5ala63f547ace/src/probestack.rs (visited on 01/04/2021).

Changes made to other software: multiboot crates



- multiboot crate³¹ was designed for use in kernels
- added support for parsing the header, for setting values in MultibootInfo and tests³²
- most difficult part: integration with Rust's and UEFI's memory management
- the same goes for multiboot23334

³¹Zellweger, *multiboot*.

³²Sombert, Add some functionality for using this in bootloaders (Pull Request 8).

³³Oppermann. *multiboot2*.

³⁴Sombert. Add a builder to multiboot2 (Pull Request 133).

Changes made to other software: hhuOS



- kernel assumed the Multiboot structs to be before the kernel in memory
- PR³⁵ to copy them to BSS very early
- kernel also looks for the free memory after data passed by the bootloader

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³⁵Niklas Sombert. *Copy the Multiboot info struct recursively to BSS before enabling paging (Pull Request 19)*. hhuOS/hhuOS. URL: https://github.com/hhuOS/hhuOS/pull/19 (visited on 01/09/2021).

Changes made to other software: miniarg



- parses strings such as program -foo "bar baz"
- no_std-compatible
- released to crates.io³⁶
- not specific to UEFI or Multiboot

³⁶Niklas Sombert. *miniarq*. crates.io. URL: https://crates.io/crates/miniarq (visited on 12/05/2020).

Changes made to other software: multiboot12



- abstraction over multiboot and multiboot2 crates
- currently just laying around on GitHub³⁷

Testing: Contents



4 Testing

Components Bootloader

Testing



Components

- unit tests for multiboot, multiboot2 and miniarg
- not (yet?) for multiboot12

Bootloader

- manual end-to-end test, could be automated?
- Multiboot specs³⁸³⁹ contain example kernels
- had to be modified to print to serial
- booting a whole operating system
- virtual machine

³⁸Ford and Boleyn, *Multiboot Specification version 0.6.96*.

³⁹Ford and Boleyn, *Multiboot2 Specification version 2.0*.

Conclusion: Contents



5 Conclusion

Testing Summary Further Impr

Further Improvements

Code

Conclusion: Testing



- successfully tested with the test kernel, hhuOS⁴⁰, GNU HURD⁴¹, Lemon OS⁴², HelenOS⁴³ and NetBSD⁴⁴
- tests with FlingOS⁴⁵ and OpenIndiana⁴⁶ failed

⁴⁰hhuOS. URL: https://hhuos.github.io/(visited on 01/01/2021).

⁴¹ GNU Hurd.

⁴²Lemon OS. URL: https://lemonos.org/ (visited on 06/30/2023).

⁴³ HelenOS. URL: http://www.helenos.org/ (visited on 06/30/2023).

⁴⁴ NetBSD. URL: https://netbsd.org/ (visited on 06/30/2023).

⁴⁵ Edward Nutting. FlingOS. URL: http://www.flingos.co.uk/ (visited on 01/01/2021).

⁴⁶ OpenIndiana. URL: https://www.openindiana.org/(visited on 06/30/2023).

Conclusion: Summary



- UEFI's abstractions make writing a bootloader very straight-forward
- uefi-rs maps them to Rust
- Rust support is good, but could be better
- Multiboot requires x86-specifics

Conclusion: Further Improvements



- Secure Boot⁴⁷
 - enabled by default on most modern systems
 - changes to kernels and the kernel file format needed to be secure
- 64-bit, UEFI-unaware kernels
- other CPU architectures?
- load improvements
- file paths

⁴⁷Richard Wilkins and Brian Richardson. *UEFI Secure Boot In Modern Computer Security Solutions*. 2013. URL: https://uefi.org/sites/default/files/resources/UEFI_Secure_Boot_in_Modern_Computer_Security_Solutions_2019.pdf (visited on 10/25/2020).



hhuOS/towboot



a bootloader for Multiboot kernels on UEFI systems written in Rust



https://github.com/hhuOS/towboot

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