**You may use this cache to test your small cache, if you prefer**

|  |  |  |  |
| --- | --- | --- | --- |
| **Instruction List** | | | |
| **Number** | **Operation** | **Mem. Address** | **Hit or Miss?** |
| 1 | READ | 000100 |  |
| 2 | READ | 010000 |  |
| 3 | READ | 011000 |  |
| 4 | READ | 101000 |  |
| 5 | READ | 111100 |  |
| 6 | WRITE(32) | 100000 |  |
| 7 | READ | 000000 |  |
| 8 | READ | 110100 |  |
| 9 | READ | 011100 |  |
| 10 | WRITE (0) | 00000 |  |
| 11 | READ | 100000 |  |
| 12 | READ | 101100 |  |
| 13 | READ | 00000 |  |
| 14 | READ | 111100 |  |

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Two-Way Set Associative Cache**  ***(contains data from previously executed instructions)*** | | | | |
| **Set** | **Valid** | **Tag** | **Last** | **Data (line size = 4 bytes)** |
| 00 |  |  |  |  |
|  |  |  |  |
| 01 |  |  |  |  |
|  |  |  |  |
| 10 |  |  |  |  |
|  |  |  |  |
| 11 |  |  |  |  |
|  |  |  |  |