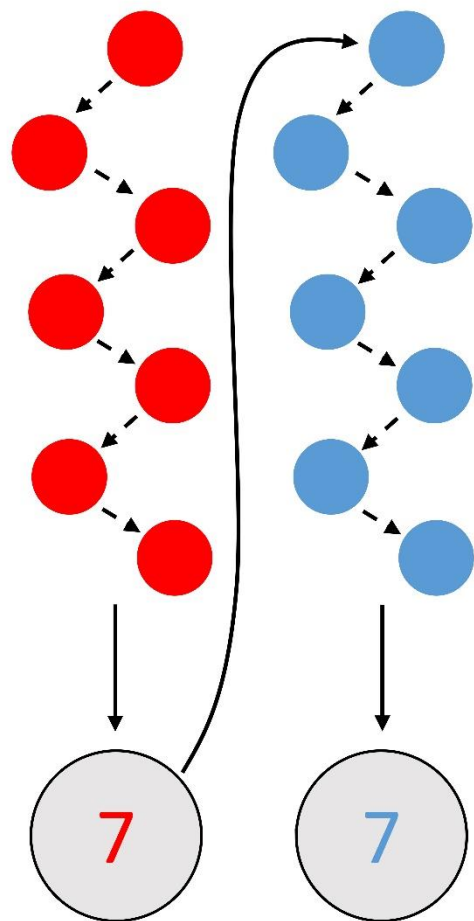
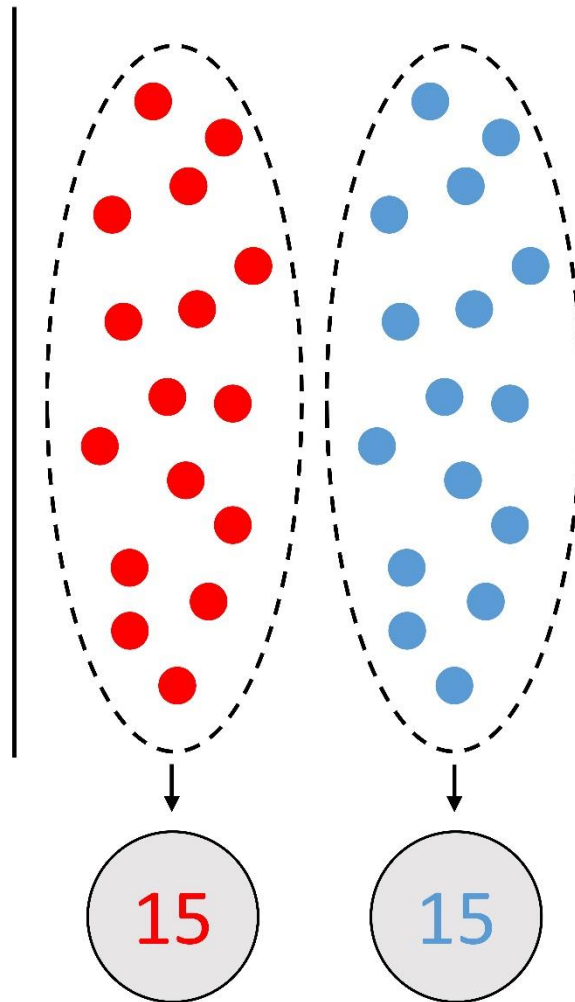


a.

Serial Counting System Architecture

**b.**

Parallel Estimation System Architecture

**c.**

Parallel Subitizing System Architecture

