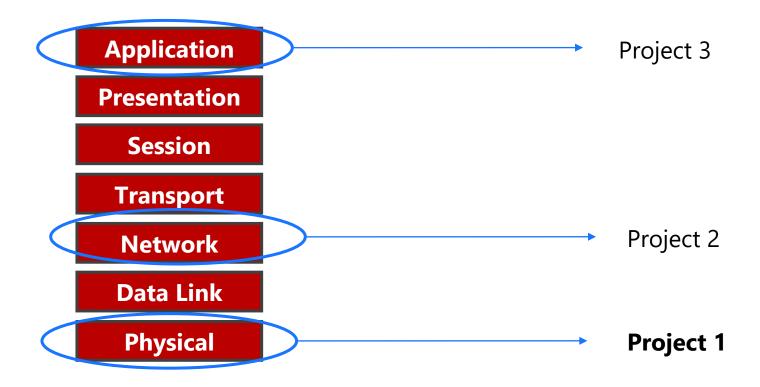
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Recitation 1: Exploring the WiFi Physical Layer

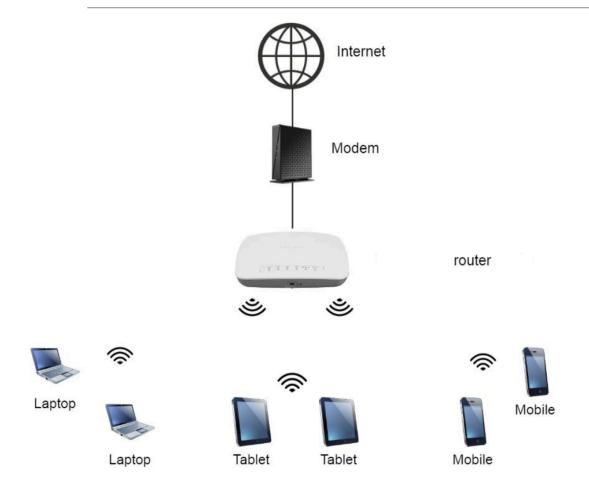
FEBRUARY 12, 2021

Atul Bansal

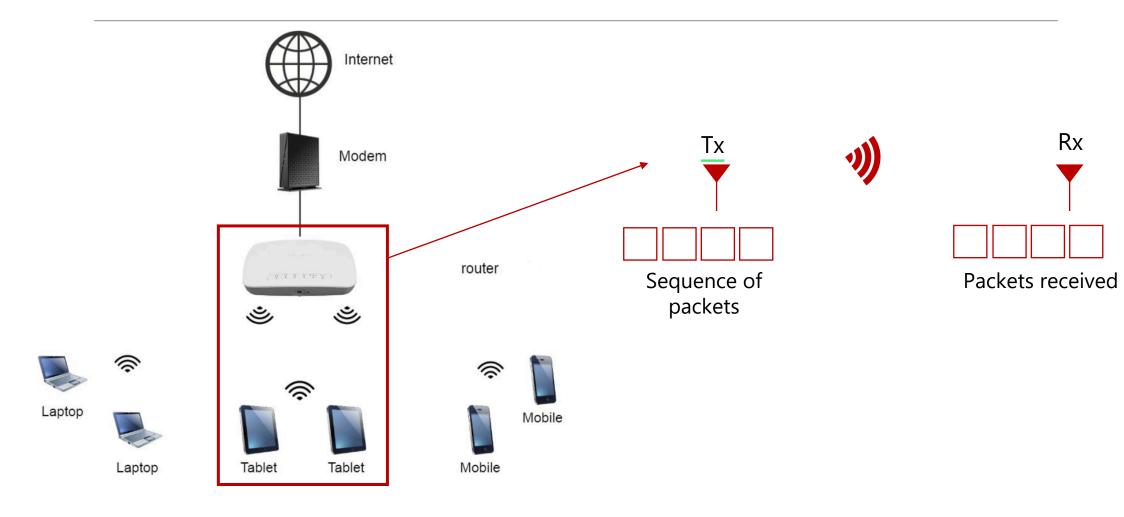
Open Systems Interconnection Model



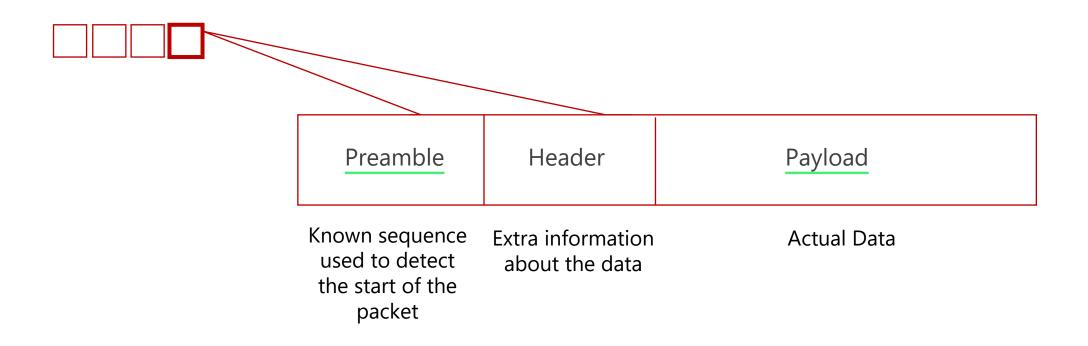
Project 1: Physical Layer(WiFi)



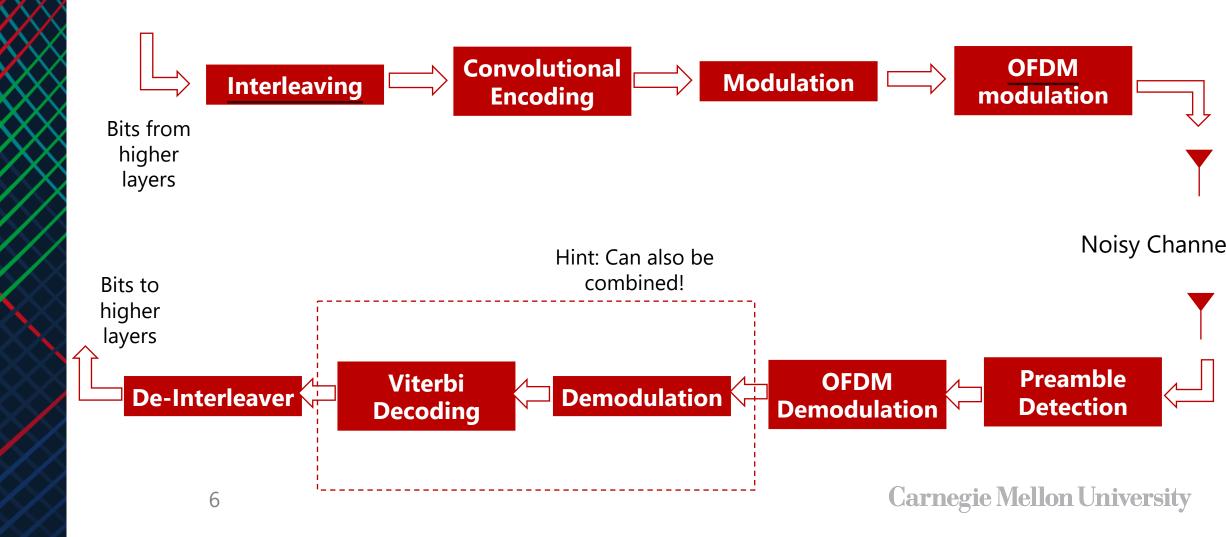
Project 1: Physical Layer(WiFi)



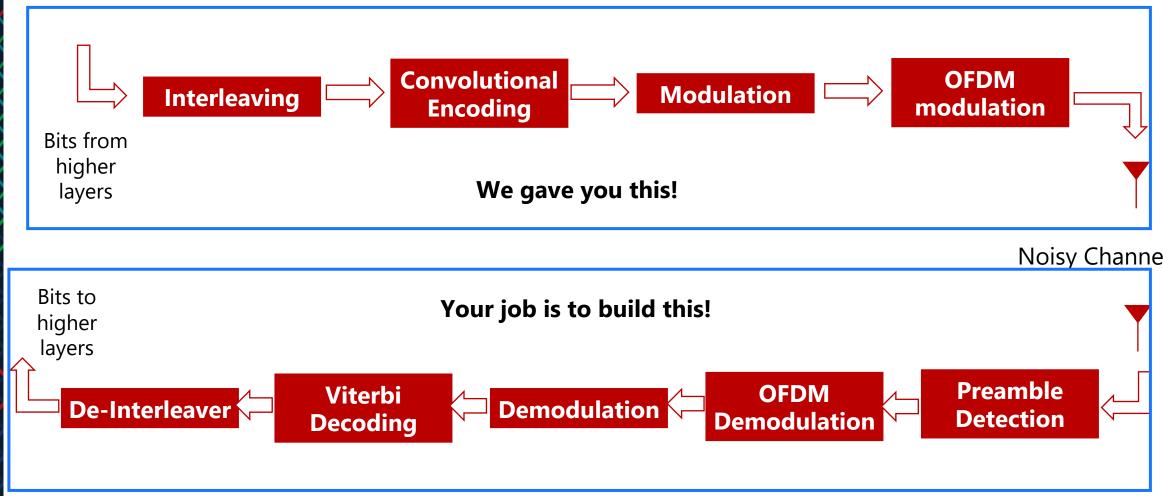
WiFi Packet



How WiFi packets are created?



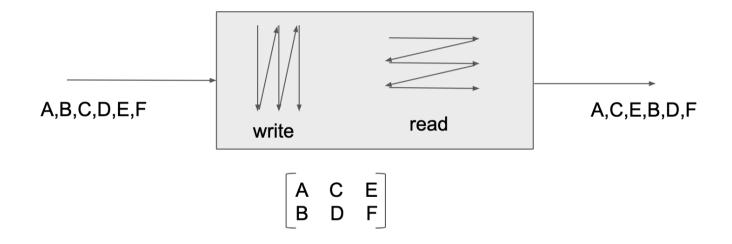
How WiFi packets are created?



Level 1: Interleaving

Input bits are rearranged by a well-known permutation pattern

Interleaver: Rows = 2, Columns = 3



Resilient to burst errors

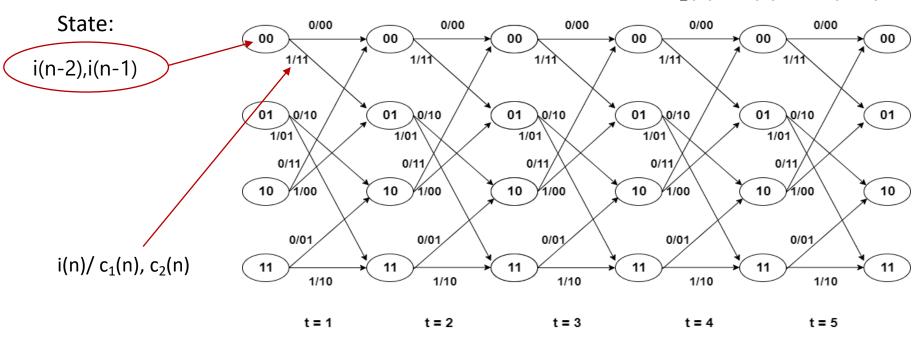
Level 2: Convolutional Coding

Encode the input bits using a 'special' polynomial to add redundancy.

For this project, we use a $\frac{1}{2}$ rate encoder with G(7,5) generator polynomial

Input Bits: i(n), i(n-1), i(n-2)

Output Bits: $c_1(n) = i(n) xor i(n-1) xor i(n-2)$ $c_2(n) = i(n) xor i(n-2)$

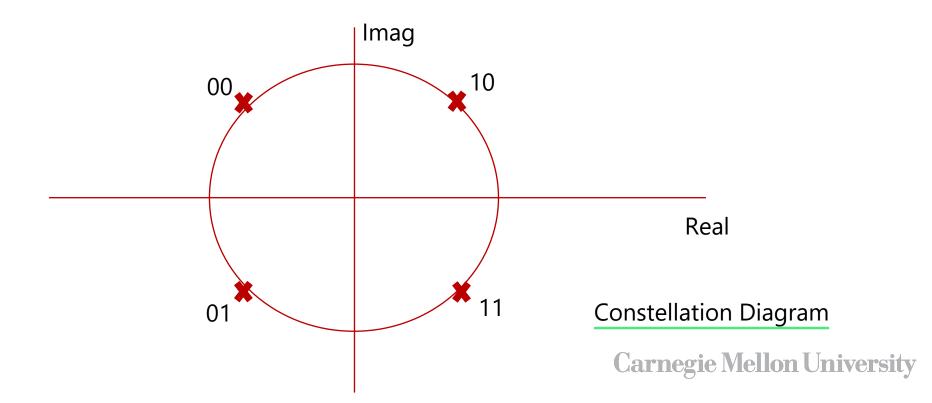


Level 3: Modulation

10

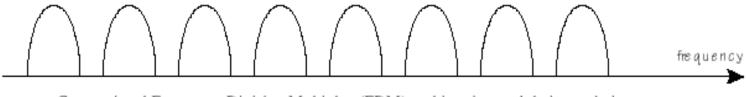
Coded Bits to Symbol Mapping: BPSK, QPSK, 4-QAM

Conveying information by changing the signal and represent them in complex domain. We use 4-QAM in this project.

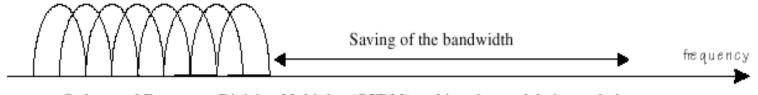


Level 4: Orthogonal Frequency Division Multiplexing (OFDM)

Divide the available bandwidth into multiple orthogonal subcarriers



Conventional Frequency Division Multiplex (FDM) multicarrier modulation technique



Orthogonal Frequency Division Multiplex (OFDM) multicarrier modulation technique

Some excellent properties: spectral efficiency, robustness to fading and inter-symbol interference

Level 5: Padding and Noise

Random number of padding bits are added before and after the information symbols

Used to model wireless channel.

Detect beginning and end of info symbols (useful symbols)

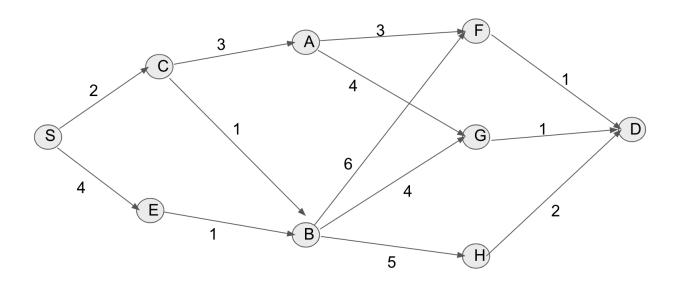
Beginning: Preamble

End: ??

Viterbi Decoding

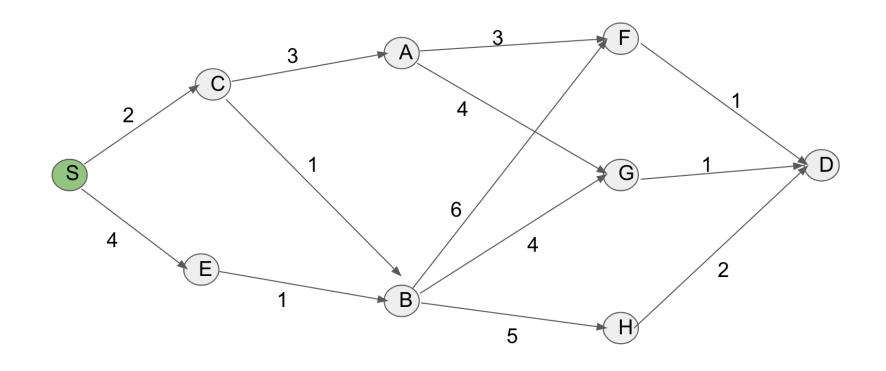
Used at WiFi receivers to inverse Convolutional coding

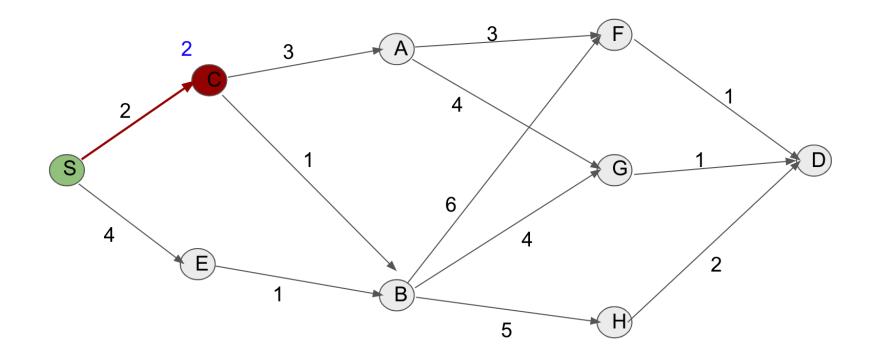
Before that, consider this problem

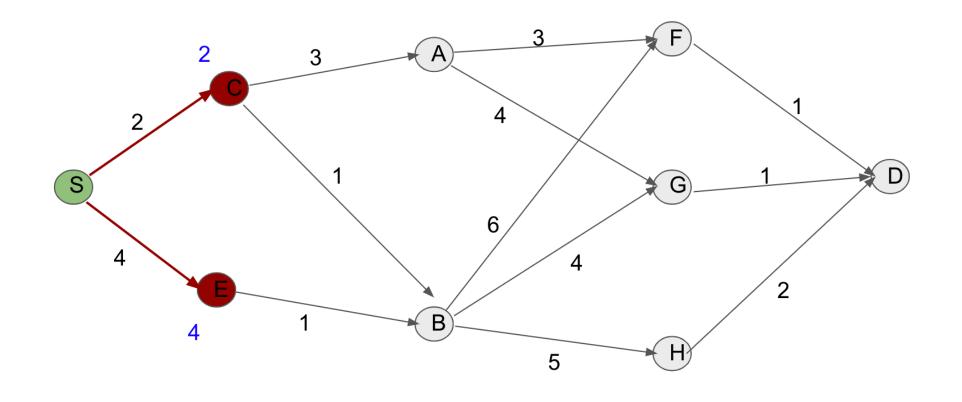


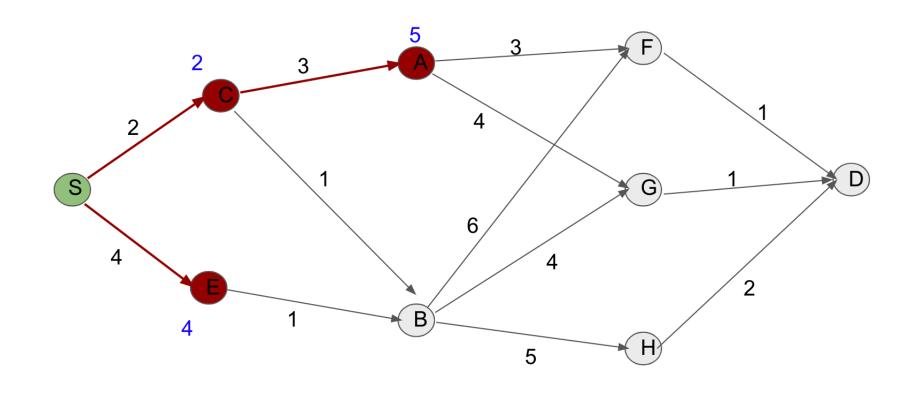
Find shortest distance from S to D?

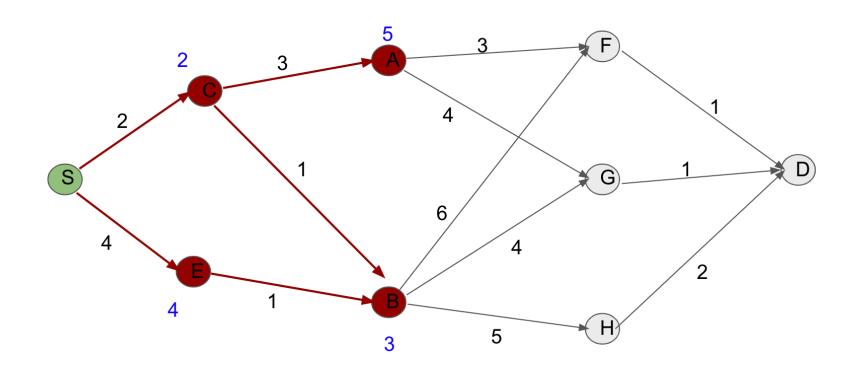
Use Dynamic Programming/Dijkstra's algorithm

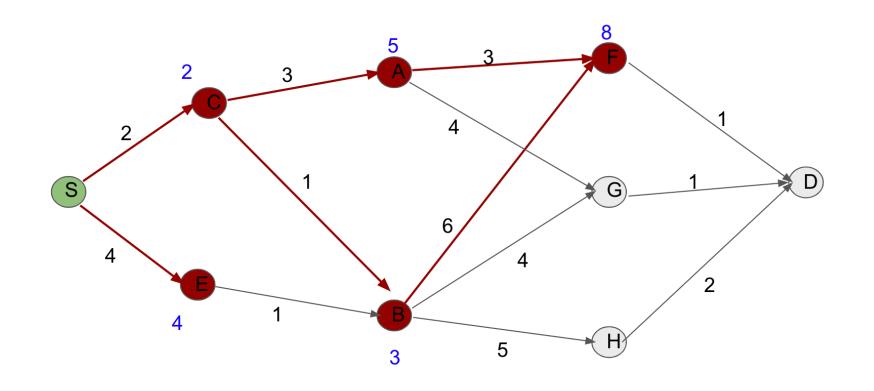


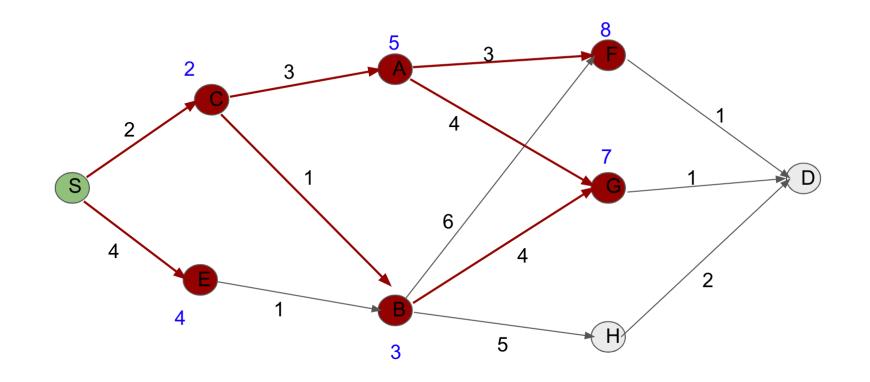


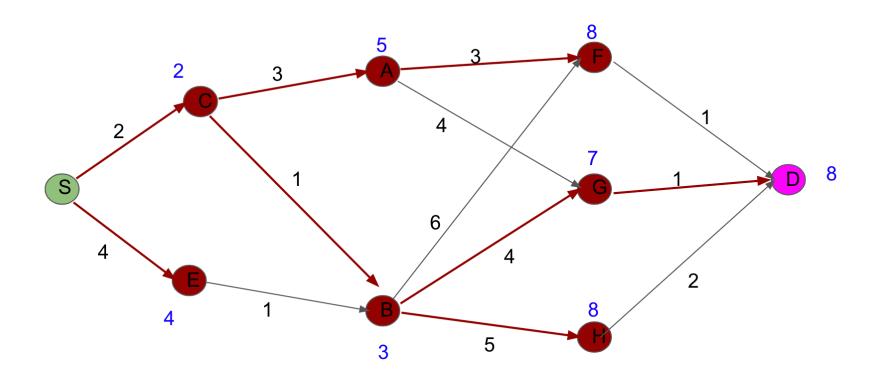






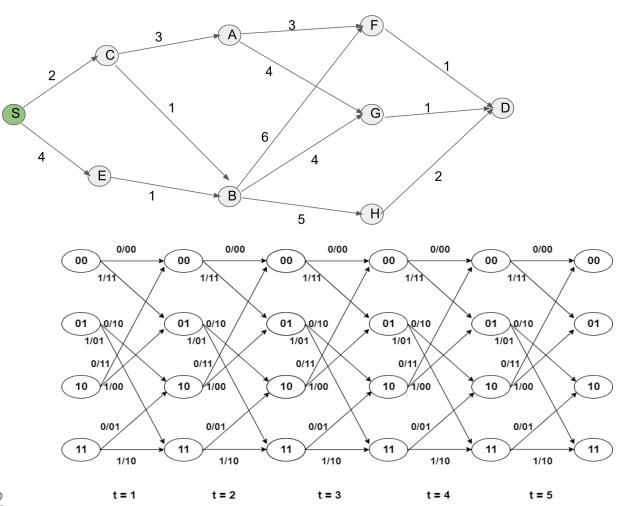






Shortest path from S to D: S-C-B-G-D

Looks similar



Starting Node = '00' state

Destination node 'D' = any rightmost state

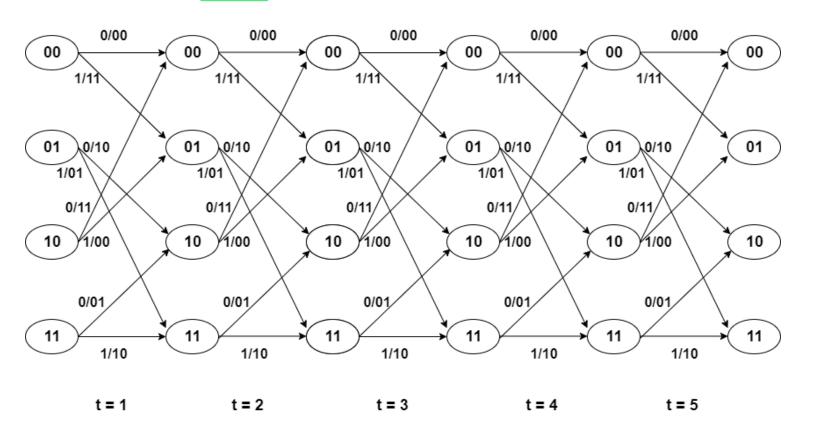
Adjacent node distance = branch metric

Received symbols: z₀,z₁,z₂,z₃,z₄

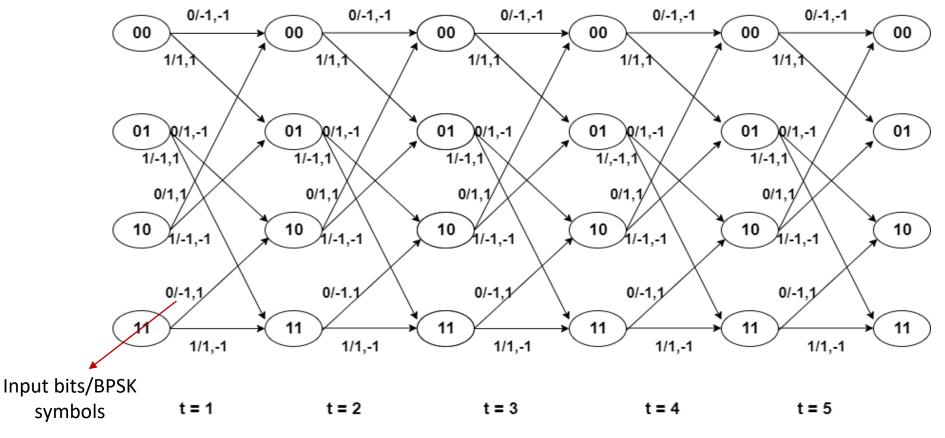
Code symbols: x_0, x_1, x_2, x_3, x_4

Branch metric = $|z_0 - x_0|^2 + |z_1 - x_1|^2$ and so on...

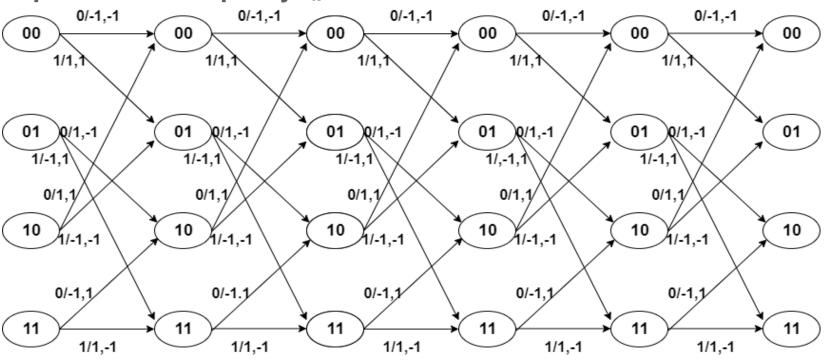
Step 1: Prepare Trellis



Step 2: Label using symbols $x_k(BPSK modulation)$





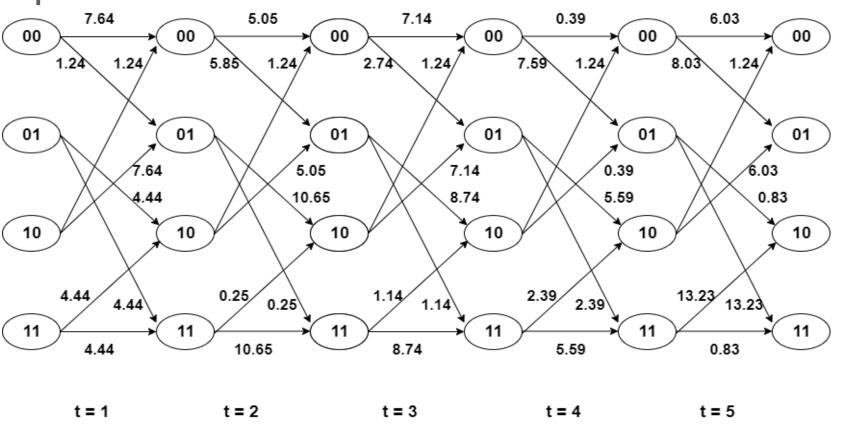


t=1 t=2 t=3 t=4 t=5 Received Sequence: $(z_0,z_1,z_2,z_3,z_4,z_5,z_6,z_7,z_8,z_9)$

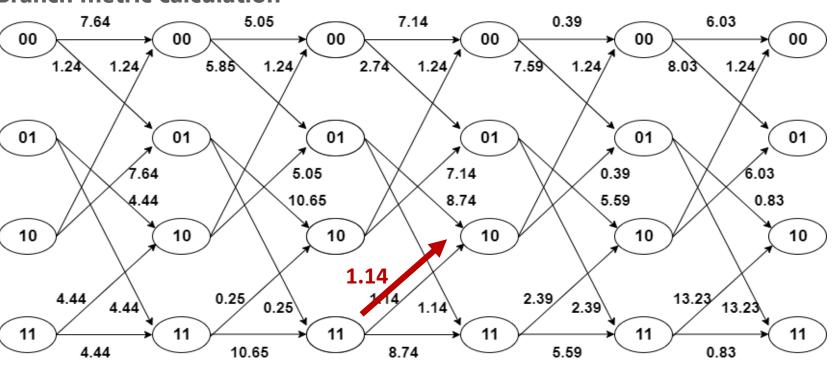
= (0.8 + 0.4j , 0.8 - j, -1.4 + 0.1j , 1.2 - 0.2j, -0.4 + 0.2j, 1.5 + 0.7j , -1.3 + 0.2j , -0.5 -0.1j, 1.3 + 0.1j, -1.8-0.3j)

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Step 4: Calculate all branch metrics



Branch metric calculation

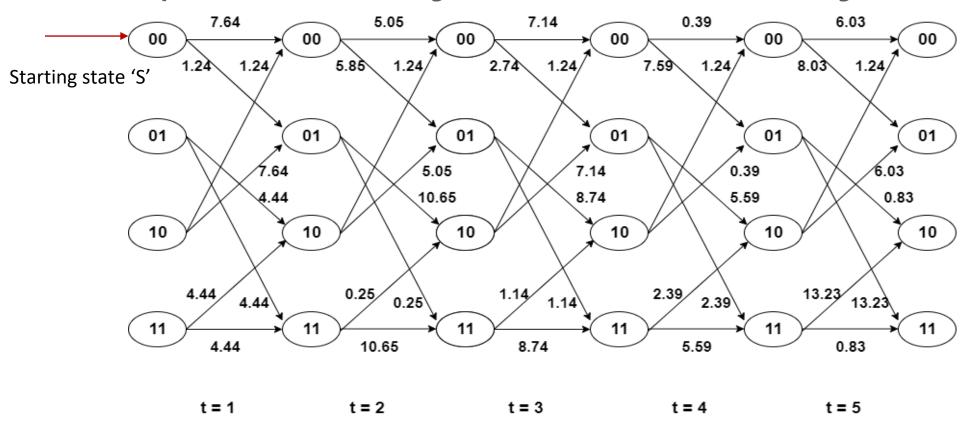


$$(x_4, x_5) = (-1, 1)$$

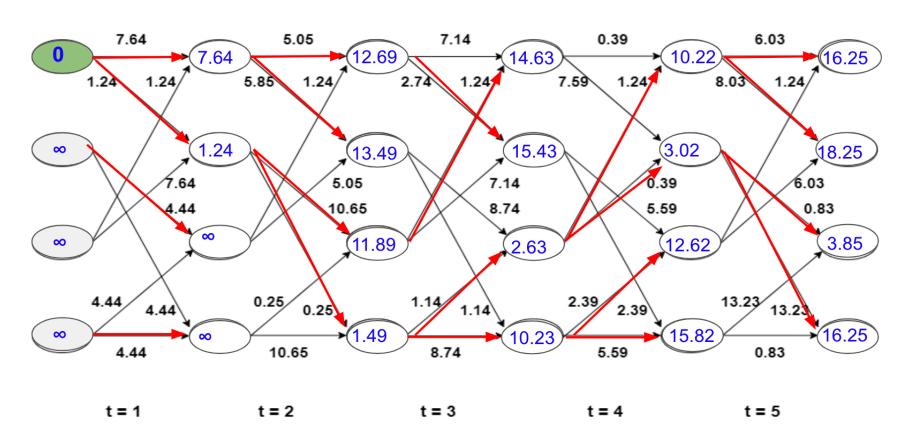
27 $(z_4, z_5) = (-0.4 + 0.2j, 1.5 + 0.7j)$

Branch metric =
$$|z_4 - x_4|^2 + |z_5 - x_5|^2 = 1.14$$

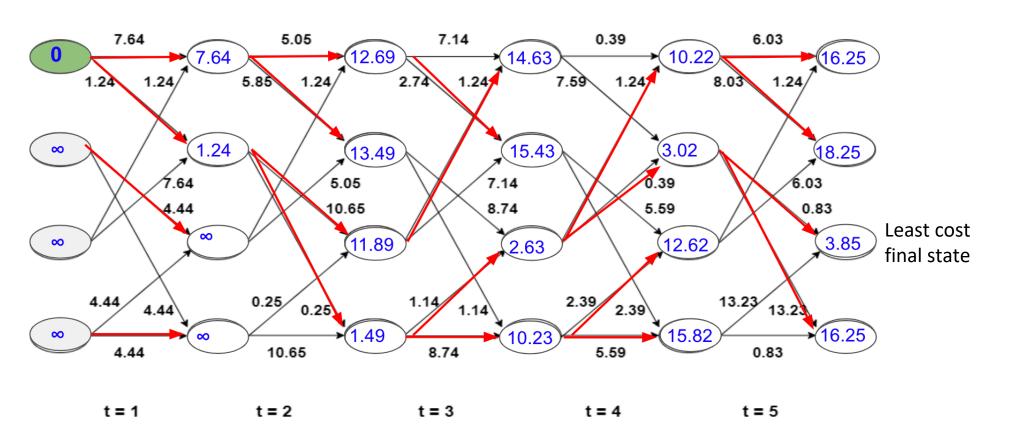
Step 5: Find cost of reaching t=5 for all states with 00 as starting state S



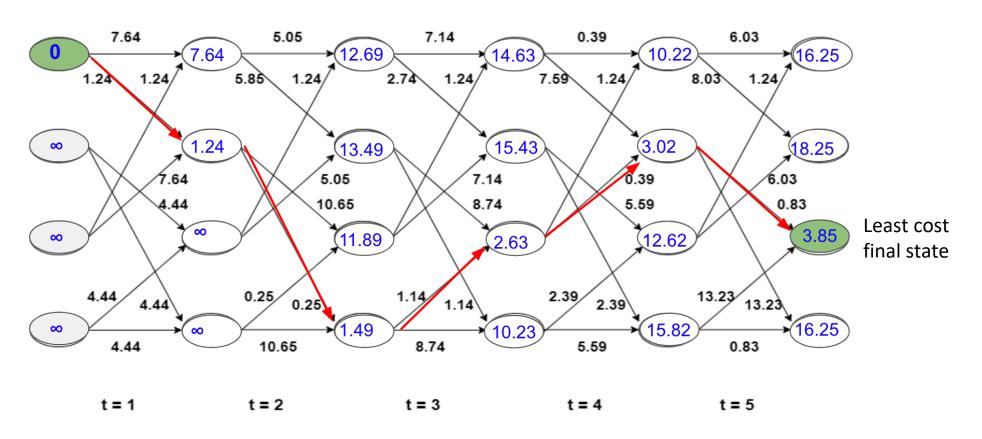
Step 5: Find cost of reaching t=5 for all states with 00 as starting state S



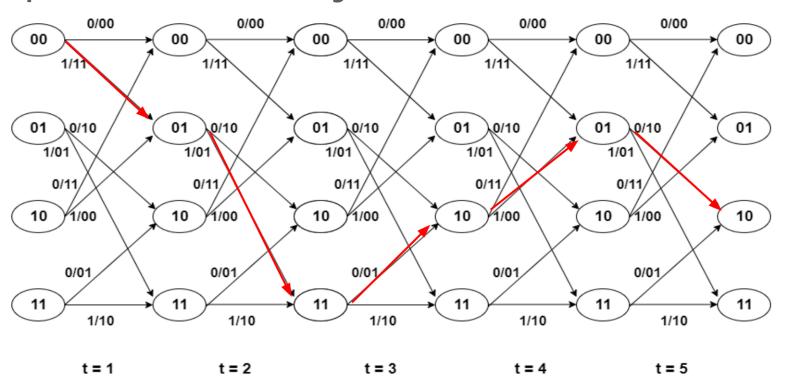
Step 5: Find cost of reaching t=5 for all states with 00 as starting state S



Step 6: Traceback to find shortest path



Step 7: Use Trellis with bits to get decoded bits



Input bit sequence estimate is 1 1 0 1 0

Closing Comments

- You are welcome to use any signal processing libraries except for turbo code decoding libraries.
- Deliverables:
- o MATLAB source code .m file
- Design report
- Don't submit executable files
- Due date: Feb 28, 11.59 pm EDT