



Comment	Description	Designator	Footprint	LibRef	Quantity
	1.5 A Low Dropout				
	Positive Regulator, 2.55 to 25 V Vin, 5 V				
	Vout, 3-pin FM (T-3),	Audio and FPGA			
LT1086CT-5	0 to 150 degC	Power Regulator	LT-T-3	LT1086CT-5	1
Speaker	Loudspeaker	Audio Out	PIN2	Speaker	1
		C1, C2, C3, C5, C6,			
	Polarized Capacitor	C9, C10, C13, C14,			
Cap Pol1	(Radial)	C18	RB7.6-15	Cap Pol1	10
		C4, C7, C8, C11, C12, C15, C16, C17, C19,			
		C20, C21, C22, C23,			
Cap2	Capacitor	C24	CAPR5-4X5	Cap2	14
	1 Amp General				
Diode 1N4007	Purpose Rectifier	D1	DO-41	Diode 1N4007	1
LT1661		DAC	DIP-8	LT1661	1
CoreEP3C16		FPGA	CoreEP3C16	CoreEP3C16	1
Header 2	Header, 2-Pin	FPGA 3.3V	HDR1X2	Header 2	1
SW-PB	DIGIKEY SW400-ND	FPGA Reset	SW PB	SW-PB	1
Hole 125mil		H1, H2, H3, H4	Hole 125mil	Hole 125mil	4
	Low Voltage Power				
PWR2.5	Supply Connector	J?	Power 2.5 fixed	PWR2.5	1
Llaadan OVO	Header, 8-Pin, Dual	LED Matrix Outroot	LIDDOVO	Handar OVO	1
Header 8X2	row	LED Matrix Output	HDR2X8	Header 8X2	
Power Port		LED Matrix Power LED Matrix Power	CON2screw	Power	1
		Regulator,			
		Microprocessor			
LT1086 3.3V		Power Regulator	TO-220	LT1086 3.3V	2
		Left Breakout, Right			
Header 20	Header, 20-Pin	Breakout	HDR1X20	Header 20	2
	8-bit AVR				
	Microcontroller, 64KB Flash, 2KB				
	EEPROM, 4KB				
	SRAM, 44-pin TQFP,				
	Industrial Grade (-				
ATmega644PA-AU	40°C to 85°C)	Microprocessor	44A_N	ATmega644PA-AU	1
	Header, 2-Pin, Dual				
Header 2X2	row	Power In	HDR2X2	Header 2X2	1
Header 10	Header, 10-Pin	Programmer	HDR1X10	Header 10	1
Res1	Resistor	R1	AXIAL-0.3	Res1	1
	SPDT Subminiature				
	Toggle Switch, Right				
	Angle Mounting,				
Power SPDT	Vertical Actuation	S1	SW DPST jameco	SW-SPDT	1
XBEE		Wireless Tranceiver	XBEE/PRO	XBEE	1