

ECE3221 Lab 0  
Name: Stephen Cole  
ID: 3553803

### Example 1

Figure 1.1
<div><div>-</div><div>Z = X AND Y (Bitwise AND)</div><div>X=00001234</div><div>Y=00005678</div><div>Z=00001230</div><div>-----</div><div>-</div></div>

### Example 2

Figure 2.1
<div><div>-</div><div>Z = X MUL Y (Multiplication)</div><div>X=00000064</div><div>Y=00000064</div><div>Z=00002710</div><div>-----</div></div>

Figure 2.2
<div><div>-</div><div>Z = X MUL Y (Multiplication)</div><div>X=00002710</div><div>Y=00000064</div><div>Z=000F4240</div><div>-----</div><div>-</div></div>

### Example 3

Figure 3.1
------------

```
-
Z = X OR Y   (Bitwise OR)
X=00000005
Y=00000002
Z=00000007
-----
-
```

Figure 3.2

```
-
Y <-
> Z (swap register contents)
X=00000005
Y=00000007
Z=00000002
-----
-
```

Figure 3.3

```
-
Z = NOT X    (Not = "1's Complement")
X=00000003
Y=00000007
Z=FFFFFFFC
-----
-
```

Figure 3.4

```
-
Z = X AND Y (Bitwise AND)
X=FFFFFFFC
Y=00000007
Z=00000004
-----
-
```

Procedure

1a.

AND

1	0	1	1	0	1	1	1	0	0	1	1	1	=	9
0	0	0	0	0	1	1	1	1	1	1	1	1	=	0
0	0	0	0	0	1	1	1	0	0	1	1	1	=	0

Table 1a

b.

OR

1	0	1	1	0	1	1	1	0	0	1	1	1	=	9
1	1	1	0	0	0	0	0	0	1	1	1	1	=	3
1	1	1	1	0	1	1	1	0	1	1	1	1	=	B

Table 1b

c.

XOR

1	0	1	1	0	1	1	1	0	0	1	1	1	=	9
0	0	0	0	0	1	1	1	1	1	1	1	1	=	0
1	0	1	1	0	0	0	0	1	1	0	0	0	=	9

Table 1c

d.

ADD

1	0	1	1	0	1	1	1	0	0	1	1	1	=	1
0	0	0	0	0	1	1	1	1	1	1	1	1	=	0
1	0	1	1	1	1	1	1	0	0	1	1	0	=	9

Table 1d

2.

Results:

0x00000FF0 AND 0x00001234 = 0x00000230

BIN: 0000 0000 0000 0000 0010 0011 0000

Explanation: In this case the AND function is used to force all but two hexadecimal values of Y low. As you can see in the example below hexadecimal values 3 and 0 have been forced low by the operation.

3	2	1	0
Y = 0001 0010 0011 0100			
Z = 0000 0010 0011 0000			

0x00000FF0 OR 0x00001234 = 0x00001FF4

BIN: 0000 0000 0000 0001 1111 1111 0100

Explanation: In this case the OR function is used to force certain values of Y to a high state. As you can see from the example below values 2 and 1 of Y have been forced high.

3	2	1	0
Y = 0001 0010 0011 0100			
Z = 0001 1111 1111 0100			

0x00000FF0 XOR 0x00001234 = 0x00001DC4

BIN: 0000 0000 0000 0001 1101 1100 0100

Explanation: In this case the XOR function is used to invert selected values of Y. As you can see from the example below hexadecimal values 2 and 1 have been inverted

3	2	1	0
Y = 0001 0010 0011 0100			
Z = 0001 1101 1100 0100			

3.

Results:

0x0000FFFF SLL 0x00000001 = 0x0001FFFE  
BIN: 0000 0000 0000 0001 1111 1111 1110

0x0000FFFF SLL 0x00000002 = 0x0003FFFC  
BIN: 0000 0000 0000 0011 1111 1111 1100

0x0000FFFF SLL 0x00000003 = 0x0007FFF8  
BIN: 0000 0000 0000 0111 1111 1111 100

0x0000FFFF SLL 0x00000004 = 0x000FFFF0  
BIN: 0000 0000 0000 1111 1111 1111 0000

What is the overall effect when Y=4?

The value of X 0x0000FFFF is multiplied by 16 as each shift left accounts for a multiplication of  $2^x$  where  $x = Y$ .

4.

Results:

0x0000FFFF SLL 0x00000014 = 0xFFFF0000  
BIN: 1111 1111 1111 0000 0000 0000 0000 0000

0x0000FFFF ROL 0x00000014 = 0xFFFF000F  
BIN: 1111 1111 1111 0000 0000 0000 0000 1111

Explain the difference between shift left and rotate left:

The difference between shift left and roll left is that if the value is shifted to a value that is greater than the maximum number of bits allotted, the values contained in the bits those locations will be forgotten. Whereas roll left will move those values to the start of the allocated bits.

5.

Results:

$$0x00000065 \text{ AND } 0x00000001 = 0x00000001$$

- a. What does the result of this operation tell you about X?

The result of this operation tells us that X is used to force all bits but the LSB to a low state.

- b. Write the operation that would complement only the least significant bit in X.

$$0x00000001 \text{ XOR } 0x00000001 = 0x00000000$$

- c. Write the operation that would set only the least significant bit to 0 in X. Show the steps necessary to do this using the calculator.

$$0x00000065 \text{ XOR } 0x00000001 = 0x00000064$$

Figure 5.1  
Set X=0x00000065

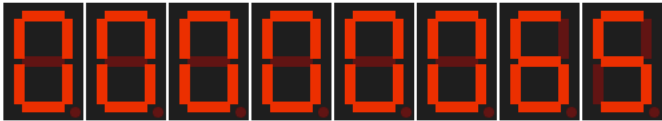
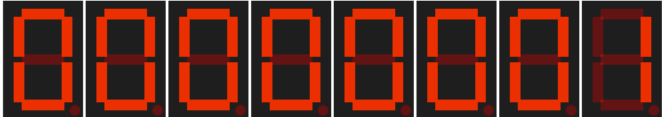
<input checked="" type="checkbox"/> <b>Seven-segment displays</b>																	
																	
<input checked="" type="checkbox"/> <b>Switches</b>																	
<div><div><input type="checkbox"/> 17</div><div><input type="checkbox"/> 16</div><div><input type="checkbox"/> 15</div><div><input type="checkbox"/> 14</div><div><input type="checkbox"/> 13</div><div><input type="checkbox"/> 12</div><div><input type="checkbox"/> 11</div><div><input type="checkbox"/> 10</div><div><input type="checkbox"/> 9</div><div><input type="checkbox"/> 8</div><div><input type="checkbox"/> 7</div><div><input checked="" type="checkbox"/> 6</div><div><input checked="" type="checkbox"/> 5</div><div><input type="checkbox"/> 4</div><div><input type="checkbox"/> 3</div><div><input checked="" type="checkbox"/> 2</div><div><input type="checkbox"/> 1</div><div><input type="checkbox"/> 0</div><div><input type="checkbox"/> All</div></div>																	
<input checked="" type="checkbox"/> <b>Push buttons</b>																	
<div><div><input type="checkbox"/> 3</div><div><input type="checkbox"/> 2</div><div><input type="checkbox"/> 1</div><div><input type="checkbox"/> 0</div><div><input type="checkbox"/> All</div></div>																	
<input checked="" type="checkbox"/> <b>JTAG UART</b>																	
Z=00000000 ----- - X=00000065 Y=00000000 Z=00000000 -----																	

Figure 5.2

Set Y = 0x00000001

☑ Seven-segment displays



☑ Switches

☐ 17 ☐ 16 ☐ 15 ☐ 14 ☐ 13 ☐ 12 ☐ 11 ☐ 10 ☐ 9 ☐ 8 ☐ 7 ☐ 6 ☐ 5 ☐ 4 ☐ 3 ☐ 2 ☐ 1 ☒ 0

☐ All ☒

☑ ? Push buttons

☐ 3 ☒ 2 ☐ 1 ☐ 0

☐ All ☒

☑ JTAG UART

Z=00000000  
-----  
-

X=00000065  
Y=00000001  
Z=00000000  
-----

Figure 5.3

Use XOR (0010) to set the least significant bit of X to 0.

☒ **Seven-segment displays**

☒ **Switches**

☐ 17
 ☐ 16
 ☐ 15
 ☐ 14
 ☐ 13
 ☐ 12
 ☐ 11
 ☐ 10
 ☐ 9
 ☐ 8
 ☐ 7
 ☐ 6
 ☐ 5
 ☐ 4
 ☐ 3
 ☐ 2
 ☒ 1
 ☐ 0

☐ ☒ All

☒ **Push buttons**

☐ 3
 ☐ 2
 ☐ 1
 ☐ 0

☐ ☒ All

☒ **JTAG UART**

```

Z = X XOR Y (Bitwise XOR)
X=00000065
Y=00000001
Z=00000064
-----

```