1	(a)	Describe the operation of each of the following logic gates:	
		NAND	
		NOR	
		XOR	
		OR	
			[4

(b) Draw a logic circuit for this logic expression:

$$\mathbf{X} = \mathsf{NOT} \; ((\mathbf{A} \; \mathsf{AND} \; \mathbf{B}) \; \mathsf{OR} \; (\mathbf{C} \; \mathsf{AND} \; \mathbf{D}))$$



A computer game is being designed that users will be able to play using a virtual reality (VF headset.	()
(a) Complete the description of the principal operation of a VR headset.	
A headset can have one or two that output the image t	0
the user. The headset has speakers that output surround sound to give a realistic experience	€.
The user's head movements are detected using a sensor.	
This sensor is a	а
microprocessor that analyses the data to identify the	of
movement. Some headsets use that record the user'	S
eye movements for analysis. [4	1]
(b) The computer uses a buffer when transmitting data to the VR headset.	
Explain how a buffer is used when data is transmitted between the computer and the VI headset.	3
[3	3]
(c) The VR headset has Electrically Erasable Programmable Read Only Memory (EEPROM).	
Explain the benefits of using EEPROM instead of other types of Read Only Memory (ROM) in the VR headset.	n
[3	3]

	ne computer can transmit a video made from bitmap images and vector graphic animations the VR headset.			
(i)	Describe how the data for a bitmapped image is encoded.			
	[3]			
(ii)	Describe the contents of a vector graphic drawing list.			
:::\	The hitmen video is not compressed before transmission to the VD headest			
, III <i>)</i>	The bitmap video is not compressed before transmission to the VR headset.			
	Give two reasons why the video does not need to be compressed. 1			
	2			
	2			
	[2]			
	to th			

3	mar	n assessment board scans exam papers and stores the digitised papers on a server. Exam arkers download the digitised papers to mark. The exam markers then upload the mark for each oper.		
	(a)	The assessment board needs to make sure the data stored on the server is secure.		
		(i)	Authentication methods can help to protect the server against hackers.	
			Identify one other security measure that helps to protect the server from hackers.	
			Describe how the security measure works.	
			Security measure	
			Description	
			[3]	
		(ii)	Identify one security measure that helps to protect the data when it is being transmitted to its destination. Describe how the security measure works.	
			Security measure	
			Description	
			[3]	

(b) The exam markers use software that operates as a thin-client to mark the exam papers.

Complete the table by identifying **two** characteristics of a thin-client.

Describe how each characteristic will be used in this software.

	Thin-client characteristic	Description of use in this software
1		
2		
		[4
Dat	ta transmitted on the internet passes throus Describe the role of routers in the transr	
.,		-
		[2
(ii)	Describe the role of the PSTN (Public S of data through the internet.	witched Telephone Network) in the transmissior
(ii)		-
(ii)		-
(ii)	of data through the internet.	-

(c)	The table EXAM_QUESTION has been created but the foreign key has not been linked.
	Write an SQL script to update EXAM_QUESTION and link the foreign key to EXAM.
	[2]
(d)	The database also needs to store data about the students, the exams the students have taken and the marks the students achieved in each question of each exam.
	Describe the additional tables that will need to be included in the database and explain how all the tables in the database will be linked.
	[5]

5 The following table shows part of the instruction set for a processor. The processor has two registers: the Accumulator (ACC) and an Index Register (IX).

Instruction Opcode Operand		Explanation	
LDD	<address></address>	Direct addressing. Load the contents of the location at the given address to ACC	
LDI	<address></address>	Indirect addressing. The address to be used is at the given address. Load the contents of this second address to ACC	
LDX	<address></address>	Indexed addressing. Form the address from <address> + the contents of the index register. Copy the contents of this calculated address to ACC</address>	
LDR	#n	Immediate addressing. Load the number n to IX	
ADD	#n/Bn/&n	Add the number n to the ACC	
ADD	<address></address>	Add the contents of the given address to the ACC	
SUB	#n/Bn/&n	Subtract the number n from the ACC	
SUB	<address></address>	Subtract the contents of the given address from the ACC	
INC	<register></register>	Add 1 to the contents of the register (ACC or IX)	

<address> can be an absolute or a symbolic address

denotes a denary number, e.g. #123
B denotes a binary number, e.g. B01001010

& denotes a hexadecimal number, e.g. &4A

(a) The current contents of memory are shown:

Address	Data
10	1
11	3
12	5
13	11
14	10
15	16
16	12

The current contents of the ACC and IX are shown:

Complete the table by writing the content of the ACC after each program has run.

Program number	Code	ACC content
1	LDI 15 SUB #1	
2	LDD 14 ADD 11	
3	LDM #11 ADD #3 SUB 16	
4	LDR #2 LDX 14 ADD #2	

(b) The processor includes these bit manipulation instructions:

Instruction Opcode Operand		Explanation	
AND	<address></address>	Bitwise AND operation of the contents of ACC with the contents of <address></address>	
XOR	#n/Bn/&n	Bitwise XOR operation of the contents of ACC with the operand	
XOR	<address></address>	Bitwise XOR operation of the contents of ACC with the contents of <address></address>	
OR	#n/Bn/&n	Bitwise OR operation of the contents of ACC with the operand	
OR	<address></address>	Bitwise OR operation of the contents of ACC with the contents of <address></address>	

<address> can be an absolute or a symbolic address

denotes a denary number, e.g. #123

B denotes a binary number, e.g. B01001010

& denotes a hexadecimal number, e.g. &4A

The current contents of memory are shown:

Address	Data
25	11000110
26	11100001
27	10000001
28	11001101
29	00001111

The current content of the ACC is shown:

|--|

Complete the table by writing the content of the ACC after each program has run.

The binary number 01000110 is reloaded into the ACC before each program is run.

Program number	Code	ACC content
1	XOR 29	
2	AND #29	
3	OR B11111111	

6	A computer has an Operating System (OS).					
	Memory management and process management are two OS tasks. Explain how memory management and process management support multi-tasking.					
	[4					

(a)	Tick (✓) one box only to identify the largest file size.	
	3300 kibibytes 0.3 megabytes 3 mebibytes 3300 kilobytes	[1]
(b)		
	Show your working.	
	Working	
	Answer	[3]
(c)	Convert the hexadecimal number C0F into denary.	[J
(0)	Show your working.	
	Working	
	Answer	[2]
		-

A computer stores binary data.

7

(a)	(a) The programmer decides to use the compiler when testing the final program.						
	Describe the b	Describe the benefits of using the compiler during testing.					
	[2]						
(b)	IDEs have man	ny features other than bu	uilt-in translators.				
			other common IDE feature that can be used for each elps the user during program development.				
			give translator as one of your features.				
	Purpose	IDE feature	Description				
	Purpose	IDE feature	Description				
	Purpose for	IDE feature	Description				
		IDE feature	Description				
	for	IDE feature	Description				
	for	IDE feature	Description				
	for	IDE feature	Description				
	for coding	IDE feature	Description				
	for coding	IDE feature	Description				
	for coding	IDE feature	Description				

A programmer uses an Integrated Development Environment (IDE) to write a computer program. The IDE has both a compiler and an interpreter as built-in translators.

8

for debugging

(c)	The programmer uses program libraries when developing the program.			
	Describe two benefits to the programmer of using program libraries.			
	1			
	2			
		[2]		