1	(a)	Con	mple	ete th	he f	ollo	win	ıg c	des	scri	ipti	or	۱.																			
		Ak	kibik	yte	ha	IS i	а.													ŗ	ore	fix.	. 7	Γhre	е	ki	bib	/tes	is	the		same
		as .											byte	es.																		
		A r	meg	abyt	te I	has	a														р	ref	X.	Tw	0	te	rab	ytes	is	the		same
		as .										!	giga	aby	yte	s.																[4]
	(b)	Con	nver	t the	de	nar	y nı	um	ıbe	r 2	41	tc	he	xa	ıde	ciı	ma	al.														
																																[1]
	(c)	Stat	te w	/hat	is m	nea	nt b	у г	an	ov	erf	lo	w i	n k	bir	ıar	у	ad	ldi	tic	on.											
																																[1]
	(d)	Con	mpu	ters	use	e ch	ıara	acte	er s	sets	s w	/he	en r	ep	re	se	nti	ng	ус	ha	ara	cte	rs	in b	ina	ary.						
		(i)		mpl each					by	' ide	ent	tify	/ing	j th	ne	nu	ml	be	r o	of k	bits	s ea	ach	n of	the	e c	har	acte	r se	ets a	llo	cates
					Ch	ara	acte	er s	set									N	lur	nk	oer	of	bi	its								
						A	SCI	П																								
					exte	end	led /	AS	CI	I																						
						Un	icoc	de																								
																													ı			[1]
		(ii)	Ex	plair	n ho	w t	he v	wo	rd	'Cl	locl	k'	is re	ері	res	ser	nte	d	by	а	ch	ara	act	er s	et.							
																																[2]

A p	hotog	graph is stored as a bitmap image.	
(a)		photograph has a resolution of 4000 pixels wide by 3000 pixels high. The bit devices.	pth is
	Cal	culate an estimate for the file size of the photograph in megabytes.	
	Sho	ow your working.	
	Woı	rking	
	Ans	swer megabytes	[2]
(b)	The	e photograph is compressed before being uploaded to a web server.	
	(i)	Give three benefits of this photograph being compressed using lossy compression.	ssion
		1	
		2	
		3	
			[3]
	(ii)	Explain how run-length encoding (RLE) will compress the photograph.	
			[2]
(c)	lder	ntify two elements of a bitmap image that can be changed to reduce its file size.	
	1		
	2		

3 The following table shows part of the instruction set for a processor. The processor has two registers: the Accumulator (ACC) and an Index Register (IX).

Ins	truction	Evalenation					
Opcode	Operand	Explanation					
LDM #n		Immediate addressing. Load the number n to ACC					
LDD <address></address>		Direct addressing. Load the contents of the location at the given address to ACC					
LDI <address></address>		Indirect addressing. The address to be used is at the given address. Load the contents of this second address to ACC					
LDX <address></address>		Indexed addressing. Form the address from <address> + the contents of the index register. Copy the contents of this calculated address to ACC</address>					
LDR	#n	Immediate addressing. Load the number n to IX					
ADD	#n/Bn/&n	Add the number n to the ACC					
ADD	<address></address>	Add the contents of the given address to the ACC					
DEC	<register></register>	Subtract 1 from the contents of the register (ACC or IX)					
SUB	#n/Bn/&n	Subtract the number n from the ACC					
SUB	<address></address>	Subtract the contents of the given address from the ACC					
INC <register></register>		Add 1 to the contents of the register (ACC or IX)					

<address> can be an absolute or a symbolic address

[#] denotes a denary number, e.g. #123

B denotes a binary number, e.g. B01001010

[&]amp; denotes a hexadecimal number, e.g. &4A

(a) The current contents of memory are shown:

Address	Data
48	51
49	6
50	48
51	50
52	49
53	50
54	6

The current contents of the ACC and IX are shown:

Complete the table by writing the content of the ACC after each program has run.

Program number	Code	ACC content
1	LDM #50 INC ACC SUB #1	
2	LDI 51 ADD 52	
3	LDR #2 LDX 50 DEC ACC	
4	LDD 52 SUB 54 INC ACC	

(b) The processor includes these bit manipulation instructions:

Ins	truction	Evalenation					
Opcode	Operand	- Explanation					
AND	#n/Bn/&n	Bitwise AND operation of the contents of ACC with the operand					
AND <address></address>		Bitwise AND operation of the contents of ACC with the contents of <address></address>					
XOR	#n/Bn/&n	Bitwise XOR operation of the contents of ACC with the operand					
XOR	<address></address>	Bitwise XOR operation of the contents of ACC with the contents of <address></address>					
OR	#n/Bn/&n	Bitwise OR operation of the contents of ACC with the operand					
OR	<address></address>	Bitwise OR operation of the contents of ACC with the contents of <address></address>					
LSL	#n	Bits in ACC are shifted logically n places to the left. Zeros are introduced on the right-hand end					
LSR #n		Bits in ACC are shifted logically n places to the right. Zeros are introduced on the left-hand end					

<address> can be an absolute or a symbolic address

The current contents of memory are shown:

Address	Data			
100	00001101			
101	10111110			
102	11110011			
103	00110111			
104	00000000			

The current content of the ACC is shown:

1	1	1	1	1	1	1	1

[#] denotes a denary number, e.g. #123

B denotes a binary number, e.g. B01001010

[&]amp; denotes a hexadecimal number, e.g. &4A

Complete the table by writing the content of the ACC after each instruction has run.

The binary number 11111111 is reloaded into the ACC before each instruction is run.

Instruction number	Instruction	ACC content
1	LSL #2	
2	XOR 100	
3	AND 103	

4 A theatre wants to use a database to store data about the shows that are scheduled, their customers and the seats that the customers have booked.

In the theatre:

- Each show can take place on multiple dates.
- Some dates can have more than one performance.
- There are multiple rows of seats.
- Each seat can be individually booked by its row letter and seat number, for example row E seat 2.

Part of the	database	design	includes	these	tables:

SHO	W(ShowID,	Title,	Duratio	on)			
SEA'	Γ(<u>SeatID</u> ,	RowLett	er, Sea	atNumber))		
PER:	FORMANCE (Performa	nceID,	ShowID,	ShowDate,	StartTime)	
(a)	Identify the I	relationshi	p betwee	n the table	S PERFORMAN	CE and SHOW.	
							 [1

(b) Sample data for the table PERFORMANCE is shown:

PerformanceID	ShowID	ShowDate	StartTime
0001	MK12	5/5/2025	13:00
0002	MK12	5/5/2025	19:30
0003	MK12	6/5/2025	19:00
0004	OP3	7/5/2025	18:30
0005	OP3	8/5/2025	18:30
0006	OP3	9/5/2025	13:00

Write a Structured Query Language (SQL) script to define the table PERFORMANCE.
[4]

(C)	the sample data in part (b) , the show MK12 is scheduled three times.
	The result needs to include the show name and a suitable field name for the number of times it is scheduled.
	[4]
(d)	Customers give their first name, last name and email address when they are making a booking. One booking can include multiple seats.
	Describe the additional tables that will need to be included in the database and explain how these tables will be linked within the database.
	[5]

5	A multimedia design company has an office with a LAN (local area network). The LAN can have up to 20 devices connected with cables and other devices connected using wireless access.						
	(a)	The	e company has private cloud storage for its employees to store their work.				
		(i)	Define the term private cloud .				
			[1]				
		(ii)	Describe the benefits to the company of using private cloud storage instead of public cloud storage.				
			[3]				
	(b)	Par	t of the internal structure of the wired LAN is a star topology.				
	` ,		lain how packets are transmitted between two devices in a star topology.				
			[2]				

	(i)	A collision is detected.	
		Describe how the collision is managed using Carrier Sense Multiple Access/Collision Detection (CSMA/CD).	ion
			[2]
	(ii)	Identify two drawbacks of using CSMA/CD.	
		1	
		2	
			[2]
(d)	The	devices in the office have static private IP addresses.	
	Stat	te what is meant by a static private IP address .	
			[1]

(c) A different part of the network uses the Ethernet protocol.

6 Each of the following truth tables has three inputs (A, B and C) and one output (X).

Draw **one** line to match each truth table with its logic expression.

Truth table

Α	В	С	Х
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	0
1	1	1	0

Α	В	С	X
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1 1 0		0
1	1	1	1

A	В	С	X
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	0
1	1	1	1

Logic expression

NOT (A XOR B) AND C

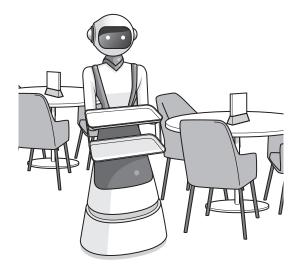
(A OR C) AND NOT B

A NAND B NAND C

(A NAND B) OR C

NOT (**A** AND **B** AND **C**)

7 Robots are used to serve food and drink to customers at a restaurant.



(a) A robot navigates through the restaurant to the table it is serving.

Complete the table by identifying **two** sensors that can be included in the robot **and** the purpose of each sensor in the navigation system.

Sensor	Purpose of sensor in navigation system			

[2]

(b)	The robot uses Artificial Intelligence (AI) to communicate with the customers. The customers speak to the robot to order their food and drinks.
	Explain how AI will be used in this part of the robot.
	[3]
	[9]

(c)	The navigation system can be considered an example of a control system.
	Describe how feedback is used in a control system.
	[2]
(d)	The robot includes a touchscreen for the customer to make their payment.
	Describe the principal operation of a touchscreen.
	[4]

(e)	Program libraries were used when writing the robot's software.				
	(i)	State what is meant by a program library .			
		[1]			
	(ii)	Some program libraries include Dynamic Link Library (DLL) files.			
		Describe the benefits of a programmer using a library with DLL files instead of using a library that does not include DLL files.			
		[4]			

(f)	The data from the robots is	transmitted to a	central comp	outer usina :	a wireless	connection
(1)	THE data HOTH the TODOLS IS	tiansimileu to a	Central Comp	puter using a	a WII CICSS	COLLIGE CHOIL

(i) Complete the table by identifying **and** describing **two** methods of data verification that can be used during data transfer.

	Method	Description
1		
2		
		[4
(ii)	Explain how encryption can	protect the security of data during transmission.