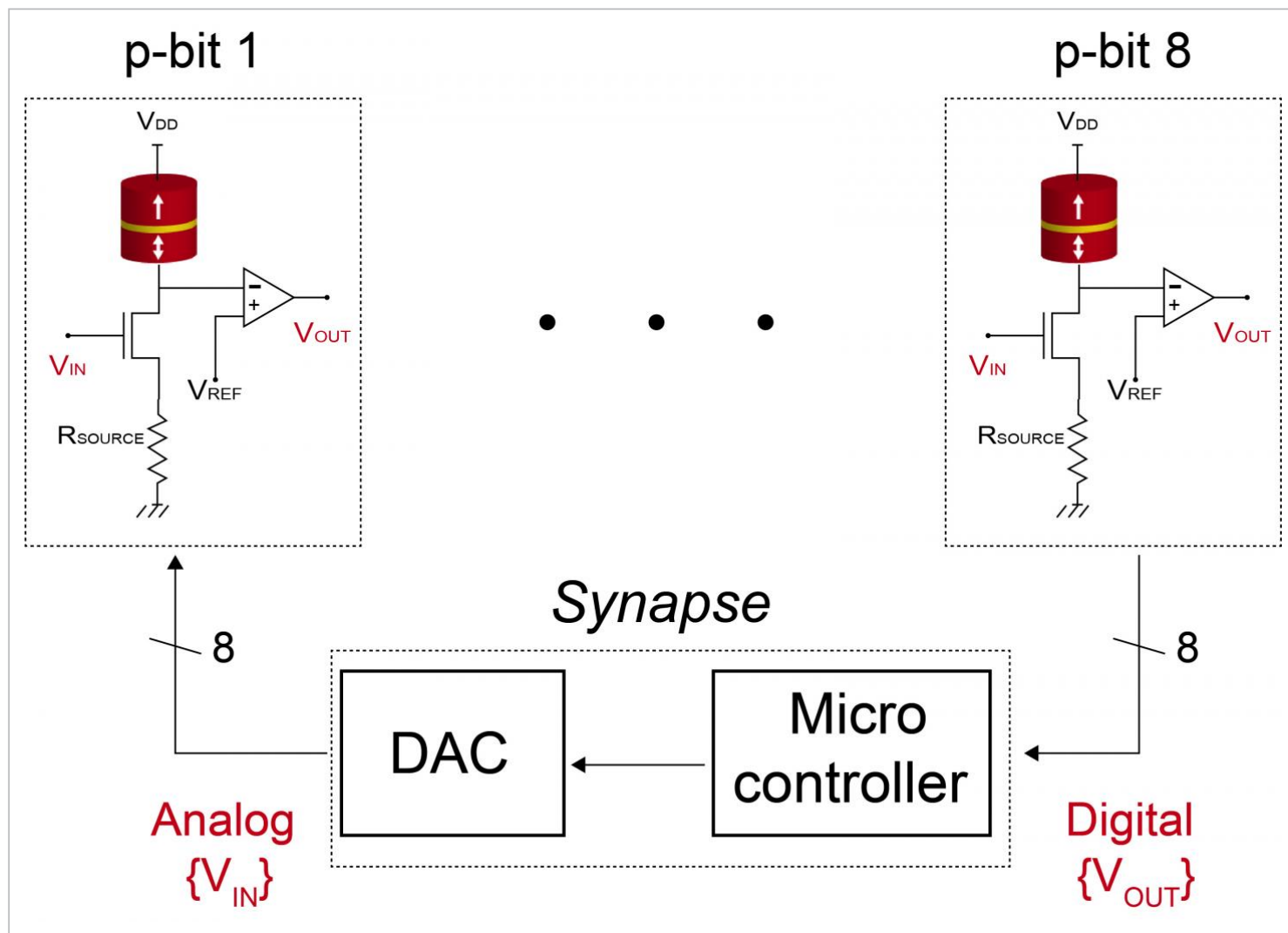


Hardware for a Probabilistic Computer (IMAGE)

PURDUE UNIVERSITY



CAPTION

For the first time, researchers have demonstrated a way to build a probabilistic computer. This circuit includes a modified version of a magnetoresistive random-access memory device (red) to interconnect eight p-bits.

CREDIT

Purdue University image/Ahmed Zeeshan Pervaiz

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None

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