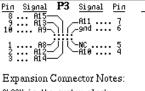


NOTES:

J1 mates with AppliCard expansion connector P2 (far right). J3 (not shown) mates with AppliCard expansion connector P3 and need only be installed if an AppliDisk card is to be piggy-backed on top of the SIO expansion board.

J2 is intended to provide 2 PC/AT-style DTE serial ports. It is constructed using 20-line ribbon cable. Install a 2x10 female IDC connector on one end. On the other end, install two IDC-style DB-9P connectors such that pin 1 aligns with line 1 (chan. A) and line 20 (chan. B) of the ribbon cable.

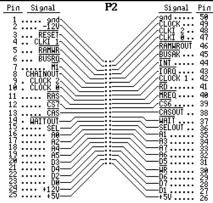
Decoupling capacitors are 0.1µF monolithic installed between Vcc and 0V on each IC.



Expansion Connector Notes: CLOCK is the system clock CLKI n is the CLK/TRG input to the 280 CTC channel 'n'. CLOCK n is the ZC/TO output of channel 'n' of the on-board 280 CTC.

CAS and CASOUT are connected by a jumper or shorting block unless an AppliDisk card is installed.

Some boards may have CAS and CRSOUT connected by a trace on the solder side of the



The Z80 DART (Z8470) may be directly substituted in place of the Z80 SIO/0 (Z8440). Choose a part rated appropriately for the clock speed of your AppliCard. Interfacing to an accelerated AppliCard may require extra circuitry or higher-speed parts.