# Learn Assembly Programming With ChibiAkumas!



### **6809 Assembly programming for the Vectrex**

The FM-7 is one of a series of Fujitsu computers released in Japan

With a pair of 6809 CPU's it was widely popular in Japan, and is one of the few 6809 computers released.

In these tutorials we'll learn the about the FM7, and write some simple programs for it

| Vectrex           |
|-------------------|
| Main: 1.5mhz 6809 |
| 1k Ram            |
| 32k               |
| AY-3-8912         |
| INFINITE!!!       |
|                   |







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### **Z80 Content**

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Hello World

**Advanced Series** 

**Multiplatform Series** 

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ChibiAkumas Series

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**DevTools kit** 



### Vectrex

## **OS Calls**

| Group   | Address | s Name  | AltName                    | DP<br>(Entry/Exit | Details  | Example |
|---------|---------|---------|----------------------------|-------------------|--|---------|
| Init    | \$F18B  | INITALL | INTALL                     | \$D0              | Full Vectrex initialization                    |         |
| Init    | \$F164  | INITMSC | INTMSC                     | \$C8              | Initialize misc. parameters                    |         |
| Init    | \$F14C  | INITPIA | INTPIA                     | \$D0              | Initialize PIA                                 |         |
| Init    | \$F272  | INITPSG | INTPSG                     | \$D0              | Initialize the REQx area (sound mirror).       | 1       |
| Init    | \$F533  | IREQ    | INTREQ                     | -                 | Initialize the 'REQZ' area                     |         |
| Init    | \$F000  | POWER   | PWRUP                      | -                 | Power-up handler                               |         |
| Drawing | \$F2E6  | DEFLOK  | <br>C1:C1048576B1:B1048576 | \$D0              | Overcome scan collapse circuitry               |         |
| Drawing | \$F192  | FRAM20  | FRWAIT                     | \$D0              | Wait for frame boundary (Pause for next frame) |         |
| Drawing | \$F29D  | INT1Q   | -                          | \$D0              | Set beam intensity 1/4 (\$1F                   | )       |
| Drawing | \$F2A5  | INT3Q   | -                          | \$D0              | Set beam intensity 3/4 (\$5F                   | )       |
| Drawing | \$F2AB  | INTENS  | -                          | \$D0              | Set beam intensity A (0-127)                   |         |
| Drawing | \$F2A9  | INTMAX  | -                          | \$D0              | Set beam intensity 4/4 (7F)                    |         |
| Drawing | \$F2A1  | INTMID  | INT2Q                      | \$D0              | Set beam intensity 2/4 (\$3F                   | )       |
| Drawing | \$F30C  | POSIT1  | -                          | \$D0              | Position relative vector List X (1x scale)     | db Y,X  |

# Z80 Platforms Amstrad CPC Elan Enterprise Gameboy & Gameboy Color Master System & GameGear MSX & MSX2 Sam Coupe TI-83 ZX Spectrum Spectrum NEXT

Camputers Lynx

6502 Content \*\*\*<u>6502 Tutorial List</u>\*\*\* Learn 6502 Assembly **Advanced Series Platform Specific Series Hello World Series** Grime 6502 6502 Downloads **6502 Cheatsheet** Sources.7z **DevTools kit** 6502 Platforms Apple Ile Atari 800 and 5200 Atari Lynx **BBC Micro** Commodore 64 Commander x16 Super Nintendo (SNES) Nintendo NES / Famicom PC Engine (Turbografx-16)

68000 Content

Vic 20

| Drawing | \$F308 | POSIT2  | -       | \$D0 | Position relative vector (2x scale)   |
|---------|--------|---------|---------|------|---|
| Drawing | \$F30E | POSITB  | -       | \$D0 | Position relative vector list X Scale B   |
| Drawing | \$F2FC | POSITD  | -       | \$D0 | Position relative vector (X,Y)=(B,A) scale 1x   |
| Drawing | \$F312 | POSITN  | -       | \$D0 | Position relative vector (X,Y)=(B,A) scale T1LOLC   |
| Drawing | \$F310 | POSITX  | -       | \$D0 | Release integrators and position beam, Ifrom X  |
| Drawing | \$F2F2 | POSWID  | -       | \$D0 | Release integrators and position beam using 16-bit dw Y,X (Y,X) values from address X Depending ZSKIP, zero |
| Drawing | \$F34F | CZERO   | ZEGO    | \$D0 | integrators and set the sample / hold for active ground.  |
| Drawing | \$F35B | ZEREF   | ZEREF   | \$D0 | Set active ground sample / hold to zero volts.  |
| Drawing | \$F34A | ZERO.DP | ZERO.DP | \$D0 | DP=\$D0 Zero integrators and set active ground  |
| Drawing | \$F36B | ZERO.   | ZERO    | \$D0 | Zero the integrators only   |
| Drawing | \$F354 | ZEROIT  | ZEROIT  | \$D0 | Zero integrators and set active ground  |
| Diffy   | \$F610 | DANROT  | DROT    | -    | Rotate �Diffy� style list<br>A=Angle B=no of Vectors<br>X=Source list U=Dest                                |
| Diffy   | \$F433 | DASHE   | DSHDF1  | \$D0 | Draw dashed lines from<br>'DIFFY' list  |
| Diffy   | \$F434 | DASHEL  | DSHDF   | \$D0 | Draw dashed lines from<br>'DIFFY' list A=Vectors-1<br>X=List  |
| Diffy   | \$F437 | DASHY   | DASHDF  | \$D0 | Draw dashed lines from<br>'DIFFY' list X  |
| Diffy   | \$F3DF | DIFFAB  | -       | \$D0 | Draw from 'DIFFY' style list (B,A)=(X,Y)  |
| Diffy   | \$F3CE | DIFFAX  | -       | \$D0 | Draw from 'DIFFY' style list<br>X=List  |
|         |        |         |         |      |   |

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| Diffy | \$F3D8 | DIFFX   | TDIFFY | \$D0 | Draw from 'DIFFY' style list<br>A=Vectors-1 B=Scale<br>X=List                        |
|-------|--------|---------|--------|------|--|
| Diffy | \$F3DD | DIFFY   | -      | \$D0 | Draw from 'DIFFY' style list<br>X=List   |
| Diffy | \$F3D6 | DIFLST  | -      | \$D0 | Draw from 'DIFFY' style list   |
| Diffy | \$F616 | DIFROT  | ADROT  | -    | Rotate �DIFFY� style list X=Source U=Dest ANGLE                                      |
| Diffy | \$F3D2 | DIFTIM  | -      | \$D0 | Draw from 'DIFFY' style list<br>B=scale X=List                                       |
| Diffy | \$F3DA | DIFTLS  | LDIFFY | \$D0 | Draw from 'DIFFY' style list<br>A=Vectors-1, X=List                                  |
| Diffy | \$F613 | DISROT  | BDROT  | -    | 'DIFFY' style rotate<br>B=vector count X=source<br>list U=Dest                       |
| Duffy | \$F3BE | DUFFAB  | -      | \$D0 | Move a single vector from the current beam position using the relative vector values |
| Duffy | \$F3AD | DUFFAX  | -      | \$D0 | given in �D� (X,Y)=(B,A)  Draw from 'DUFFY' style list  X=list                       |
| Duffy | \$F3BC | DUFFY   | -      | \$D0 | Draw from 'DUFFY' style list<br>X=list   |
| Duffy | \$F3BC | DUFLTLS | TDUFFY | \$D0 | Draw from 'DUFFY' style list<br>B=length X=List                                      |
| Duffy | \$F3B5 | DUFLST  | -      | \$D0 | Draw from 'DUFFY' style list<br>X=list   |
| Duffy | \$F3B1 | DUFTIM  | -      | \$D0 | Draw from 'DUFFY' style list<br>B=length X=List                                      |
| Diffy | \$F3B9 | DUFLST  |        | \$D0 | A=Vectors-1 X=List   |
| Dot   | \$F2D5 | DIFDOT  | -      | \$D0 | Draw dots according to<br>'DIFFY' format X=List<br>pointer                           |
| Dot   | \$F2C5 | DOT     | -      | \$D0 | Turn on beam for dot   |
| Dot   | \$F2C3 | DOTAB   | -      | \$D0 | Draw Dot at relative (X,Y) pos (B,A)   |
| Dot   | \$F2DE | DOTPAK  | DOTPCK | \$D0 | Draw dots according to   |
|       |        |         |        |      |  |

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|          |               |         |          |          | 'PACKET' format X=list   |                                    |
|----------|---------------|---------|----------|----------|--|------------------------------------|
| Dot      | \$F2BE        | DOTTIM  | -        | \$D0     | Draw one dot from 'DIFFY' style list B=on time X=List  |                                    |
| Dot      | \$F2C1        | DOTX    | -        | \$D0     | Draw one dot from 'DIFFY' style list X   |                                    |
| Packet   | \$F46E        | DASHY3  | DASHPK   | \$D0     | Draw dashed lines from<br>'PACKET' list X  |                                    |
|          |               |         |          |          | Draw according to  |                                    |
| Packet   | \$F408        | PAC1X   | PACK1X   | \$D0     | Packet style list X scale 1x   | 9                                  |
|          |               |         |          |          | Draw according to  |                                    |
| Packet   | \$F404        | PAC2X   | PACK2X   | \$D0     | <ul><li>Packet style list X scale</li><li>2x</li></ul>   | 9                                  |
| Packet   | \$F40E        | PACB    | TPACK    | \$D0     | Draw from 'PACKET' list X B=Scale  |                                    |
| Packet   | \$F410        | PACKET  | -        | \$D0     | Draw according to  |                                    |
|          | ** ***        |         |          | <b>-</b> | ♦Packet♦ style list X  |                                    |
| Packet   | \$F40C        | PACXX   | LPACK    | \$D0     | Draw from 'PACKET' style list X=list   |                                    |
|          |               |         |          |          | 'PACKET' style rotate  |                                    |
| Packet   | \$F61F        | POTATA  | PROT     | -        | A=Angle X=Source List<br>U=Dest List   |                                    |
| Packet   | \$F622        | POTATE  | APROT    | _        | 'PACKET' style rotate  |                                    |
| Facket   | φι <b>022</b> | FOIAIL  | AFICOT   | -        | Print \$80 terminated String   |                                    |
| Raster   | \$F37A        | POSDRAS | SMSSPOS  | \$D0     | U at (X,Y) relative pos (B,A)  | )                                  |
| Raster   | \$F378        | POSNRAS | SRSTPOS  | \$D0     | Display raster message from U  | ,                                  |
| Raster   | \$F498        | RASTER  | MRASTR   | \$D0     | Display raster string from MESSAGE   |                                    |
| Raster   | \$F495        | RASTUR  | RASTER   | \$D0     | Display raster string from U   |                                    |
| Raster   | \$F87C        | SADD    | SCRADD   | -        | Add contents of score D to BCD at address X  |                                    |
| Raster   | \$F880        | SADD2   | STKADD   | _        | Add stack to indicated score   | 9                                  |
| 1 (4010) | Ψ. σσσ        | 0, 1222 | 0.1.0.22 |          | The state of the s | A = 2-digit                        |
| Raster   | \$F85E        | SHADD   | BYTADD   | -        | Add contents of 'A' to indicated score   | BCD number X = Score field pointer |
| Raster   | \$F391        | SHIPSAT | SHIPX    | \$D0     | Display markers (count   | neia politiei                      |

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| Raster     | \$F393                 | SHIPSHO | DSHIP  | \$C8       | remaining)<br>Display markers (count<br>remaining) A=ascii |  |
|------------|------------------------|---------|--------|------------|--|--|
| . 13.313.  | <b>4.</b> 333          |         |        | <b>400</b> | B=remaining X=Pos  |  |
| Raster     | \$F373                 | SIZPRAS | RSTSIZ | \$D0       | Display raster message from U                              |  |
|            |                        |         |        |            |  | dc.b                                     |
| Raster     | \$F38C                 | TEXPOS  | TXTPOS | \$D0       | Draw Strings from U (0 terminated)                         | Y,X,"TXT",\$80<br>dc.b<br>Y,X,"TXT",\$80 |
|            |                        |         |        |            | D'autan martan mara  | dc.b 0                                   |
| Raster     | \$F385                 | TEXSIZ  | TXTSIZ | \$D0       | Display raster message from U                              |  |
|            |                        |         |        |            | Read controller switches                                   |  |
| Controlle  | r <b>\$F1B4</b>        | ENPUT   | DBNCE  | \$D0       | and debounce switch status. (A=Response mask)              |  |
|            |                        |         |        |            | Calculate high score and                                   |  |
| Controlle  | r <b>\$F8D8</b>        | HIGHSCR | HISCR  | -          | save for logo C=Score Field                                |  |
| Controlle  | r <b>\$F1BA</b>        | INPUT   | -      | \$D0       | U=Highscore Read controller buttons                        |  |
| 0011110110 | . 41 1271              |         |        | Ψ20        | Fetch number of players                                    |  |
| Controlle  | r <b>\$F7A9</b>        | OPTION  | SELOPT | -          | and options from player A + B = No of players (0-9)        |  |
| Controlle  | r <b>\$F1F8</b>        | PANG    | JOYBIT | \$D0       | Read joystick UDLR   |  |
| Controlle  | r <b>\$F1F5</b>        | PBANG4  | JOYSTK | \$D0       | Read the absolute position of the controller joysticks.    |  |
| Controlle  | r <b>\$F84F</b>        | SCLR    | -      | _          | Clear indicated score X                                    |  |
| Controlle  | r <b>\$F8C7</b>        | WINNER  | -      | _          | Determine highest score X                                  |  |
| Rotate     | \$F610                 | DANROT  | DROT   | _          | or U<br>'DIFFY' style rotate                               |  |
| Rotate     | \$F616                 | DIFROT  | ADROT  | _          | 'DIFFY' style rotate                                       |  |
| Rotate     | \$F613                 | DISROT  | BDROT  | -          | 'DIFFY' style rotate                                       |  |
| Rotate     | \$F61F                 | POTATA  | PROT   | -          | 'PACKET' style rotate                                      |  |
| Detete     | <b>¢</b> E <b>co</b> o |         | ADDOT  |            | 'PACKET' style rotate                                      |  |
| Rotate     | \$F622                 | POTATE  | APROT  | -          | X=Soource U=Dest<br>(ANGLE)                                |  |
| Rotate     | \$F5FF                 | RATOT   | LROT90 | \$C8       | Rotate a single line A=Initial Y B=Angle                   |  |

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| Rotate | \$F603 | ROTAR  | ALNROT | \$C8 | Rotate a single line A=Initial Y (ANGLE)                 |
|--------|--------|--------|--------|------|--|
| Rotate | \$F601 | ROTOR  | LNROT  | \$C8 | Rotate a single line A=Initial Y B=Angle                 |
| Maths  | \$F584 | ABSVAL | ABSAB  | -    | Form absolute value for 'A' & 'B' registers              |
| Maths  | \$F58B | AOK    | ABSB   | -    | Form absolute value for 'B' register                     |
| Maths  | \$F57E | BITE   | DECBIT | -    | Decode bit position A=Bit number (0-7)                   |
| Maths  | \$F593 | COMPAS | CMPASS | \$C8 | Return angle for given delta 'Y:X'                       |
| Maths  | \$F5D9 | COSGET | COSINE | -    | Calculate the cosine of 'A'                              |
| Maths  | \$F511 | RAND3  | -      | -    | Calculate new random number from SEED                    |
| Maths  | \$F517 | RANDOM | -      | -    | Calculate new random number from SEED                    |
| Maths  | \$F663 | RCOS   | LCSINE | \$C8 | Multiply 'LEG' by previous cosine value                  |
| Maths  | \$F661 | RCOSA  | MCSINE | \$C8 | Multiply 'A' by previous cosine value                    |
| Maths  | \$F65D | RSIN   | LSINE  | \$C8 | Multiply 'LEG' by previous sine value                    |
| Maths  | \$F65B | RSINA  | MSINE  | \$C8 | Multiply 'A' by previous sine value WSINE                |
| Maths  | \$F5EF | SINCOS | -      | \$C8 | Calculate the sine and cosine of 'ANGLE'                 |
| Maths  | \$F5DB | SINGET | SINE   | \$C8 | Calculate the sine of 'A'                                |
| Misc   | \$F67F | BAGAUX | BLKMV1 | -    | Transfer A+1 bytes (127 max) from source U destination X |
| Misc   | \$F545 | CLR256 |        | -    | Clear 256 bytes at address<br>X                          |
| Misc   | \$F542 | CLRMEM | CLREX  | -    | Clear executive area of memory (\$C800 - \$C8FF)         |
| Misc   | \$F53F | CLRSON | BCLR   | -    | Clear 'B' bytes from X                                   |
| Misc   | \$F55E | DEKR   | DECTMR | -    | Decrement interval timers (XTMR0 � XTMR5)                |



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| Misc | \$F55A | DEKR3   | D3TMR -  | Decrement 3 interval timers (XTMR0 • XTMR2)                | 3  |   |
|------|--------|---------|----------|--|--|---|
| Misc | \$F57A | DEL     | -        | Delay execution for a<br>minimum of 20 cycles<br>(B=Delay) |  |   |
| Misc | \$F57D | DEL13   | -        | Delay execution for 13 cycles                              |  |   |
| Misc | \$F579 | DEL20   | -        | Delay execution for 20 cycles                              |  | Want to help support<br>my content creation?  |
| Misc | \$F575 | DEL28   | -        | Delay execution for 28 cycles                              |  | nq content creation:  |
| Misc | \$F571 | DEL33   | -        | Delay execution for 33 cycles                              |  | SUBSCRIBESTAR   |
| Misc | \$F56D | DEL38   | -        | Delay execution for 38 cycles                              |  |   |
| Misc | \$F1AA | DPIO    | -        | Set 6809 �DP� register for I/O accesses (\$D0)             |  |   |
| Misc | \$F1AF | DPRAM   | -        | Set 6809 �DP� register for RAM accesses (\$C8)             |  |   |
| Misc | \$F552 | FILL    | BLKFIL - | Set a block of memory A=data B=bytes X=dest                |  |   |
| Misc | \$F8FF | FINDBOX | BXTEST - | Symmetric collison test                                    | A = Box  Y  dimension  (delta Y  )  B = Box  X  dimension  (delta X  )  X = Y:X  coordinates of point to be tested  Y = Y:X  coordinates of box center | Recent New Content  Amiga - ASM PSET and POINT for Pixel Plotting  Learn 65816 Assembly: 8 and 16 bit modes on the 65816  SNES - ASM PSET and POINT for Pixel Plotting  ARM Assembly Lesson H3  Lesson P65 - Mouse reading on the Sam Coupe |
| Misc | \$F548 | GILL    | CLRBLK - | Clear D bytes of memory from X                             | DOX GOTTLOT  | Mouse Reading in MS-DOS  Risc-V Assembly Lesson 3 - Bit   |

| Misc  | \$F550 | NEGSOM  | CLR80  | -    | Set B bytes at memory X to \$80  |
|-------|--------|---------|--------|------|--|
| Misc  | \$F8E5 | OFF1BOX | OFF1BX | -    | Off-center symmetric collision test  |
| Misc  | \$F8F3 | OFF2BOX | OFF2BX | -    | Off-center symmetric collision text  |
| Misc  | \$F683 | STFAUX  | BLKMOV | -    | Transfer �A� bytes (0-127) from source �U� to destination �X�                |
| Sound | \$F92E | AXE     | EXPLOD | \$C8 | Complex explosion sound effect U=table                                       |
| Sound | \$F9CA | LOUDIN  | SETAMP | \$C8 | Set amplitude in <b>♦</b> REQx <b>♦</b><br>B=Volume                          |
| Sound | \$F259 | PSG     | WRPSG  | \$D0 | Write to PSG A=PSG Addr<br>B=PSG data X=mirror                               |
| Sound | \$F284 | PSGLPU  | PSGMIR | \$D0 | Send sound string to PSG from U and mirror X                                 |
| Sound | \$F27D | PSGLUP  | PSGLST | \$D0 | Send sound string to PSG from U  |
| Sound | \$F256 | PSGX    | WRREG  | \$D0 | Write to PSG and Morror<br>A=Psg Addr B=PSG Data                             |
| Sound | \$F687 | REPLAY  | -      | \$C8 | Set tune Sequence to list U  |
| Sound | \$F289 | REQOUT  | -      | \$D0 | Send 'REQX' to PSG and mirror  |
| Sound | \$F690 | SOPLAY  | ASPLAY | \$C8 | Set tune sequence with<br>alternate note set X=user<br>not table U=Tune List |
| Sound | \$F68D | SPLAY   | -      | \$C8 | Set tune Sequence to list U  |
| Sound | \$F742 | XPLAY   | -      | \$C8 | Terminate current tune   |
| Sound | \$F692 | YOPLAY  | TPLAY  | \$C8 | Set tune Sequence to list U  |

### **OS Vars**

| Address | Name | Details                       |
|---------|------|-------------------------------|
| \$C800  | REG0 | Channel A: Fine tone period   |
| \$C801  | REG1 | Channel A: Course tone period |
| \$C802  | REG2 | Channel B: Fine tone period   |

ops and more maths!

**Mouse reading on the MSX** 

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\$150 calculator: Unboxing the Ti-84 Plus CE (eZ80 cpu)

**Notes** 

| \$C803<br>\$C804<br>\$C805<br>\$C806<br>\$C807<br>\$C808<br>\$C809<br>\$C80A<br>\$C80B<br>\$C80C<br>\$C80D<br>\$C80E<br>\$C80F<br>\$C811<br>\$C812<br>\$C813<br>\$C814<br>\$C815<br>\$C816<br>\$C817<br>\$C818<br>\$C816<br>\$C817<br>\$C818<br>\$C819<br>\$C81B<br>\$C81D<br>\$C81B<br>\$C81C<br>\$C81D<br>\$C81B<br>\$C81C<br>\$C81D<br>\$C81B<br>\$C81C<br>\$C81D<br>\$C81C<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D<br>\$C81D | REG3 REG4 REG5 REG6 REG7 REG8 REG9 REGA REGB REGC REGD REGE TRIGGR EDGE (HEDGES) KEY0 KEY1 KEY2 KEY3 KEY4 KEY5 KEY6 KEY7 POTRES POT0 POT1 POT2 POT3 EPOT0 (DPOT0) EPOT1 (DPOT1) EPOT2 (DPOT2) | Channel B: Course tone period Channel C: Fine tone period Channel C: Course tone period Noise period Tone / Noise enables Channel A: Amplitude Channel B: Amplitude Channel C: Amplitude Fine envelope period Course envelope period Envelope shape / cycle I/O port data register Collective Switch Settings Used by button handlers  Controller #1 - Switch #1 Controller #1 - Switch #2 Controller #1 - Switch #3 (Rightmost) Controller #2 - Switch #1 Controller #2 - Switch #1 Controller #2 - Switch #3 Controller #2 - X' Axis Joystick #1 - 'X' Axis Joystick #2 - 'X' Axis Controller #1: Right / left joystick pot enable (must be \$00 or \$01) Controller #1: Up / down joystick pot enable (must be \$00 or \$03)  Controller #2: Right / left joystick #2 enable (must be \$00 or \$05) |
|--|---|--|
|  | (DPOT1<br>EPOT2<br>(DPOT2)  |  |
| \$C822<br>\$C823   | EPOT3 (DPOT3)<br>LIST   | Controller #2: Up / down joystick #2 enable (must be \$00 or \$07)  Number Of Vectors to be drawn  |
|  |   |  |

| \$C824                  | ZSKIP              | Flag controlling whether integrators will be zeroed:                                 |
|-------------------------|--------------------|--|
| \$C825 -<br>\$C826      | FRAME              | Frame Counter  |
| \$C827                  | TENSTY             | Contains the last value used for the intensity setting                               |
| \$C828                  | DWELL              | Dot ♦ON♦ time  |
| \$C829                  | DASH               | Dash pattern for drawing routines  |
| \$C82A -<br>\$C82B      | SIZRAS             | Raster Message Size (\$HHWW)   |
| \$C82C -<br>\$C82D      | MESAGE             | Used by string display functions to hold pointer to message to be displayed.         |
| \$C82E                  | XTMR0 (X0)         | ?  |
| \$C82F                  | XTMR1 (X1)         | Countdown timer  |
| \$C830                  | XTMR2 (X2)         | Countdown timer  |
| \$C831                  | XTMR3 (X3)         | Countdown timer  |
| \$C832                  | XTMR4 (X4)         | Countdown timer  |
| \$C833                  | XTMR5 (X5)         | Countdown timer  |
| \$C834                  | ABSY               | Working storage for �CMPASS�   |
| \$C835                  | ABSX               | Working storage for <b>�</b> CMPASS <b>�</b>   |
| \$C836                  | ANGLE              | Angle for rotation   |
| \$C837 -<br>\$C838      | WSINE (SINE)       | Location for parameter passing. Generally contains the last sine value calculated    |
| \$C839 <b>�</b><br>C83A | WCSINE<br>(COSINE) | Location for parameter passing. Generally contains the last cosine value calculated. |
| \$C83B                  | LEG                | Executive Working Storage  |
| \$C83C                  | LAG                | Used by transformation functions   |
| \$C83D -                | FRMTIM             | •  |
| \$C83E                  | (XMSEC)            | Frame rate   |
| \$C83F                  | REQ0               | Envelope shape / cycle   |
| \$C840                  | REQ1               | Course envelope period   |
| \$C841                  | REQ2               | Fine envelope period   |
| \$C842                  | REQ3               | Channel C: Amplitude   |
| \$C843                  | REQ4               | Channel B: Amplitude   |
| \$C844                  | REQ5               | Channel A: Amplitude   |
| \$C845                  | REQ6               | Tone / noise enables   |
| \$C846                  | REQ7               | Noise period   |
| \$C847                  | REQ8               | Channel C: Course tone period  |
|                         |                    |  |



Z=no NZ=Yes

= sml

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| \$C848<br>\$C849<br>\$C84A<br>\$C84B<br>\$C84B<br>\$C84B -<br>\$C84F -<br>\$C850<br>\$C851<br>\$C853 -<br>\$C856<br>\$C855<br>\$C856<br>\$C858<br>\$C85B<br>\$C85B<br>\$C85B<br>\$C85B<br>\$C85B<br>\$C85B<br>\$C85E<br>\$C85E<br>\$C861 -<br>\$C862<br>\$C864<br>\$C865 -<br>\$C864<br>\$C865 -<br>\$C866<br>\$C867 -<br>\$C866<br>\$C866 -<br>\$C866 | REQ9 REQA REQB REQC REQD DOREMI  FADE VIBE TUNE NEWGEN TSTAT RESTC RATEA VIBA RATEB VIBB RATEC VIBC FADEA FADEA FADEB FADEC TONEA TONEA TONEB TONEC GAP F1FREQ TRIGGR | Channel C: Fine tone period Channel B: Course tone period Channel A: Course tone period Channel A: Fine tone period Channel A: Fine tone period Channel A: Fine tone period Note table pointer  Working storage for SELOPT and tune player subroutines Working storage for tune player subroutines ?  Working storage for tune player subroutines Working storage for DEXPLOD Working storage for EXPLOD Working storage for EXPLOD Working storage for EXPLOD Working storage for EXPLOD Working storage for SELOPT and tune player subroutines Working storage for tune player subroutines |
|--|---|---|
| \$C810<br>\$C867   | SATUS   | Working storage for �EXPLOD�  |
| \$C867   | SATUS   | Working storage for �EXPLOD�  |





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| \$C868<br>\$C869<br>\$C86B -<br>\$C86C<br>\$C86D -<br>\$C86E<br>\$C871<br>\$C872   | LATUS XATUS B1FREQ B2FREQ FEAST PEDGE  | Allocated but not used by Mine Storm or Executive Allocated but not used by Mine Storm or Executive Allocated but not used by Mine Storm or Executive Allocated but not used by Mine Store or Executive Allocated but not used by Mine Storm or Executive Allocated but not used by Mine Storm or Executive Allocated but not used by Mine Storm or Executive Allocated but not used by Mine Storm or Executive   |
|--|--|---|
| \$C873<br>\$C874 \$\infty\$<br>\$C875<br>\$C876<br>\$C877<br>\$C878<br>\$C879<br>\$C87A<br>\$C87B -<br>\$C87C<br>\$C87D \$\infty\$<br>C87F<br>\$C880<br>\$C881<br>\$C883<br>\$C884<br>\$C885 | NEDGE K1FREQ BACON XACON SPEKT PLAYRS (PLAYRZ) OPTION (GAMZ) SEED RANCID SBTN SJOY ETMP1 ETMP2 ETMP3 | Allocated but not used by Mine Storm or Executive Allocated but not used by Mine Storm or Executive Allocated but not used by Mine Storm or Executive Working storage for EXPLOD Allocated but not used by Mine Store or Executive Number of players (\$01 - \$09)  Number of player options (\$01 - \$09)  Seed used by the random number generator  Working storage for random number generators  First memory location available for use by a game Joystick mask (used by MineStorm) Mine Storm: Temporary working storage |
| \$C886<br>\$C887<br>\$C888<br>\$C889<br>\$C88A<br>\$C88B<br>\$C88C<br>\$C88F<br>\$C890<br>\$C891   | ETMP4 ETMP5 ETMP6 ETMP7 ETMP8 ETMP9 ETMP10 TEMP1 TEMP2 TEMP3   | Mine Storm: Temporary working storage   |





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```
$C892
           TEMP4
                         Mine Storm: Temporary working storage
$C893
                         Mine Storm: Temporary working storage
           TEMP5
                         Mine Storm: Temporary working storage
$C894
           TEMP6
                         Mine Storm: Temporary working storage
$C895
           TEMP7
$C896
           TEMP8
                         Mine Storm: Temporary working storage
$C897
           TEMP9
                         Mine Storm: Temporary working storage
$C898 -
           TEMP10
                         Mine Storm: Temporary working storage
$C89A
$C89B
           ACTPLY
                         Mine Storm: Currently active player ($00 or $02)
$C89C -
           TMR1
                         Mine Storm: Timer
$C89E
$C89F -
           TMR2
                         Mine Storm: Timer
$C8A1
$C8A2 -
           TMR3
                         Mine Storm: Timer
$C8A4
$C8A5 -
           TMR4
                         Mine Storm: Timer
$C8A7
$C8A8 -
           SCOR1
                         Holds player 1 s score
$C8AE
$C8AF �
           SCOR2
                         Holds player 2 s score
C8B5
$C8EB -
           HISCOR
                         Contains ASCII high score
$C8F1
           (HEIGH)
```

### Hardware Registers

\$D000

```
$D001 8-bit port ♠A♠ (DAC and PSC data) [DAC]
$D002 Port ♠B♠ direction control [DCNTRL]
$D003 Port ♠A♠ direction control [DDAC]
$D004-7 Timer #1 [T1LOLC, T1HOC, T1LOL, T1HOL]
$D008-9 Timer #2 [T2LOLC, T2HOC]
$D00A Shift register [SHIFT]
$D00B Auxiliary control register [ACNTRL]
$D00C Peripheral control register [PCNTRL]
$D00D Interrupt flag register [IFLAG]
$D00E Interrupt enable register [IENABL]
```

8-bit port **P**B (Control bits) [CNTRL]

bit modes on the 65816

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# **Character Map**

The Vectrex has some character printing routines - they have a built in character map, which has upper case and a few system characters.



### **Direct Page**

The Vectrex uses two Direct page settings

\$C8 points to RAM

\$D0 points to Hardware registers

With ASW (Our assembler) use ASSUME dpr:\$xx to tell the assembler where the direct page is... this is called SETDP on some assemblers.

# AY Registers

The procedure for setting and reading AY registers is not quite direct... the method is shown here

Select Register: RegNum -> \$D001 #\$19 -> \$D000 #\$01 -> \$D000

Write Selected Register: New Value -> \$D001 #\$11 -> \$D000 #\$01 -> \$D000

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