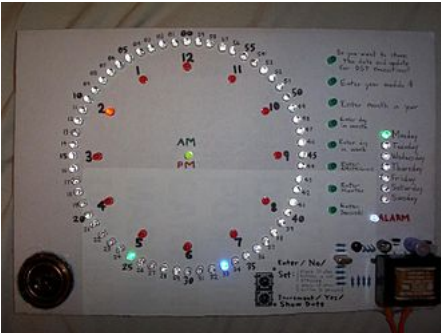


# Charlieplexing

**Charlieplexing** (also known as **tristate multiplexing**, **reduced pin-count LED multiplexing**, **complementary LED drive** and **crossplexing**) is a technique for driving a multiplexed display in which relatively few I/O pins on a microcontroller are used e.g. to drive an array of LEDs.

The method uses the tri-state logic capabilities of microcontrollers in order to gain efficiency over traditional multiplexing. Although it is more efficient in its use of I/O, there are issues that cause it to be more complicated to design and render it difficult for larger displays. These issues include duty cycle, current requirements and the forward voltages of the LEDs.

As with any multiplexing, there is a requirement to cycle through the in-use LEDs rapidly so that the persistence of the human eye perceives the display to be lit as a whole. Multiplexing can generally be seen by a strobing effect and skewing if the eye's focal point is moved past the display rapidly.



A Charlieplexed digital clock which controls 90 LEDs with 10 pins of a PIC16C54 microcontroller

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## Origin

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The Charlieplexing technique was introduced<sup>[1]</sup> by Maxim Integrated in 2001<sup>[2]</sup> as a reduced pin-count LED multiplexing scheme in their MAX6951 LED display driver.<sup>[2][1]</sup> The name "Charlieplexing", however, first occurred in a 2003 application note.<sup>[1]</sup> It was named after Charles "Charlie" M. Allen, an applications engineer of MAX232 fame,<sup>[3][4][5]</sup> who had proposed this method internally.

Also in 2001, Don Lancaster illustrated the method as part of his musings about the "N-connectedness" problem,<sup>[6]</sup> referring to Microchip Technology,<sup>[6]</sup> who had already discussed it as "complementary LED drive technique" in a 1998 application note<sup>[7]</sup> and would later include it in a tips & tricks booklet.<sup>[8]</sup>

While Microchip did not mention the origin of the idea, they might have picked it up in the PICLIST, a mailing list on Microchip PIC microcontrollers, where, also in 1998, Graham Daniel<sup>[9][10]</sup> proposed it to the community as a method to drive rows and columns of bidirectional LEDs. Daniel at the time had created simple circuits with PIC 12C508 chips driving 12 LEDs off 5 pins with a mini command set to set various lighting displays in motion.<sup>[9][10]</sup>

The method, however, was known and utilized by various parties much earlier in the 1980s, and has been described in detail as early as in 1979 in a patent by Christopher W. Malinowski, Heinz Rinderle and Martin Siegle of the Department of Research and Development, AEG-Telefunken, Heilbronn, Germany for what they called a "three-state signaling system".<sup>[11]</sup>

Reportedly, similar techniques were already in use as early as 1972 for track signaling applications in model railroading.<sup>[12]</sup>

## Traditional multiplexing

---

Display multiplexing is very different from multiplexing used in data transmission, although it has the same basic principles. In display multiplexing, the data lines of the displays\* are connected in parallel to a common databus on the microcontroller. Then, the displays are turned on and addressed individually. This allows use of fewer I/O pins than it would normally take to drive the same number of displays directly. Here, each "display" could, for instance, be one calculator digit, not the complete array of digits.

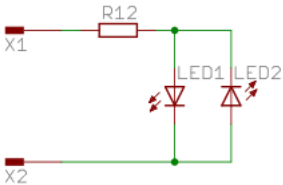
When using Charlieplexing,  $n$  drive pins can drive  $n$  digits with  $n - 1$  segments. When simplified, it equates to  $n$  pins being able to drive  $n^2 - n$  segments or LEDs. Traditional multiplexing takes many more pins to drive the same number of LEDs;  $2n$  pins must be used to drive  $n^2$  LEDs (though a 1-of- $n$  decoder chip can be used to reduce the number of microcontroller I/O pins to  $n + \lceil \log_2 n \rceil$ ).

If the number of LEDs is known, then the previous equation can be worked backwards to determine the number of pins required. That is,  $L$  LEDs can be driven by  $\lceil \frac{1}{2} (1 + \sqrt{1 + 4 \cdot L}) \rceil$  pins.

Complementary drive

Charlieplexing in its simplest form works by using a diode matrix of complementary pairs of LEDs. The simplest possible Charlieplexed matrix would look like this:

Pins	LEDs
1	0
2	2
3	6
4	12
5	20
6	30
7	42
8	56
9	72
10	90
20	380
40	1560
$n$	$n^2 - n$



Minimal 2-pin configuration for identical LEDs

By applying a positive voltage to pin X1 and grounding pin X2, LED1 will light. Since current cannot flow through LEDs in reverse direction at this low voltage, LED2 will remain unlit. If the voltages on pin X1 and pin X2 are reversed, LED2 will light and LED1 will be unlit.

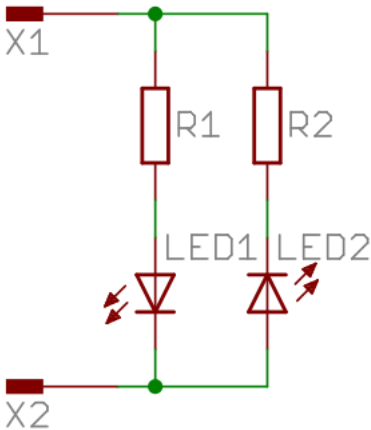
The Charlieplexing technique does not actually make a larger matrix possible when only using two pins, because two LEDs can be driven by two pins without any matrix connections, and without even using tri-state

mode. In this two-LED example, Charlieplexing would save one ground wire, which would be needed in a common 2-pin driver situation.

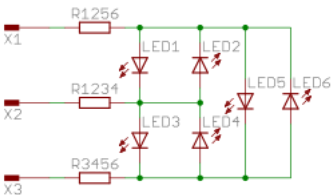
However, the 2-pin circuit serves as a simple example to show the basic concepts before moving on to larger circuits where Charlieplexing actually shows an advantage.

Expanding: tri-state logic

If the circuit above were to be expanded to accommodate 3 pins and 6 LEDs, it would look like this:



2-pin configuration for different LEDs



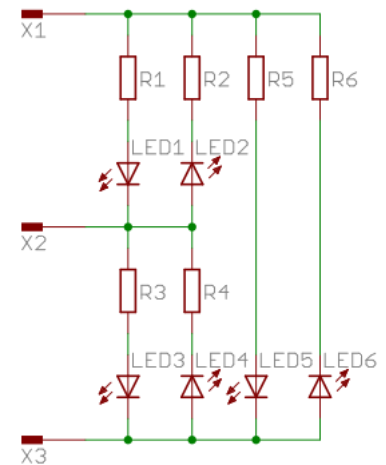
3-pin configuration for identical LEDs

This presents a problem, however. In order for this circuit to act like the previous one, one of the pins must be disconnected before applying charge to the remaining two. If, for example, LED5 was intended to be lit, X1 must be charged and X3 must be grounded. However, if X2 is also charged, LED3 would illuminate as well. If X2 was instead grounded, LED1 would illuminate, meaning that LED5 cannot be lit by itself. This can be solved by utilizing the tri-state logic properties of microcontroller pins. Microcontroller pins generally have three states: "high" (5 V), "low" (0 V) and "input". Input mode puts the pin into a high-impedance state, which, electrically speaking, "disconnects" that pin from the circuit, meaning little or no current will flow through it. This allows the circuit to see

any number of pins connected at any time, simply by changing the state of the pin. In order to drive the six-LED matrix above, the two pins corresponding to the LED to be lit are connected to 5 V (I/O pin "high" = binary number 1) and 0 V (I/O pin "low" = binary 0), while the third pin is set in its input state.

In doing so, current leakage out of the third pin is prevented, ensuring that the LED wished to be lit is the only one lit. Because the desired LED reduces the voltage available after the resistor, current will not flow across alternate paths (an alternate 2-LED path exists for every pair of pins in the 3-pin diagram, for example), so long as the voltage drop in the desired LED path is less than the total voltage drop across each string of alternative LEDs. However, in the variant with individual resistors this voltage-regulating effect does not affect the alternative paths so you must ensure that all LEDs used will not light with half the supply voltage applied because this variant does not benefit from the voltage-regulating effect of the desired path LED.

By using tri-state logic, the matrix can theoretically be expanded to any size, as long as pins are available. For  $n$  pins,  $n(n - 1)$  LEDs can be in the matrix. Any LED can be lit by applying 5 V and 0 V to its corresponding pins and setting all of the other pins connected to the matrix to input mode. Under the same constraints as discussed above up to  $n - 1$  LEDs sharing a common positive or negative path can be lit in parallel.



3-pin configuration for *different* LEDs

## Expanding

The 3-wire circuit can be rearranged to this near-equivalent matrix (resistors have been relocated).

This emphasizes the similarities between ordinary grid multiplex and Charlieplex, and demonstrates the pattern that leads to "the  $n$ -squared minus  $n$ " rule.

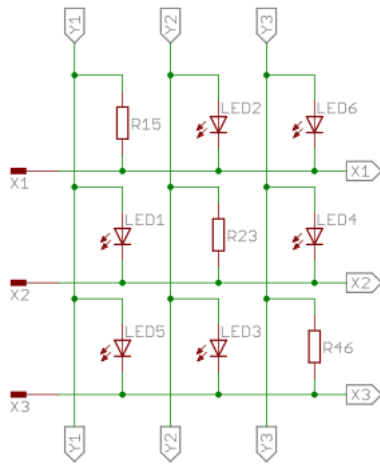
In typical usage on a circuit board the resistors would be physically located at the top of the columns and connected to the input pin. The rows would then be connected directly to the input pin bypassing the resistor.

The first setup in the image on the left is suitable only when identical LEDs are used since a single resistor is used for current-limiting through more than one LED (though not at the same time—rather, one resistor limits current through only one LED in a given column at one time). This is contrasted to the second configuration with individual resistors for each LED, as shown in the image on the right. In this second configuration, each LED has a unique resistor paired with it. This makes it possible to mix different kinds of LEDs by providing each with its appropriate resistor value.

In both of these configuration, as shown in both the left and the right image, the relocated resistors make it possible to light multiple LEDs at the same time row-by-row, instead of requiring that they be lit individually. The row current capacity could be boosted by an NPN emitter follower BJT transistor instead of driving the current directly with the typically much weaker I/O pin alone.

## Problems with Charlieplexing

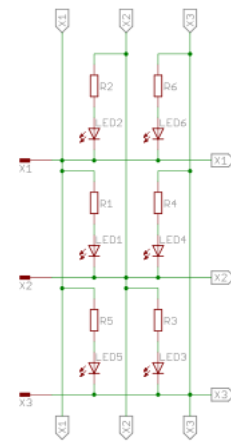
## Refresh rate



3-pin configuration arranged in a 3×2 display pattern for *identical* LEDs; any number of LEDs on a single row may be powered at a time

Because only a single set of LEDs, all having a common anode or cathode, can be lit simultaneously without turning on unintended LEDs, Charlieplexing requires frequent output changes, through a method known as multiplexing. When multiplexing is done, not all LEDs are lit quite simultaneously, but rather one set of LEDs is lit briefly, then another set, and eventually the cycle repeats. If it is done fast enough, they will appear to all be on, all the time, to the human eye because of persistence of vision. In order for a display to not have any noticeable flicker, the refresh

rate for each LED must be greater than 50 Hz.



3-pin configuration arranged in a 3×2 display pattern for *different* LEDs; any number of LEDs on a single row may be powered at a time

Suppose 8 tri-state pins are used to control 56 LEDs through Charlieplexing, which is enough for 8 7-segment displays (without decimal points). Typically 7-segment displays are made to have a common cathode, sometimes a common anode, but without loss of generality suppose it is a common cathode. All LEDs in all 8 7-segment displays cannot be turned on simultaneously in any desired combination using Charlieplexing. It is impossible to get 56 bits of information directly from 8 trits (the term for a base-3 character, as the pins are 3-state) of information, as 8 trits fundamentally comprises  $8 \log_2 3$ , or about 12.7 bits of information, which falls far short of the 56 bits required to turn all 56 LEDs on or off in any arbitrary combination. Instead, the human eye must be fooled by use of multiplexing.

Only one 7-segment display, one set of 7 LEDs can be active at any time. The way this would be done is for the 8 common cathodes of the 8 displays to each get assigned to its own unique pin among the 8 I/O ports. At any time, one and only one of the 8 controlling I/O pins will be actively low, and thus only the 7-segment display with its common cathode connected to that actively low pin can have any of its LEDs on. That is the active 7-segment display. The anodes of the 7 LED segments within the active 7-segment display can then be turned on in any combination by having the other 7 I/O ports either high or in high-impedance mode, in any combination. They are connected to the remaining 7 pins, but through resistors (the common cathode connection is connected to the pin itself, not through a resistor, because otherwise the current through each individual segment would depend on the number of total segments turned on, as they would all have to share a single resistor). But to show a desired number using all 8 digits, only one 7-segment display can be shown at a time, so all 8 must be cycled through separately, and in a 50th of a second for the entire period of 8. Thus the display must be refreshed at 400 Hz for the period-8 cycle through all 8 segments to make the LEDs flash no slower than 50 times per second. This requires constant interruption of whatever additional processing the controller performs, 400 times per second.

## Peak current

Due to the decreased duty cycle, the current requirement of a Charlieplexed display increases much faster than it would with a traditionally multiplexed display. As the display gets larger, the average current flowing through the LED must be (roughly) constant in order for it to maintain constant brightness, thus requiring the peak current to increase proportionally. This causes a number of issues that limit the practical size of a Charlieplexed display.

- LEDs often have a maximum peak current rating as well as an average current rating.
- If the microcontroller code crashes, and a one-led-at-a-time Charlieplex is being used, the single LED left lit is under much higher stress than it would be in a row-at-a-time Charlieplexed display or in a traditionally multiplexed display, increasing the risk of a failure before the fault is spotted.

## Requirement for tristate

All the outputs used to drive a Charlieplexed display must be tristate. If the current is low enough to drive the displays directly by the I/O pins of the microcontroller, this is not a problem, but if external tristates must be used, then each tristate will generally require two output lines to control, eliminating most of the advantage of a Charlieplexed display. Since the current from microcontroller pins is typically limited to 20 mA or so, this severely restricts the practical size of a Charlieplexed display. However, it can be done by enabling one segment at a time.<sup>[13]</sup>

## Complexity

Charlieplex matrices are significantly more complicated, both in the required PCB layout and microcontroller programming, than using prebuilt multiplex matrices. This increases design time. Soldering components can also be more time-consuming than for multiplexed LED arrays. A balance between complexity and pin use can be achieved by Charlieplexing several pre-built multiplexed LED arrays together.<sup>[14]</sup>

## Forward voltage

When using LEDs with different forward voltages, such as when using different color LEDs, some LEDs can light when not desired.

In the diagram above it can be seen that if LED 6 has a 4 V forward voltage, and LEDs 1 and 3 have forward voltages of 2 V or less, they will light when LED 6 is intended to, as their current path is shorter. This issue can easily be avoided by comparing forward voltages of the LEDs used in the matrix and checking for compatibility issues. Or, more simply, using LEDs that all have the same forward voltage.<sup>[6][1]</sup>

This is also a problem where the LEDs are using individual resistors instead of shared resistors, if there is a path through two LEDs that has less LED drop than the supply voltage these LEDs may also illuminate at unintended times.

## LED failure

If a single LED fails, by becoming either open-circuit, short-circuit, or leaky (developing a parasitic parallel resistance, which allows current in both directions), the impact will be catastrophic for the display as a whole. Furthermore, the actual problematic LED may be very difficult to identify, because potentially a large set of LEDs which should not be lit may all come on together, and—without detailed knowledge of the circuit—the relation between which LED is bad and what set of LEDs all come on together cannot be easily established.

If the failed LED becomes an open circuit, the voltage between the LED's 2 electrodes may build up until it finds a path through two other LEDs. There are as many such paths as there are pins used to control the array minus 2; if the LED with anode at node  $m$  and cathode at node  $n$  fails in this way, it may be that every single pair of LEDs in which one's anode is node  $m$ , cathode is  $p$  for any value of  $p$  (with the exceptions that  $p$  cannot be  $m$  or  $n$ , so there are as many possible choices for  $p$  as the number of pins controlling the array minus 2), along with the LED whose anode is  $p$  and cathode is  $n$ , will all light up.

If there are 8 I/O pins controlling the array, this means that there will be 6 parasitic paths through pairs of 2 LEDs, and 12 LEDs may be unintentionally lit, but fortunately this will only happen when the one bad LED is supposed to come on, which may be a small fraction of the time and will exhibit no deleterious symptoms when the problem LED is not supposed to be lit. If the problem is a short between nodes  $x$  and  $y$ , then every time any LED  $U$  with either  $x$  or  $y$  as its anode or cathode and some node  $z$  as its other electrode is supposed to come on (without loss of generality, suppose  $U$ 's cathode is connected to  $x$ ), the LED  $V$  with cathode  $y$  and anode  $z$  will light as well, so any time either node  $x$  or  $y$  is activated as an anode OR a cathode, two LEDs will come on instead of one. In this case, it lights only one additional LED unintentionally, but it does it far more frequently; not merely when the failed LED is supposed to come on, but when *any* LED that has a pin in common with the failed LED is supposed to come on.

The problematic elements become especially difficult to identify if there are two or more LEDs at fault. What this means is that unlike most methods in which the loss of a single LED merely causes a single burned-out segment, when Charlieplexing is used, one or two burned-out LEDs, whatever the mode of failure, will almost certainly cause a catastrophic cascade of unintended lightings of the LEDs that still work, very likely rendering the entire device completely and immediately unusable. This must be taken into account when considering the required lifetime and failure characteristics of the device being designed.

## Alternative use cases and variants

---

### Input data multiplexing

Charlieplexing can also be used to multiplex digital input signals into a microcontroller. The same diode circuits are used, except a switch is placed in series with each diode. To read whether a switch is open or closed, the microcontroller configures one pin as an input with an internal pull-up resistor. The other pin is configured as an output and set to the low logic level. If the input pin reads low, then the switch is closed, and if the input pin reads high, then the switch is open.<sup>[15]</sup>

One potential application for this is to read a standard (4×3) 12-key numeric keypad using only 4 I/O lines. The traditional row-column scan method requires  $4 + 3 = 7$  I/O lines. Thus Charlieplexing saves 3 I/O lines; however it adds the expense of 12 diodes, (since the diodes are only free when LEDs are used). A variation of the circuit with only 4 diodes is possible,<sup>[15]</sup> however this reduces the rollover of the keyboard. The microcontroller can always detect when the data is corrupt, but there is no

guarantee it can sense the original key presses, unless only one button is pressed at a time. (However, it is probably possible to arrange the circuit so that if at most any two adjacent buttons are pressed, then no data loss will occur.) The input is only lossless in the 4-diode circuit if only one button is pressed at a time, or if certain problematic multiple key presses are avoided. In the 12-diode circuit, this is not an issue, and there is always a one-to-one correspondence between button presses and input data. However, there are so many diodes that are required to use the method (especially for larger arrays) that there is generally no cost savings over the traditional row-column scan method, unless the cost of a diode is only a fraction of the cost of an I/O pin, where that fraction is one over the number of I/O lines.

## GuGaplexing

In 2008, Dhananjay V. Gadre devised *Gugaplexing*, which is like Charlieplexing with multiple drive voltages.<sup>[16][17]</sup>

## Chiplexing

In 2008, Guillermo Jaquenod's so called *Chiplexing* adds emitter followers to boost the strength of the row drive allowing rows wider than a single microcontroller port could drive to be lit simultaneously.<sup>[18][19]</sup>

## Cross-plexing

In 2010, the Austrian chip manufacturer austriamicrosystems AG (named ams AG<sup>[nb 1]</sup> since 2012, and ams-OSRAM AG since 2020) introduced the multiplexing LED driver IC AS1119,<sup>[20][21]</sup> followed by the AS1130 in 2011.<sup>[22][23]</sup> Also, the analog & mixed signal (AMS)<sup>[nb 1]</sup> division (named Lumissil Microsystems since 2020) of Integrated Silicon Solution Inc. (ISSI) introduced the IS31FL3731 in 2012<sup>[24][25]</sup> and the IS31FL3732 in 2015.<sup>[26][27][28]</sup> They all use a technique they call *cross-plexing*, a variant of Charlieplexing with automatic detection of open or shorted connections and anti-ghosting measures.<sup>[29]</sup>

## Tucoplexing

In 2019, Micah Elizabeth Scott developed a method to use 3 pins to run 4 LEDs and 4 switches called *Tucoplexing*.<sup>[30]</sup>

## Pulse-width modulation

Charlieplexing can even be used with pulse-width modulation to control the brightness of 12 LEDs with 4 pins.<sup>[31]</sup>

## Code example

---



In the following code example, the circuit<sup>[32][33]</sup> uses ATtiny 8-pin microcontroller which has 5 I/O pins to create a 7-segment display. Since a 7-segment display only requires control of 7 individual LEDs, we use 4 of the ATtiny I/O pins as charlieplexed outputs ( $n*(n-1)$ ). Leaving the fifth I/O pin to be used as digital or analog input or another output.

```

1 // ATtiny code
2 // Reads analog (or digital) input from pin 4 and every time the input goes below a set threshold
3 // it counts one and displays the increase in count either by activating up one of four LEDs (or transistors)
4 // or one of twelve charlieplexed LEDs.
5
6 // SET THESE VALUES:
7 int threshold = 500;
8 int maxCount = 7;
9 ///////////////////////////////////////////////////
10 boolean sensorTriggered = false;
11 int count = 0;
12 int sensorValue = 0;
13 long lastDebounceTime = 0; // the last time the output pin was toggled
14 long debounceDelay = 50; // the debounce time; increase if the output flickers
15 ///////////////////////////////////////////////////
16 void setup() {
17   for (int pin=0; pin<4; pin++) {
18     pinMode(pin, OUTPUT);
19     digitalWrite(pin, LOW);
20   }
21   pinMode(4, INPUT);
22   digitalWrite(4, HIGH); // internal pull-up
23 }
24 ///////////////////////////////////////////////////
25 void loop() {
26   testDigits();
27 }
28 void testDigits() {
29   charlieLoop();
30 }
31 ///////////////////////////////////////////////////
32 void readSensor() {
33   sensorValue = analogRead(2); // pin4!
34   delay(100);
35   if (sensorValue < threshold && sensorTriggered == false) {
36     sensorTriggered = true;
37     count++;
38     if (count > maxCount) count = 0;
39     charlieLoop();
40   }
41   if (sensorValue > threshold) sensorTriggered = false;
42 }
43 ///////////////////////////////////////////////////
44 void charlieLoop() {
45   count++;
46
47   for (int i=0; i<1000; i++) {
48     for (int c=0; c<count; c++) {
49       charliePlexPin(c);
50     }
51   }
52   delay(1000);
53   if (count > maxCount) count = 0;
54 }
55 ///////////////////////////////////////////////////
56 void charliePlexPin(int myLed){
57
58   // Make sure we don't feed random voltages to the LEDs
59   // during the brief time we are changing pin modes and voltages.
60   pinMode(0, INPUT);
61   pinMode(1, INPUT);
62   pinMode(2, INPUT);
63   pinMode(3, INPUT);
64
65   switch(myLed){

```

```

66
67     case 0:
68         pinMode(0, OUTPUT);
69         pinMode(2, OUTPUT);
70         digitalWrite(2, LOW);
71         digitalWrite(0, HIGH);
72         break;
73
74     case 1:
75         pinMode(3, OUTPUT);
76         pinMode(2, OUTPUT);
77         digitalWrite(2, LOW);
78         digitalWrite(3, HIGH);
79         break;
80
81     case 2:
82         pinMode(3, OUTPUT);
83         pinMode(1, OUTPUT);
84         digitalWrite(1, LOW);
85         digitalWrite(3, HIGH);
86         break;
87
88     case 3:
89         pinMode(1, OUTPUT);
90         pinMode(0, OUTPUT);
91         digitalWrite(0, LOW);
92         digitalWrite(1, HIGH);
93         break;
94
95     case 4:
96         pinMode(0, OUTPUT);
97         pinMode(1, OUTPUT);
98         digitalWrite(1, LOW);
99         digitalWrite(0, HIGH);
100        break;
101
102     case 5:
103         pinMode(2, OUTPUT);
104         pinMode(0, OUTPUT);
105         digitalWrite(0, LOW);
106         digitalWrite(2, HIGH);
107         break;
108
109     case 6:
110         pinMode(2, OUTPUT);
111         pinMode(1, OUTPUT);
112         digitalWrite(1, LOW);
113         digitalWrite(2, HIGH);
114         break;
115     }
116 }
117 ///////////////////////////////////////////////////////////////////////////////////////////////////////////////////////////////////
118 void spwm(int freq, int pin, int sp) {
119     // call charlieplexing to set correct pin outs:
120     //on:
121     digitalWrite(pin, HIGH);
122     delayMicroseconds(sp * freq);
123     // off:
124     digitalWrite(pin, LOW);
125     delayMicroseconds(sp * (255 - freq));
126 }

```

## See also

- [Anti-parallel \(electronics\)](#)
- [Flicker fusion](#)
- [N-connectedness](#)
- [Polarity reversing switch](#)

## Notes

1. The ICs AS1119 and AS1130 were introduced by austriamicrosystems AG (formerly Austria Mikro Systeme), later named ams AG and ams-OSRAM AG. The ICs IS31FL3731 and IS31FL3732 were introduced by the AMS (analog & mixed signal) division (now called Lumissil Microsystems) of the fab-less chip manufacturer Integrated Silicon Solution Inc. (ISSI). Presumably, the fact that both manufacturers of cross-plexing LED driver ICs carry "AMS" in their name is coincidence only.

## References

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2. *MAX6950/MAX6951 - Serially Interfaced, +2.7V to +5.5V, 5- and 8-Digit LED Display Drivers* (<http://www.allcomponents.ru/pdf/maxim/max6950max6951.pdf>) (PDF). Revision 1. Sunnyvale, California, USA: Maxim Integrated Products. December 2001. 19-2227. Archived (<https://web.archive.org/web/20211210200824/http://www.allcomponents.ru/pdf/maxim/max6950max6951.pdf>) (PDF) from the original on 2021-12-10. Retrieved 2021-12-10. (19 pages)
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