

SPR-128

PRELIMINARY INFORMATION

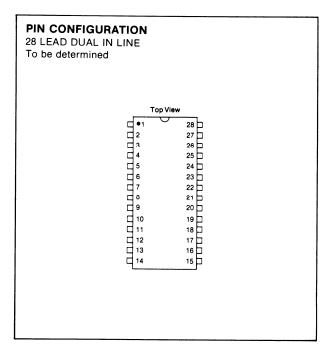
131072 Bit Serial Read Only Memory

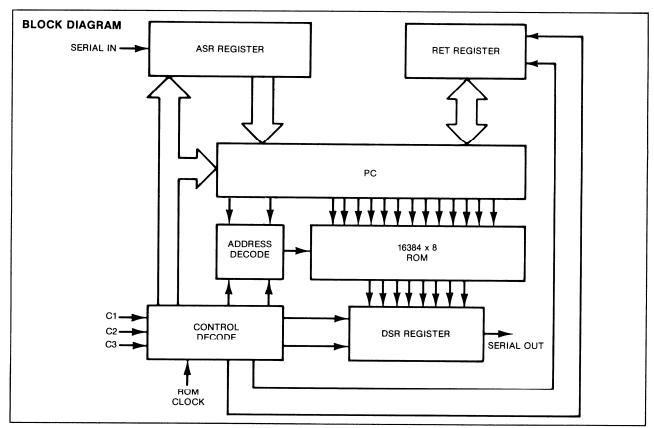
FEATURES

- 16K x 8 Organization
- Single +5 Volt Supply
- Tri-State Serial Output
- Totally Automated Custom Programming
- Zener Protected Inputs
- Glass Passivation Protection

DESCRIPTION

The General Instrument SPR-128 is a 131,072 Bit Serial Read Only Memory organized as 16,384 eight-bit words and is ideally suited for interfacing with the SP-0256 Speech Processor. Fabricated in General Instrument's advanced N-Channel Ion-Implant process to enable operation from a single +5 Volt power supply, up to 4 SPR-128s can be interfaced to the SP-0256 without buffering.





PIN ASSIGNMENTS

Pin Number	Name	Function			
	ROM ENABLE	Active low chip select used in system to eliminate bus conflict at system start-up. When brought high, ROM ENABLE Tri-States Serial Out.			
	SERIAL IN	Serial Input used to load 16 bit address into device.			
	SERIAL OUT	Output pin used to shift out data byte.			
	CS1	Active high chip select. Will Tri-State Serial output when low.			
	CS2	Active low chip select. Will Tri-State Serial output when high.			
	ROM CLOCK	1.56MHz clock input from SP-0256 speech processor.			
	V _{SS}	Ground pin. Control pins decoded to determine device function.			
	C2 } C1	Positive supply pin (+4.6V to +7.0V)			

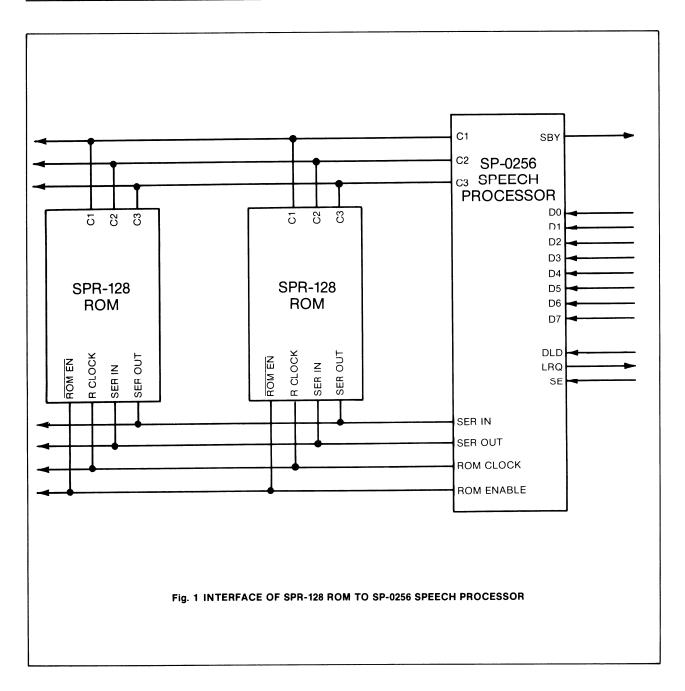


TABLE I SPR-128 CONTROL STATES

C1	C2 C3			Function				
0	0	0	NOP	No action taken.				
0	0	1	ASR Load	Accepts data from the serial input, synchronous to the externally supplied ROM clock. This data is shifted into the ASR holding register in preparation for loading into the PC. Although ASR is 16 bits long, it is not necessary to load all 16 bits of address sequentially in one ASR load.				
0	1	0	PC Load	Loads the contents of the ASR register into the PC.				
0	1	1	DSR Load	Loads the 8 bits of data pointed to by the present value of the least significant 14 bits of the PC into the data output shift register (DSR). At the completion of the DSR load the PC is incremented.				
1	0	0	DSR Shift-Out	Shifts out the contents of DSR to the serial out pin, synchronous to the ROM clock.				
1	0	1	RET Register Load	Loads the return register (RET) with the current value of the PC.				
1	1	0	Return	Loads the PC with the contents of the RET register.				
1	1	1	NOP	No action taken.				

ELECTRICAL CHARACTERISTICS

Maximum Ratings

V _{DD}
Storage Temperature25° to +125° C
Lead Temperature (soldering) 10 Sec

Standard Conditions (unless otherwise stated)

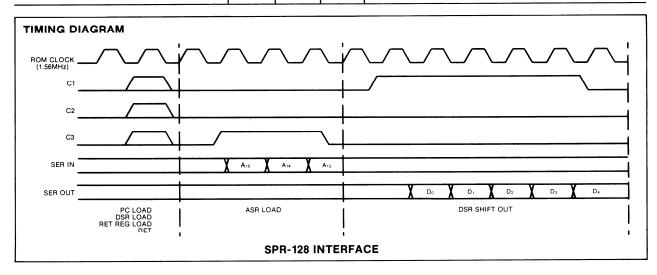
 $V_{DD} = +4.6 V$ to +7.0 VOperating Temperature = 0° C to +55° C

Supply Current

 $I_{DD} = 25 mA \quad V_{DD} = 7.0 V \\ V_{SS} = 0.0 V \\ ROM clock frequency typically 1.56MHz$

DC CHARACTERISTICS

Characteristic	Min	Max	Units	Conditions	
Inputs					
ROM ENABLE, SERIAL IN,			1		
CS1, CS2, C1, C2, C3,	l				
ROM Clock					
Logic 0	0.0	.4	V	_	
Logic 1	2.2	V_{DD}	V	_	
Capacitance	-	10	pF		
Leakage	_	10	μA	_	
Outputs	İ				
SERIAL OUT					
Logic 0	0.0	.6	V	1.6mA	
Logic 1	2.4	V_{DD}	l v	50μA	



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