LD2979

Very low drop voltage regulators with inhibit

Datasheet - production data

SOT23-5L

Features

- Very low dropout voltage (0.2 V typ. at 50 mA load)
- Very low quiescent current (typ. 500 μA at 50 mA load)
- Output current up to 50 mA
- Logic-controlled electronic shutdown
- Output voltages of 3.0 and 3.3 V
- Internal current and thermal limit
- Supply voltage rejection: 63 dB (typ)
- Only 1 μF for stability
- Temperature range: -25 °C to 125 °C
- Package available: SOT23-5L

Description

The LD2979 is a very low drop regulator available in SOT23-5L.

The very low drop-voltage and the very low quiescent current make them particularly suitable for low noise, low power applications and in battery powered systems.

Shutdown logic control function is available on five pin version (TTL compatible). This means that when the device is used as local regulator, it is possible to put a part of the board in standby, decreasing the total power consumption.

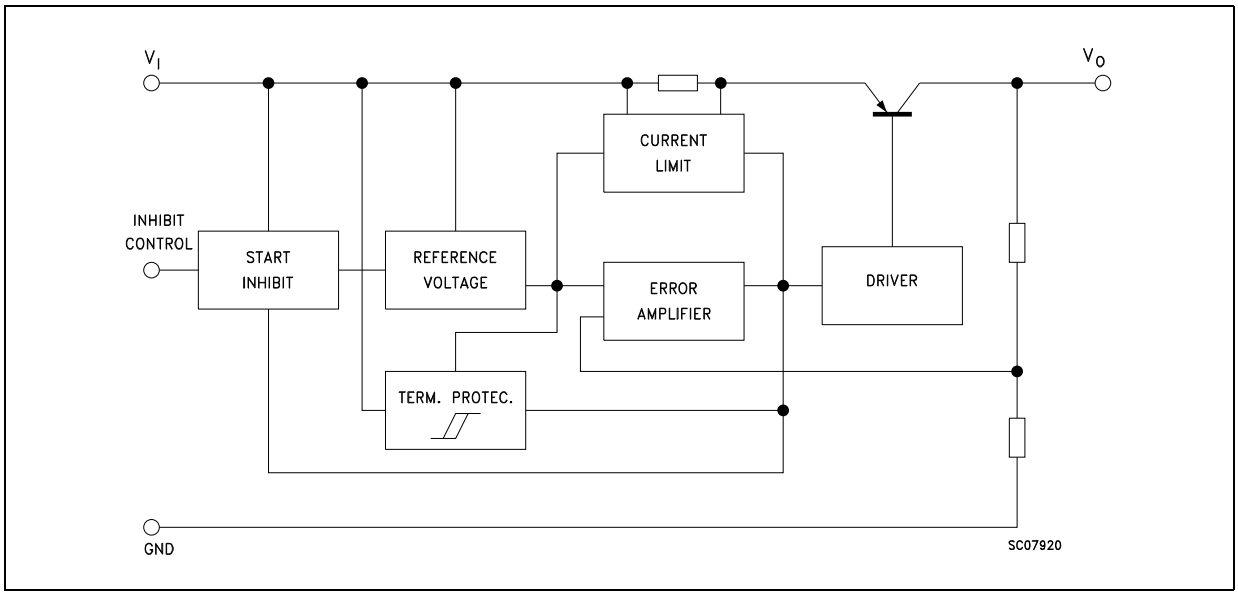
Table 1. Device summary

Order codes	Output voltages
LD2979M30TR	3.0 V
LD2979M33TR	3.3 V

Contents

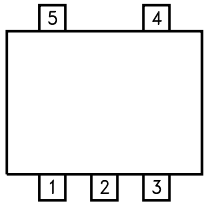
1 Diagram

Figure 1. Schematic diagram



2 Pin configuration

Figure 2. Pin connections (top view)



SOT23-5L

Table 2. Pin description

Symbol	Name and function	Pin number
V_I	Input voltage	1
GND	Ground	2
INHIBIT	Control switch ON/OFF (1)	3
NC	Not to be connected	4
V_O	Output voltage	5

1. Inhibit pin is not internally pulled-up then it must not be left floating. Connect to a positive voltage higher than 2 V to able the device.

3 Maximum ratings

Table 3. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_I	DC input voltage	16	V
V_{INH}	DC inhibit input voltage	V_{IN}	V
I_O	Output current	Internally limited	
P_D	Power dissipation	Internally limited	
T_{STG}	Storage temperature range	-40 to 150	°C
T_{OP}	Operating junction temperature range	-25 to 125	°C

Note: Absolute maximum ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied.

4 Electrical characteristics

(Refer to the test circuits, Ta = 25 °C, $V_{IN} = V_{O(NOM)} + 1$ V, I_O = 1 mA, V_{INH} = 2 V, C_O = 1 μF, unless otherwise specified).

Table 4. Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_O	Output voltage	V_{IN} = 4 V	2.940	3	3.060	V
		I_O = 1 to 50mA, T_a= -25 to 125°C	2.880		3.120	
V_O	Output voltage	V_{IN} = 4.3 V	3.234	3.3	3.366	V
		I_O = 1 to 50mA, T_a= -25 to 125°C	3.168		3.432	
I_O	Output current limit		100			mA
ΔV_O	Line regulation	$V_{IN} = V_{O(NOM)}$+1V to 16V, I_O = 1mA			0.028	%/V_{IN}
		T_a= -25 to 125°C			0.064	
I_d	Quiescent current (On Mode)	I_O = 0		80	110	μA
		I_O = 0, T_a= -25 to 125°C			170	
		I_O = 50mA		500	700	
		I_O = 50mA, T_a= -25 to 125°C			1300	
	Quiescent current (Off Mode)	V_{INH} < 0.18 V		0		μA
		V_{INH} < 0.18 V, T_a= -25 to 125°C			1	
SVR	Supply voltage rejection	I_O = 50mA, C_{OUT} = 10µF, f = 120Hz		63		dB
V_d	Dropout voltage	I_O = 0		6	12	mV
		I_O = 0, T_a= -25 to 125°C			18	
		I_O = 1mA		30	60	
		I_O = 1mA, T_a= -25 to 125°C			90	
		I_O = 10mA		100	200	
		I_O = 10mA, T_a= -25 to 125°C			300	
		I_O = 50mA		200	400	
		I_O = 50mA, T_a= -25 to 125°C			600	
V_{IL}	Inhibit input logic low	Device Off, T_a= -25 to 125°C			0.18	V
V_{IH}	Inhibit input logic high	Device On, T_a= -25 to 125°C	2			V
I_I	Inhibit input current	V_{INH} = 0 V		0	-1	μA
		V_{INH} = 5V, T_a= -25 to 125°C		5	15	
eN	Output noise voltage (RMS)	BW= 300Hz to 50kHz, C_O = 10μF		160		μV

5 Typical characteristics

(unless otherwise specified T_A = 25 °C)

Figure 3. Output voltage vs temperature

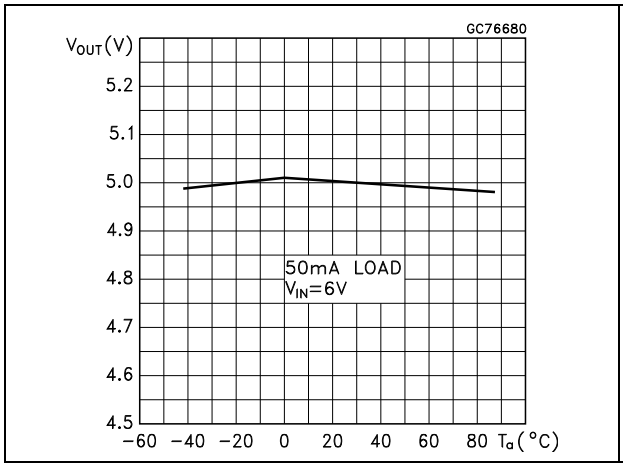


Figure 4. Output voltage vs input voltage

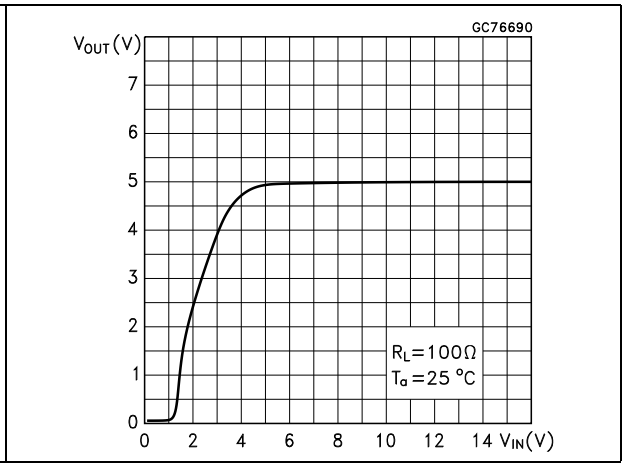


Figure 5. Output voltage vs input voltage

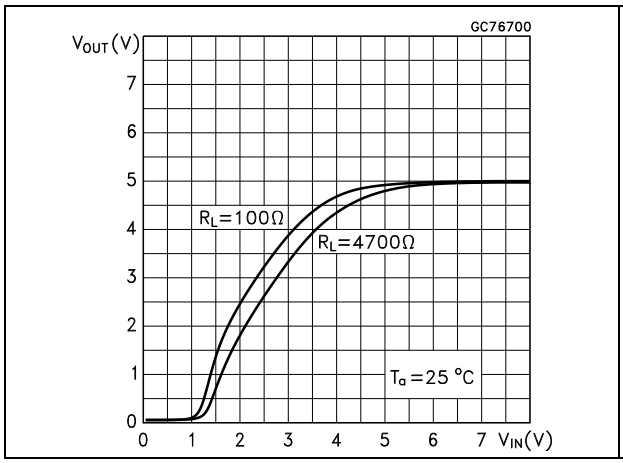


Figure 6. Dropout voltage vs output current

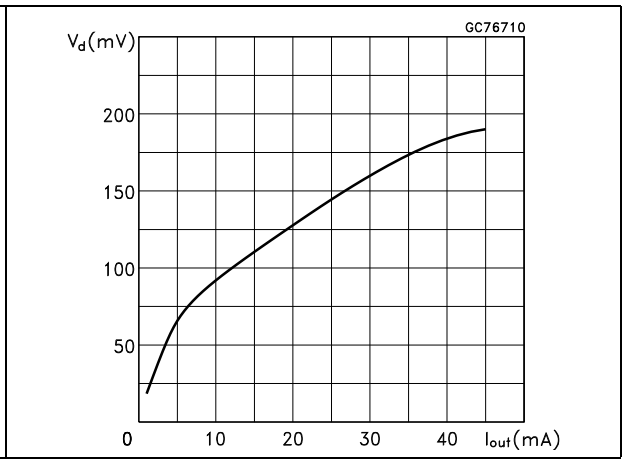


Figure 7. Dropout voltage vs temperature

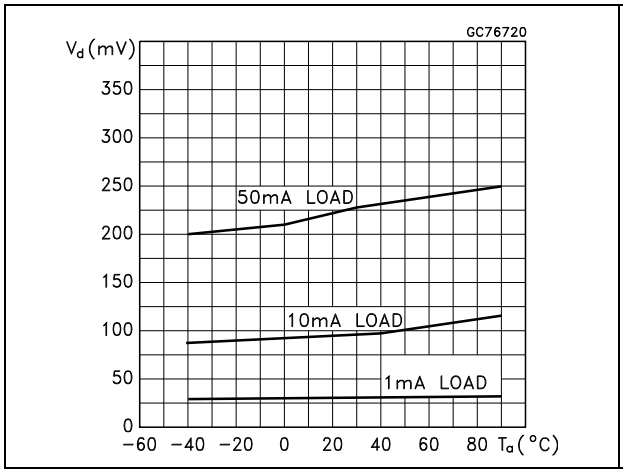


Figure 8. Quiescent current vs temperature

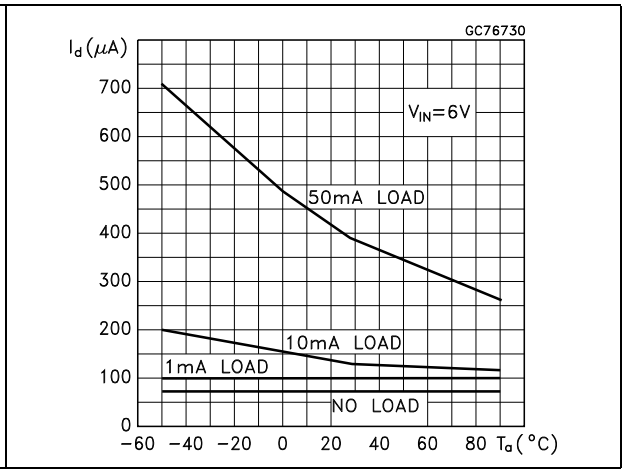


Figure 9. Short circuit current vs dropout voltage

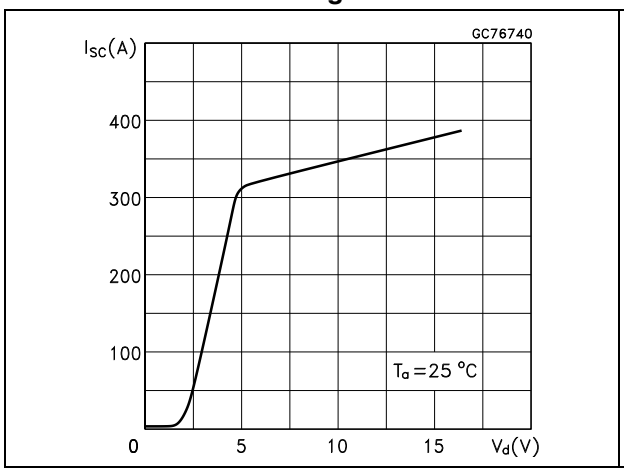


Figure 10. Inhibit voltage vs temperature

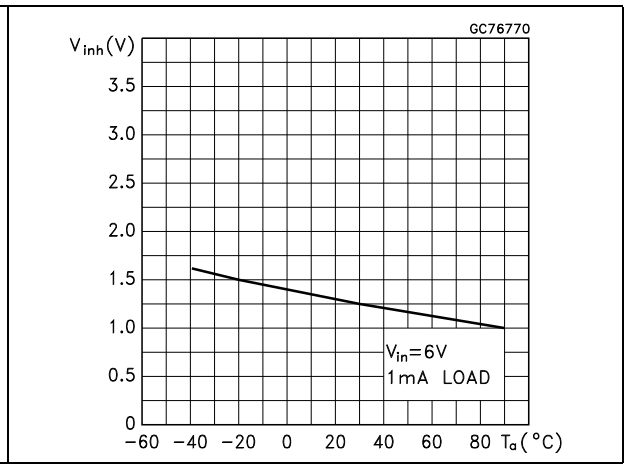


Figure 11. Supply voltage rejection vs frequency

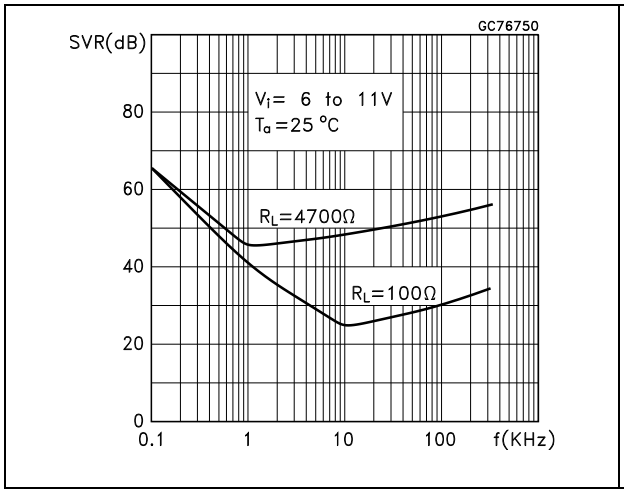
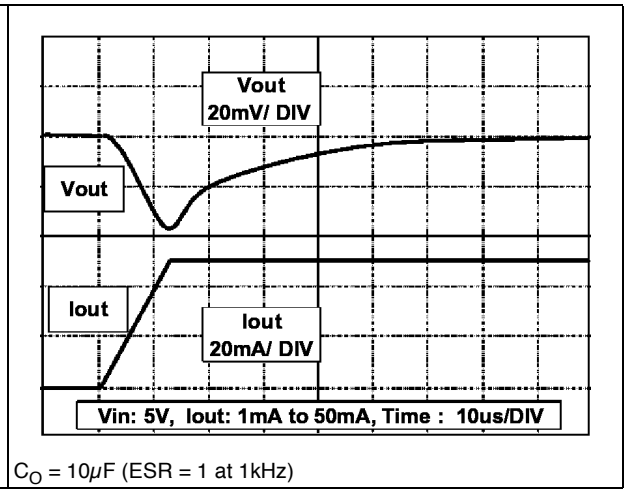


Figure 12. Load transient response



C_O = 10μF (ESR = 1 at 1kHz)

Figure 13. Inhibit current vs temperature

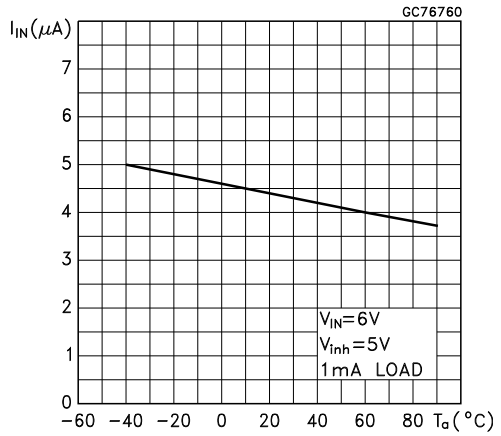
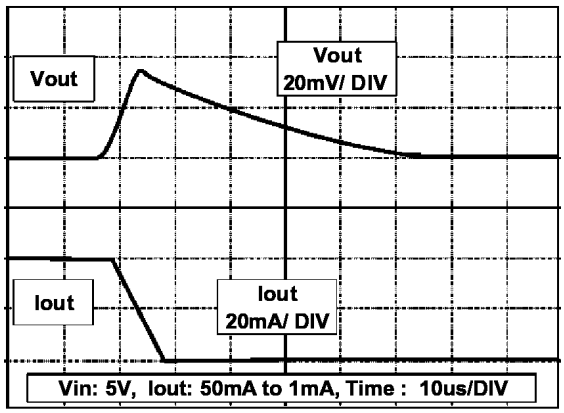
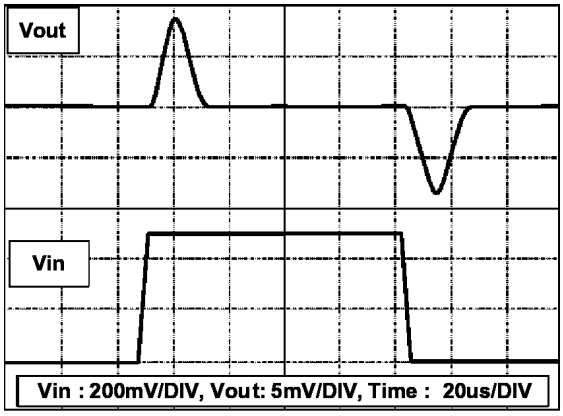


Figure 14. Load transient response



C_O = 10μF (ESR = 1 at 1kHz)

Figure 15. Line transient response



C_O = 10μF (ESR = 1 at 1kHz)

6 Package mechanical data

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Table 5. SOT23-5L mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A	0.90		1.45
A1	0		0.15
A2	0.90		1.30
b	0.30		0.50
c	2.09		0.20
D		2.95	
E		1.60	
e		0.95	
H		2.80	
L	0.30		0.60
θ	0		8

Figure 16. SOT23-5L mechanical drawing

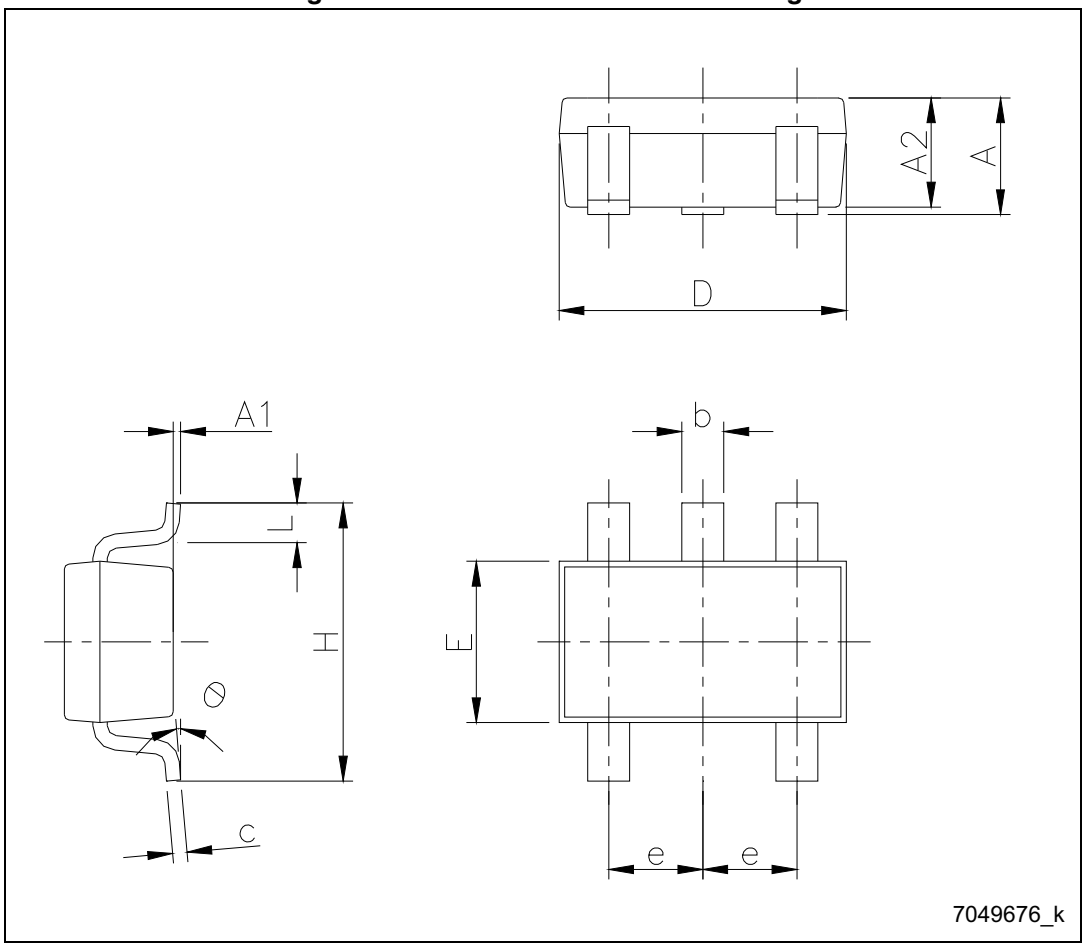
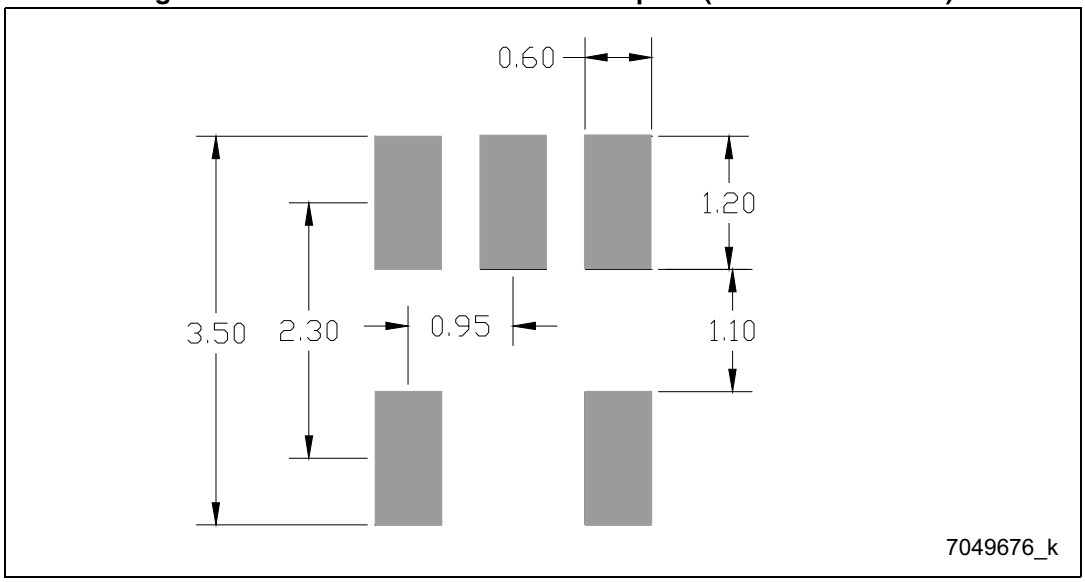


Figure 17. SOT23-5L recommended footprint (dimensions in mm)

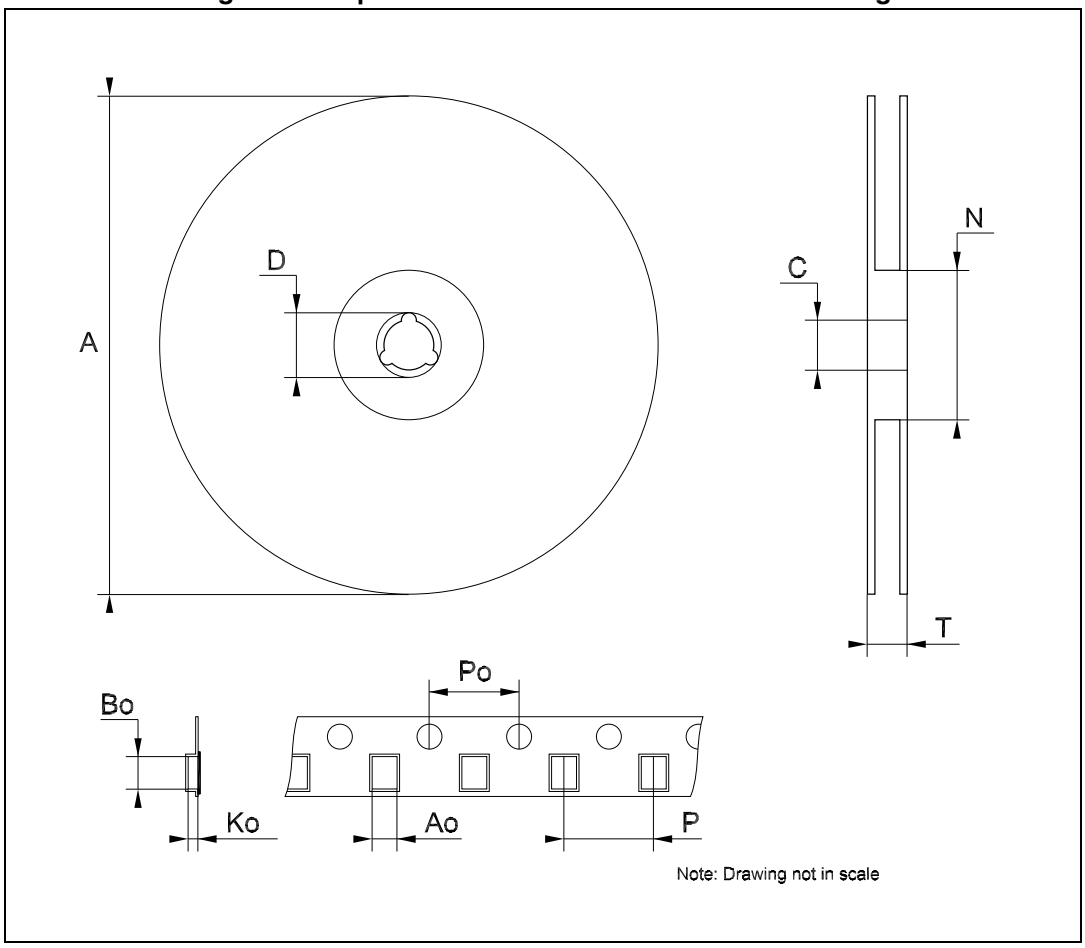


7 Packaging mechanical data

Table 6. Tape and reel SOT23-5L mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A			180
C	12.8	13.0	13.2
D	20.2		
N	60		
T			14.4
Ao	3.13	3.23	3.33
Bo	3.07	3.17	3.27
Ko	1.27	1.37	1.47
Po	3.9	4.0	4.1
P	3.9	4.0	4.1

Figure 18. Tape and reel SOT23-5L mechanical drawing



8 Revision history

Table 7. Document revision history

Date	Revision	Changes
15-Mar-2005	10	Add tape & reel for TO-92.
03-Jul-2006	11	Order codes updated.
16-May-2007	12	Order codes updated.
08-Jun-2007	13	Order codes updated.
09-Apr-2008	14	Modified: *Table 1 on page 1*.
06-Nov-2013	15	Document name changed from LD2979xx to LD2979. Updated *Table 1: Device summary*, *Table 1: Device summary* and *Section Table 4.: Electrical characteristics*. Minor text changes in features and description in cover page.

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