

Sheet: CPU and FPU

D[0..31]
 A[0..31]
 HALT
 MMUDIS
 CDIS
 STERM
 DSACK1
 BG
 CS_FPU
 BERR
 FC0
 FC1
 FC2
 RDWR
 DS
 ASD
 DSACK0
 IPL0
 IPL1
 IPL2
 FPUSENSE

File: CPU.sch

Sheet: Reset and LEDs

File: Reset.sch

Sheet: ROM and RAM

RDWR
 CS_SRAM
 CS_ROM
 RDWR
 D[0..31]
 A[0..31]

File: ROMRAM.sch

Sheet: Decode and Logic

SIZ0
 JTAG_TCK
 JTAG_TMS
 JTAG_TDI
 JTAG_TDO
 DSACK0
 DSACK1
 DRAM_DSACK0
 DRAM_DSACK1
 A[0..31]
 D[0..31]
 LDS
 UDS
 E
 EXT_DBEN
 RDWR
 EXT_IRQ[1..4]
 DSACK1_EXT
 DSACK0_EXT
 EXT_PRD0[1..4]
 EXT_PRD1[1..4]
 EXT_DTACK
 EXT_BERR
 CLK10
 CS_ETH
 RESET
 SIZ1
 DS
 AS
 CDIS
 MMUDIS
 CS_DRAM
 CS_DRAMCPLD
 CS_FPU
 FPUSENSE
 BERR
 RDWR
 INT_IDE
 INT_IDE
 RESET
 CS_SRAM
 CS_ROM
 IPL[0..2]
 IDE_RD
 IDE_WR
 CS_IDE
 USB_OVERCUR
 USB_ENABLED
 INT_USB
 CS_USB
 CS_RTC
 INT_PS2
 CS_KBD
 CS_DUART
 INT_DUART
 INT_ETH

File: Decode.sch

Sheet: DRAM

JTAG_TDO
 JTAG_TDI
 JTAG_TMS
 JTAG_TCK
 DRAM_CLK
 AS
 CS_DRAM
 STERM
 CBREQ
 SIZ0
 SIZ1
 BG
 CS_DRAMCPLD
 D[0..31]
 A[0..31]

File: DRAM.sch

Sheet: Expansion Slots

A[0..31]
 D[0..31]
 RESET
 AS
 DS
 LDS
 UDS
 SIZ0
 SIZ1
 RDWR
 RDWR
 VMA
 DSACK0_EXT
 DSACK1_EXT
 EXT_DTACK
 EXT_BERR
 EXT_IRQ[1..4]
 EXT_PRD0[1..4]
 EXT_PRD1[1..4]

E
 CLK10
 CPUCLK

CS_EXTREG[1..4]
 CS_EXTDATA[1..4]

EXT_DBEN

File: Expansion.sch

Sheet: IDE

D[0..31]
 A[0..31]
 IDE_WR
 IDE_RD
 RESET
 CS_IDE
 INT_IDE

File: IDE Interface.sch

Sheet: USB

D[0..31]
 A[0..31]
 USB_CS
 RDWR
 RDWR
 RESET
 USB_INT
 USB_ENABLED
 USB_OVERCUR

File: USB.sch

Sheet: Real Time Clock

A[0..31]
 D[0..31]
 CS_RTC
 RDWR
 RDWR
 RESET

File: RTC.sch

Sheet: Power

File: Power.sch

Sheet: Keyboard

INT_PS2
 RDWR
 RDWR
 CS_KBD
 RESET
 D[0..31]
 A[0..31]

File: Keyboard.sch

Sheet: Serial

CS_DUART
 INT_DUART
 A[0..31]
 D[0..31]
 EXT_PD[1..4]

File: Serial and USB.sch

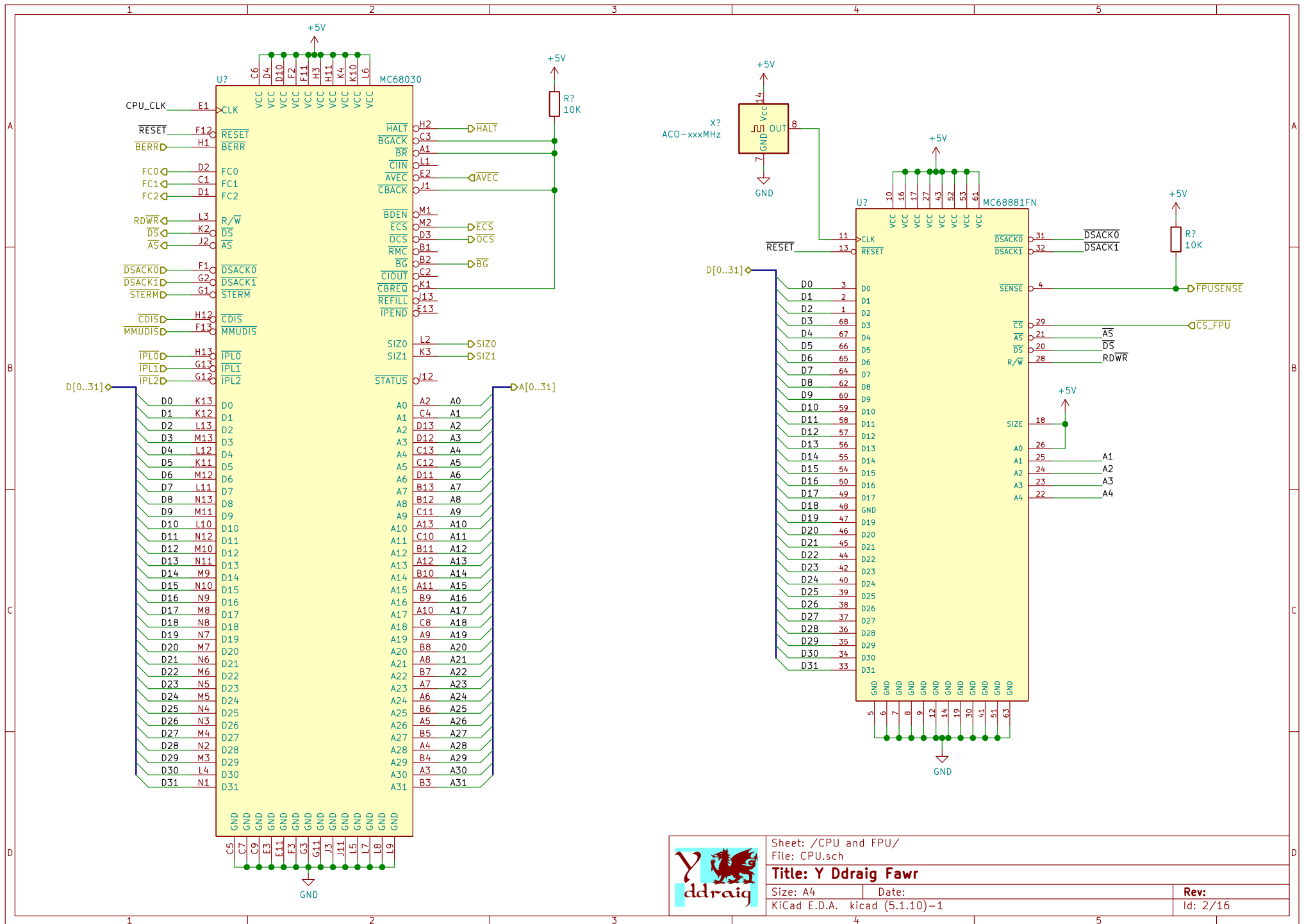
Sheet: Ethernet

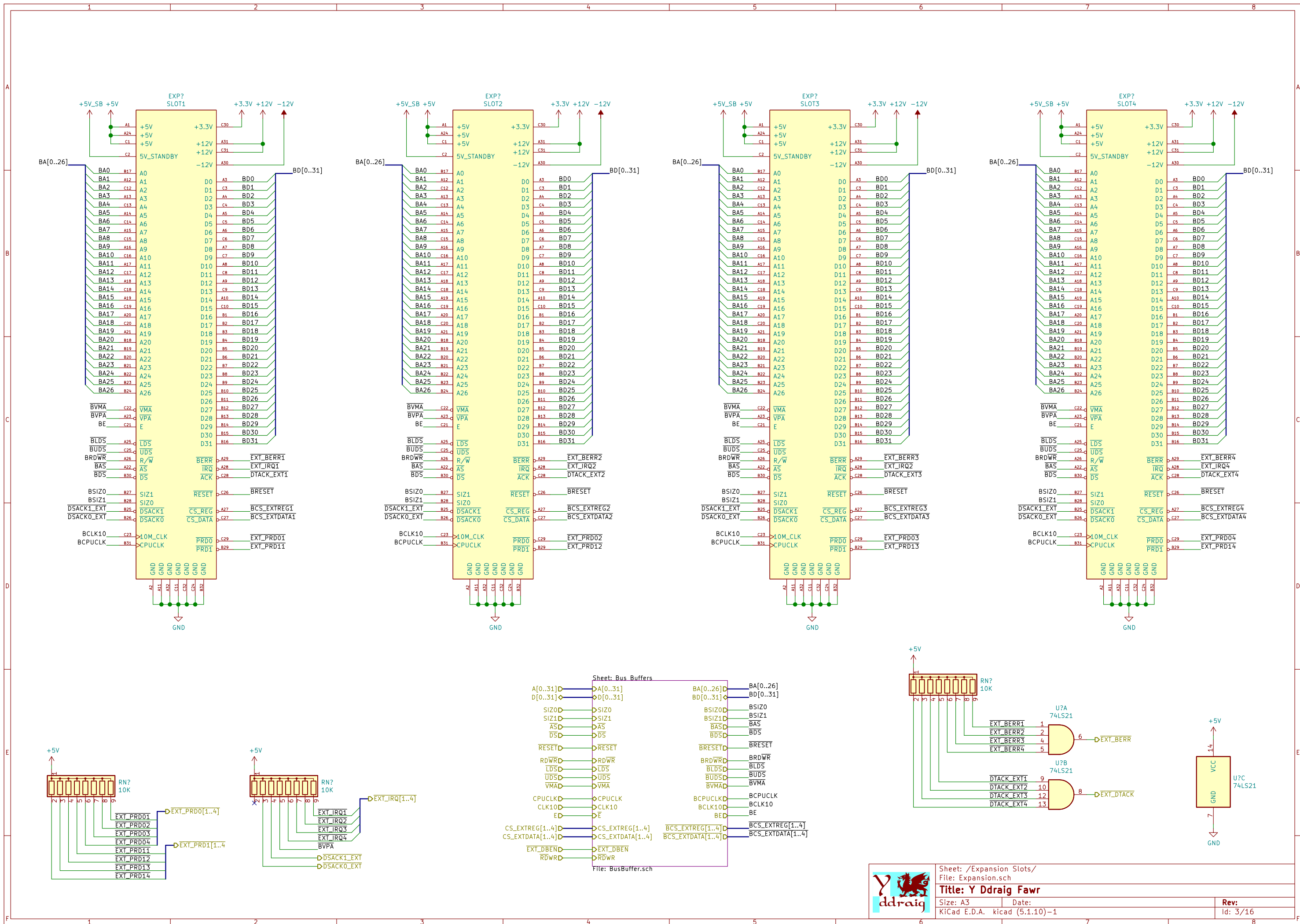
D[0..31]
 A[0..31]
 INT_ETH
 CS_ETH
 RDWR
 RDWR
 RESET

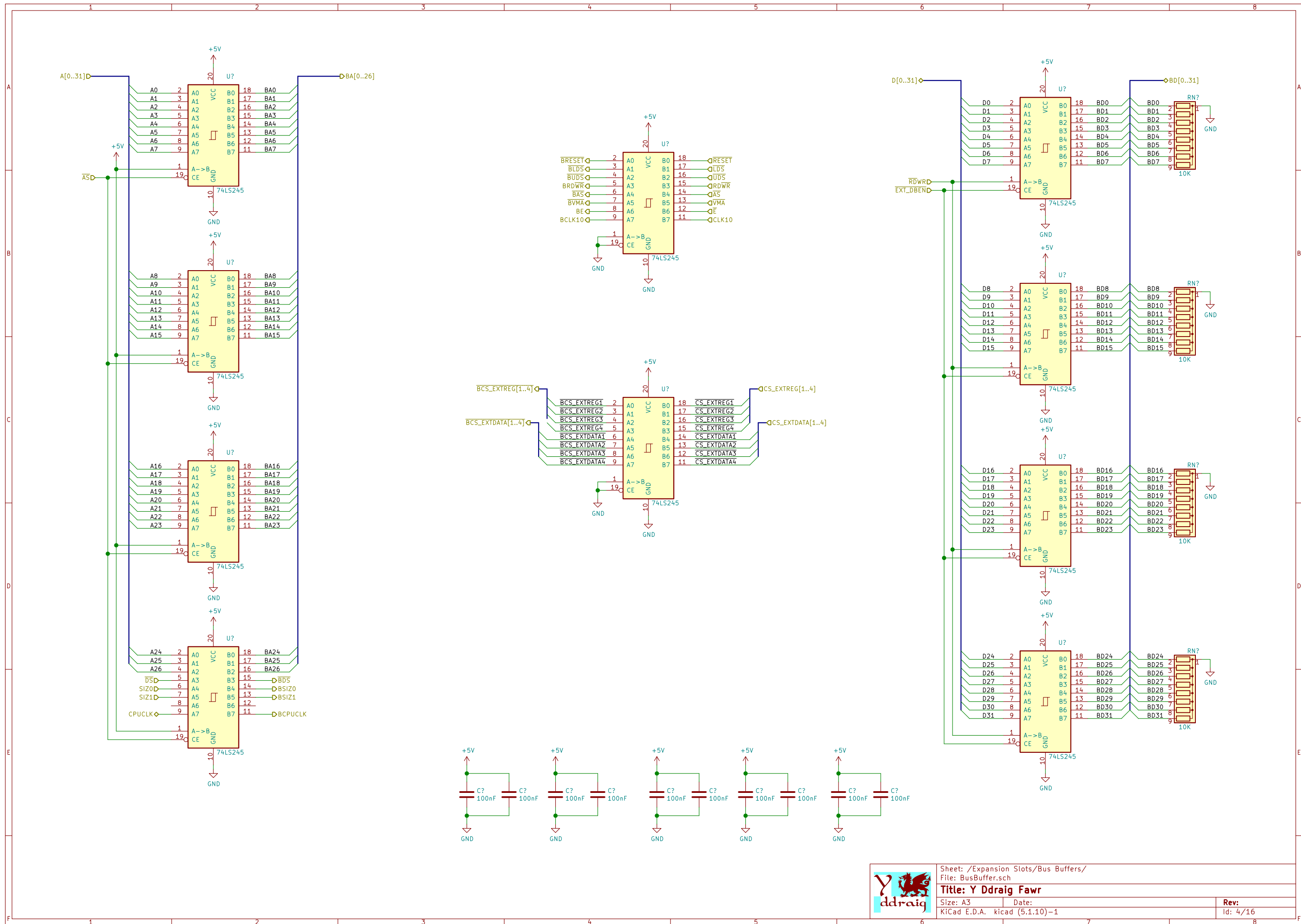
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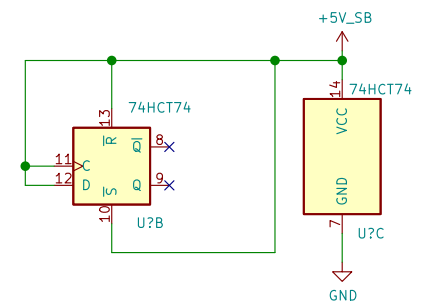
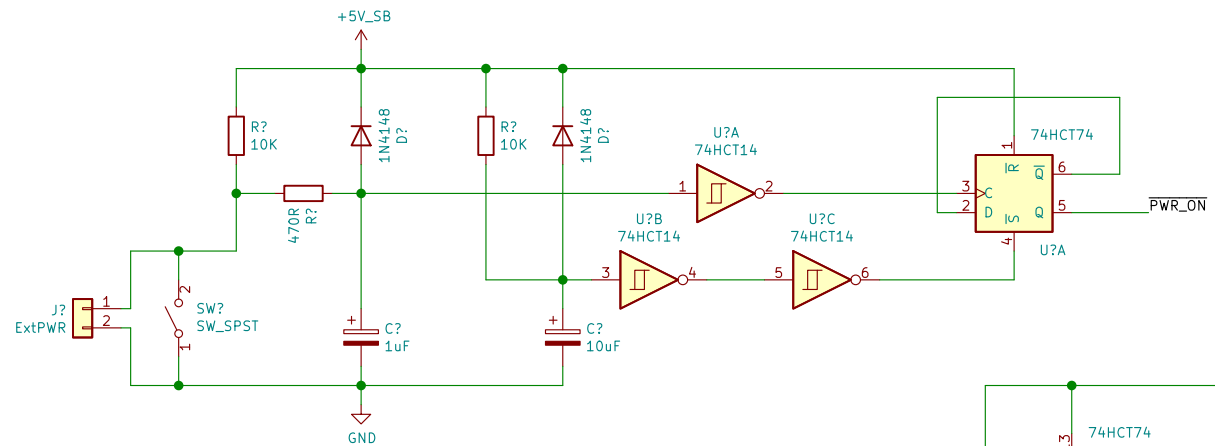
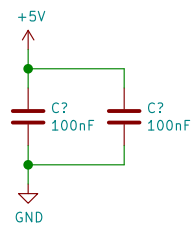
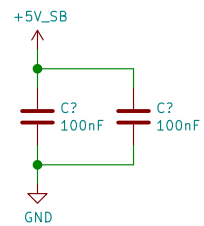
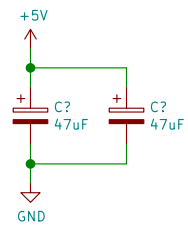
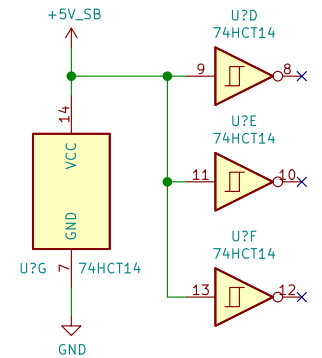
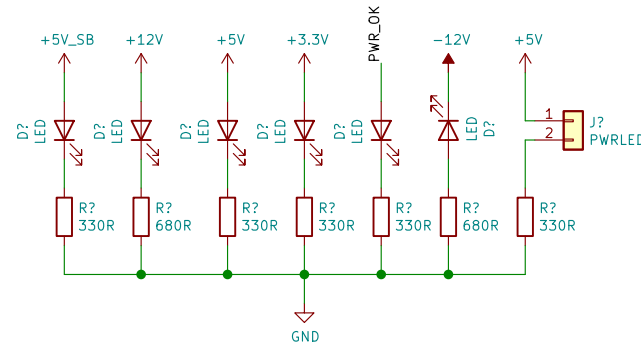
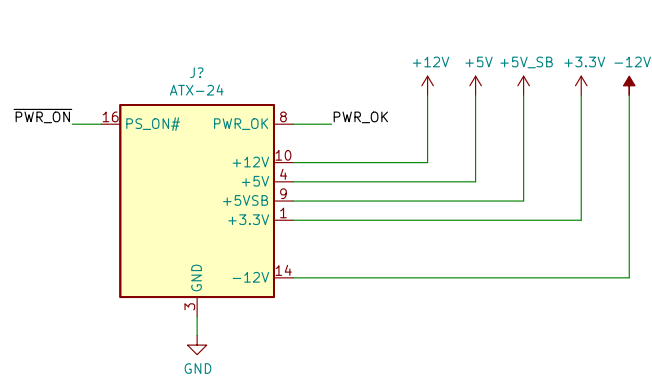


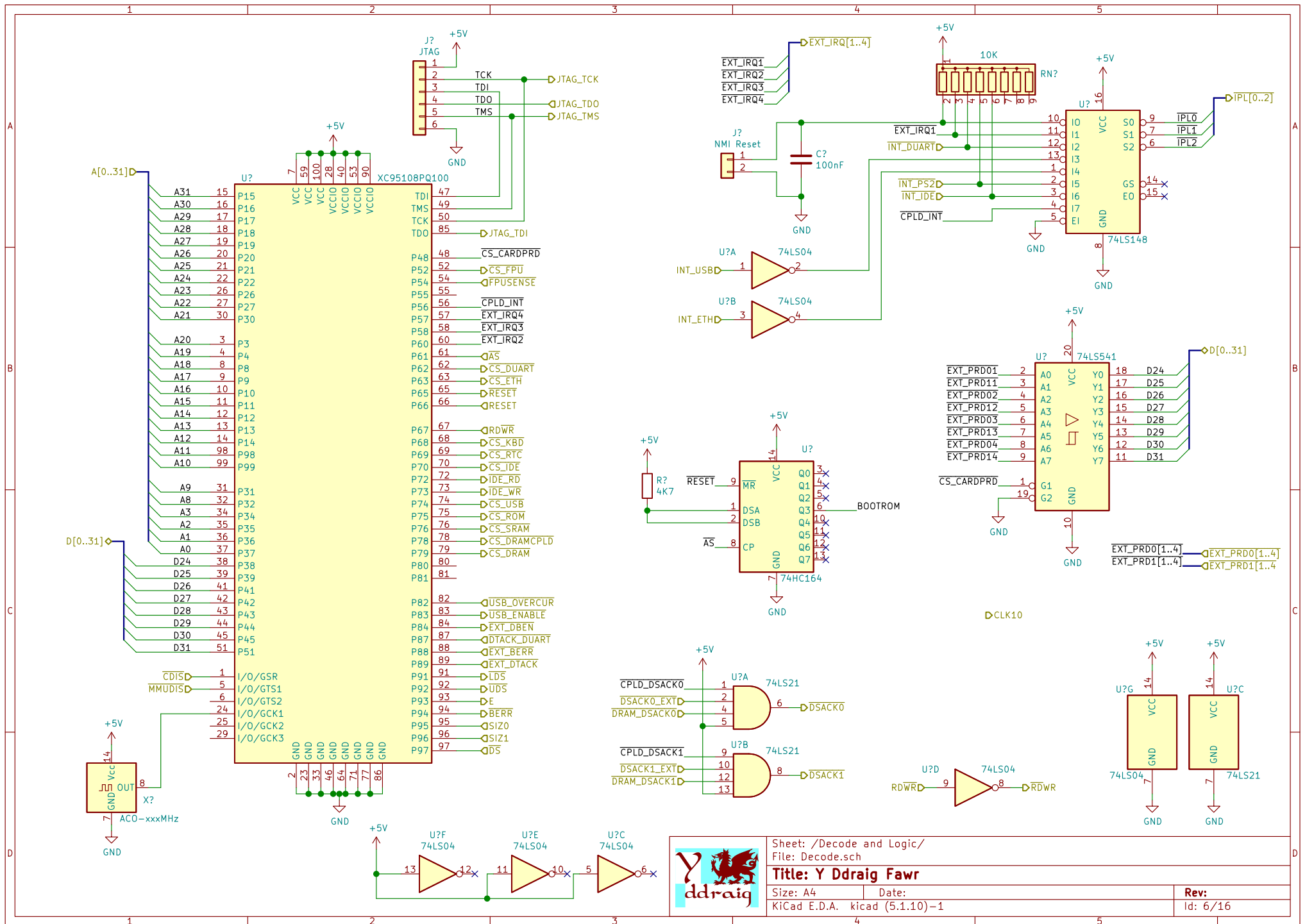
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Size: A4	Date:
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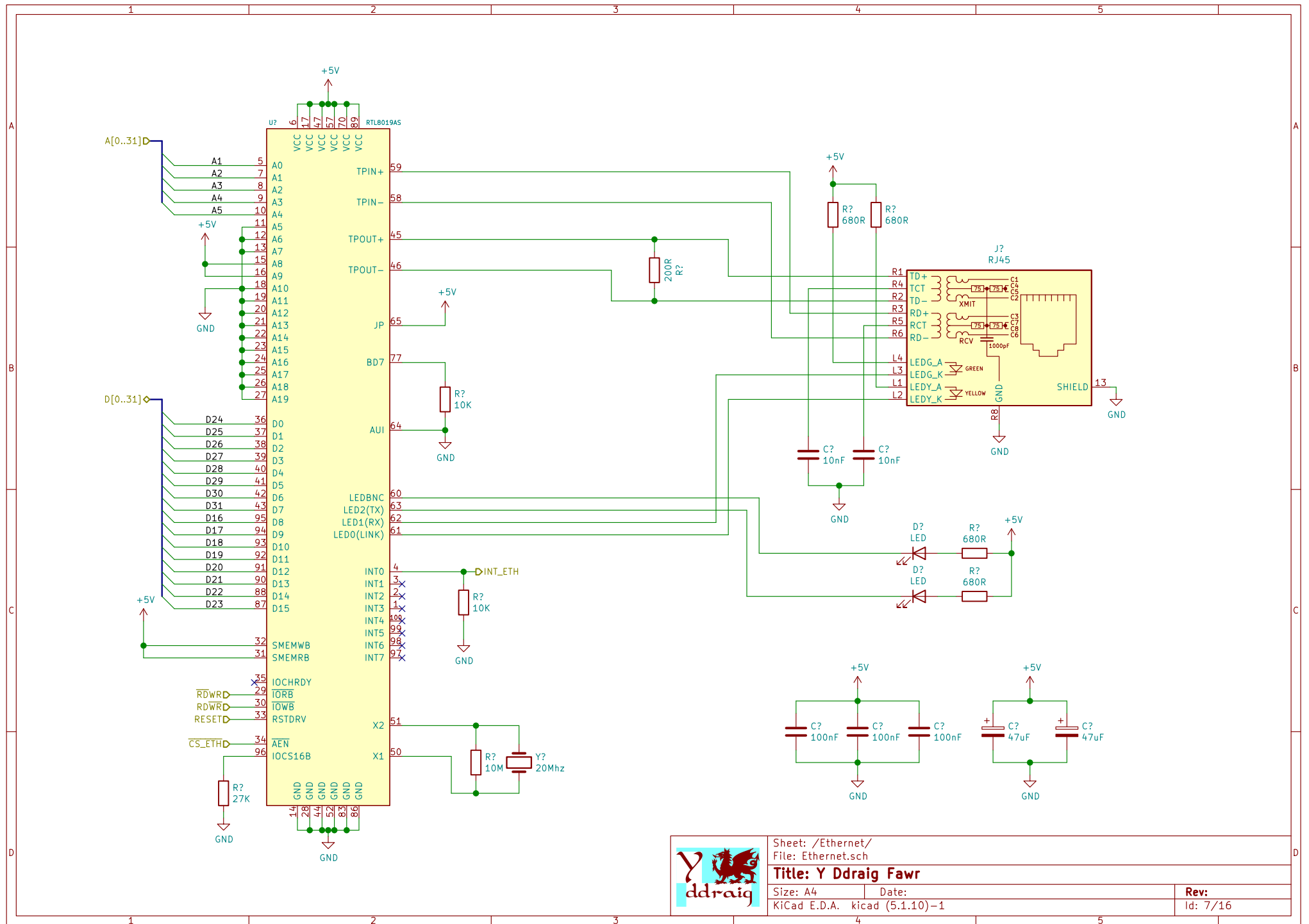




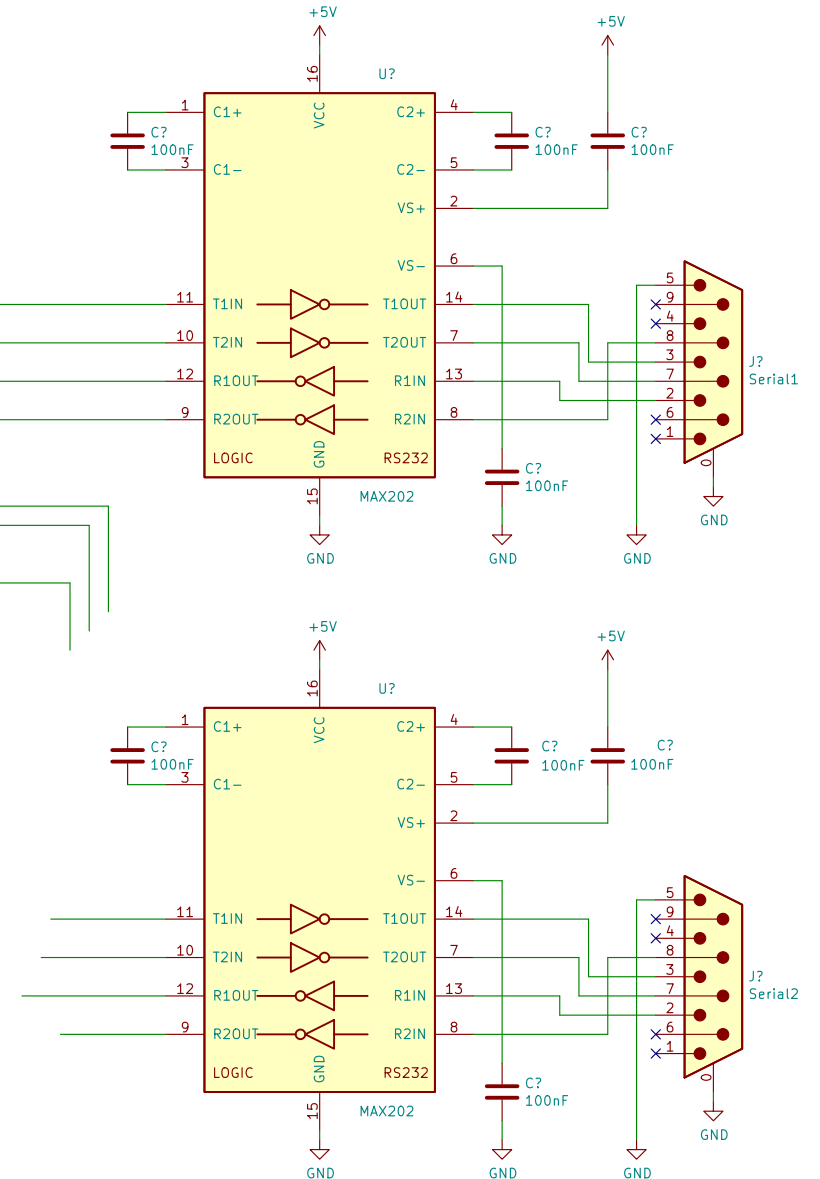
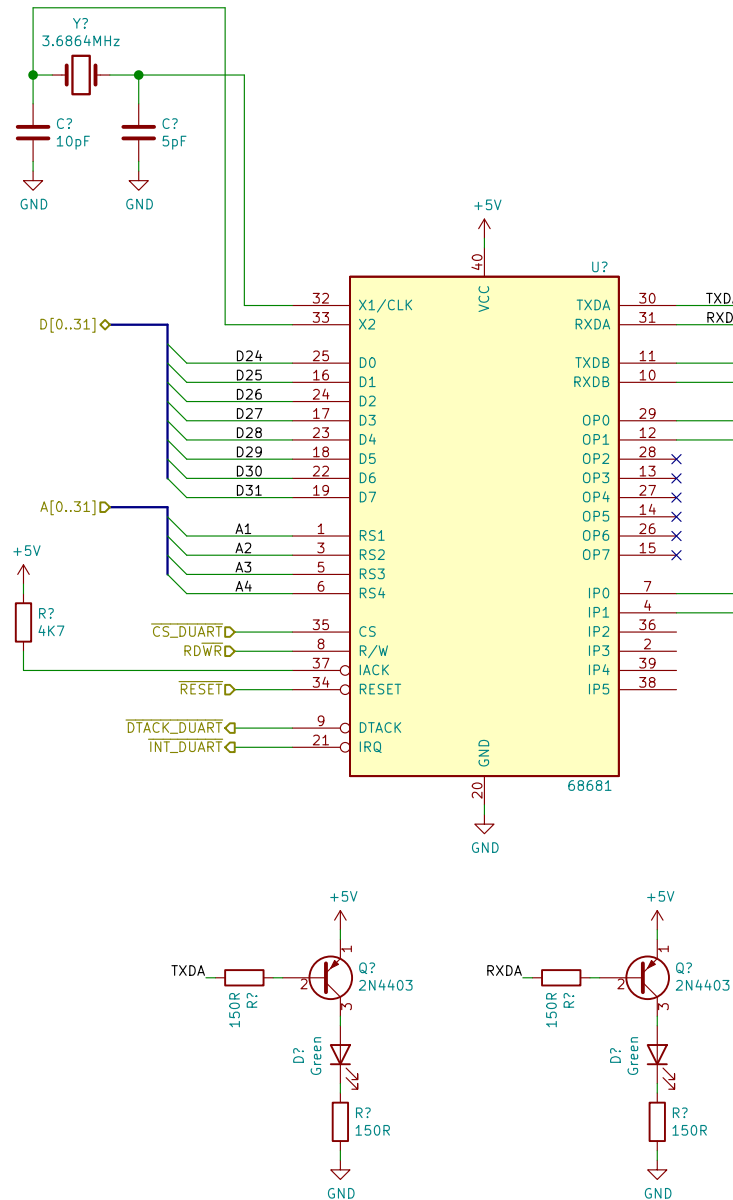




Sheet: /Decode and Logic/ File: Decode.sch	
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