

Sheet: CPU, RAM and ROM

A[0..15]
D[0..7]

CLOCK_ED
CS_ROM
CS_RAM

File: CPU.sch

Sheet: Decode Logic

RAMBANK[0..5]
ROMBANK[0..3]

A[0..15]
D[0..7]
CLK8M
RESET
UART_RD
UART_WR
UART_CS
UART_INT
VDP_RD
VDP_WR
VDP_INT

File: Decode.sch

Sheet: Serial Ports

A[0..15]
D[0..7]

RESET
UART_RD
UART_WR
UART_CS
UART_INT

File: Serial.sch

Sheet: Video

A[0..15]
D[0..7]

RESET
VDP_RD
VDP_WR
VDP_INT
WAIT

File: Video.sch

Sheet: Audio

A[0..15]
D[0..7]

RESET
YM_CS
YM_WR
YM_RD
YM_IRQ
YM_CLK

File: Audio.sch

Sheet: VideoOut

GREEN
RED
BLUE

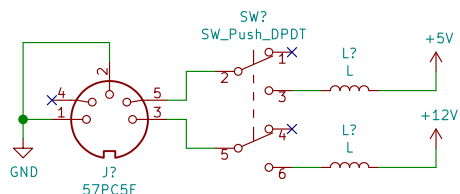
CSYNC

GREEN
RED
BLUE

CSYNC

AUDIO_L
AUDIO_R

File: VideoOut.sch



Sheet: /
File: yddraig09.sch

Title:

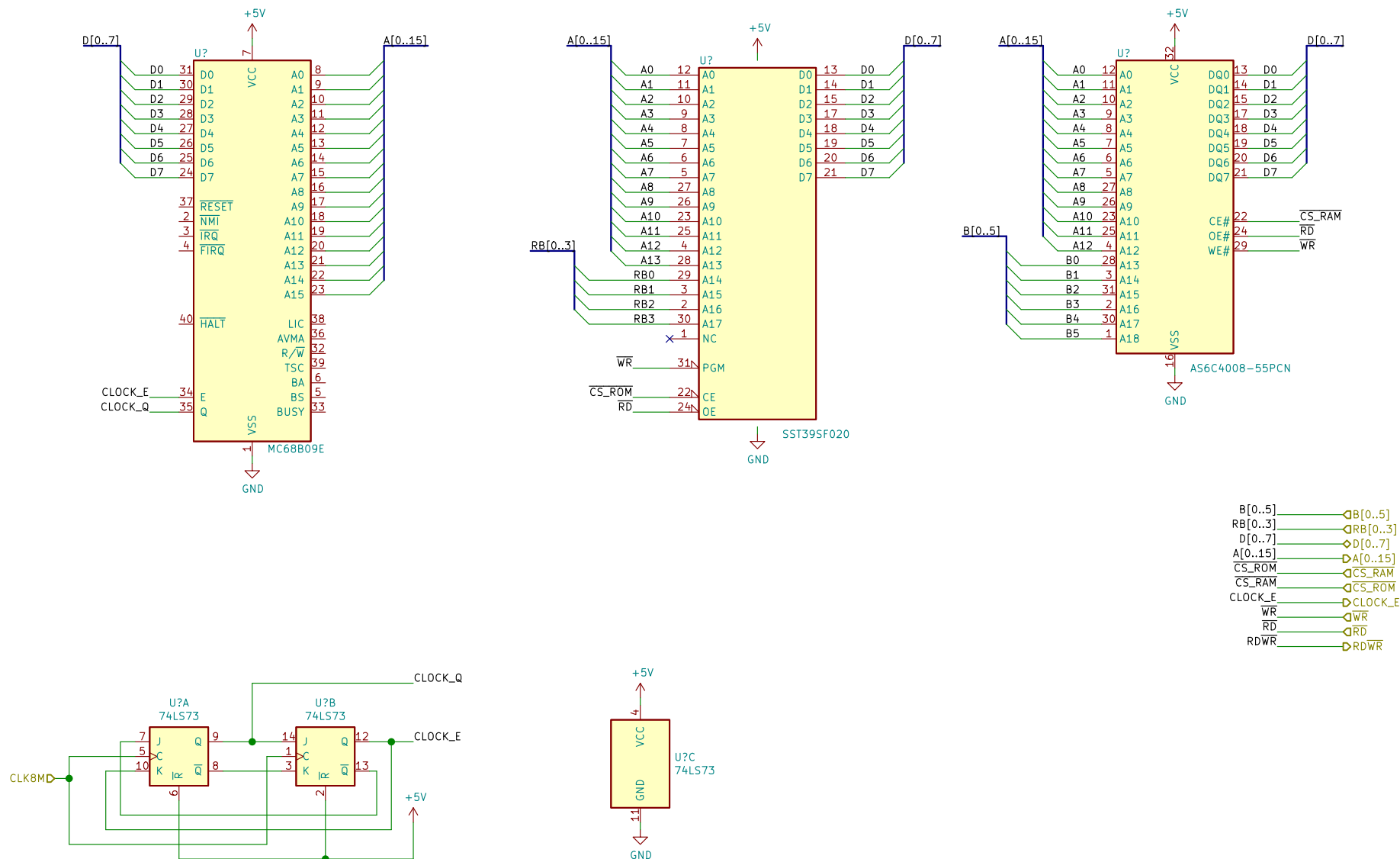
Size: A4

Date:

KiCad E.D.A. kicad (5.1.10)-1

Rev:

Id: 1/7



Sheet: /CPU, RAM and ROM/
File: CPU.sch

Title:

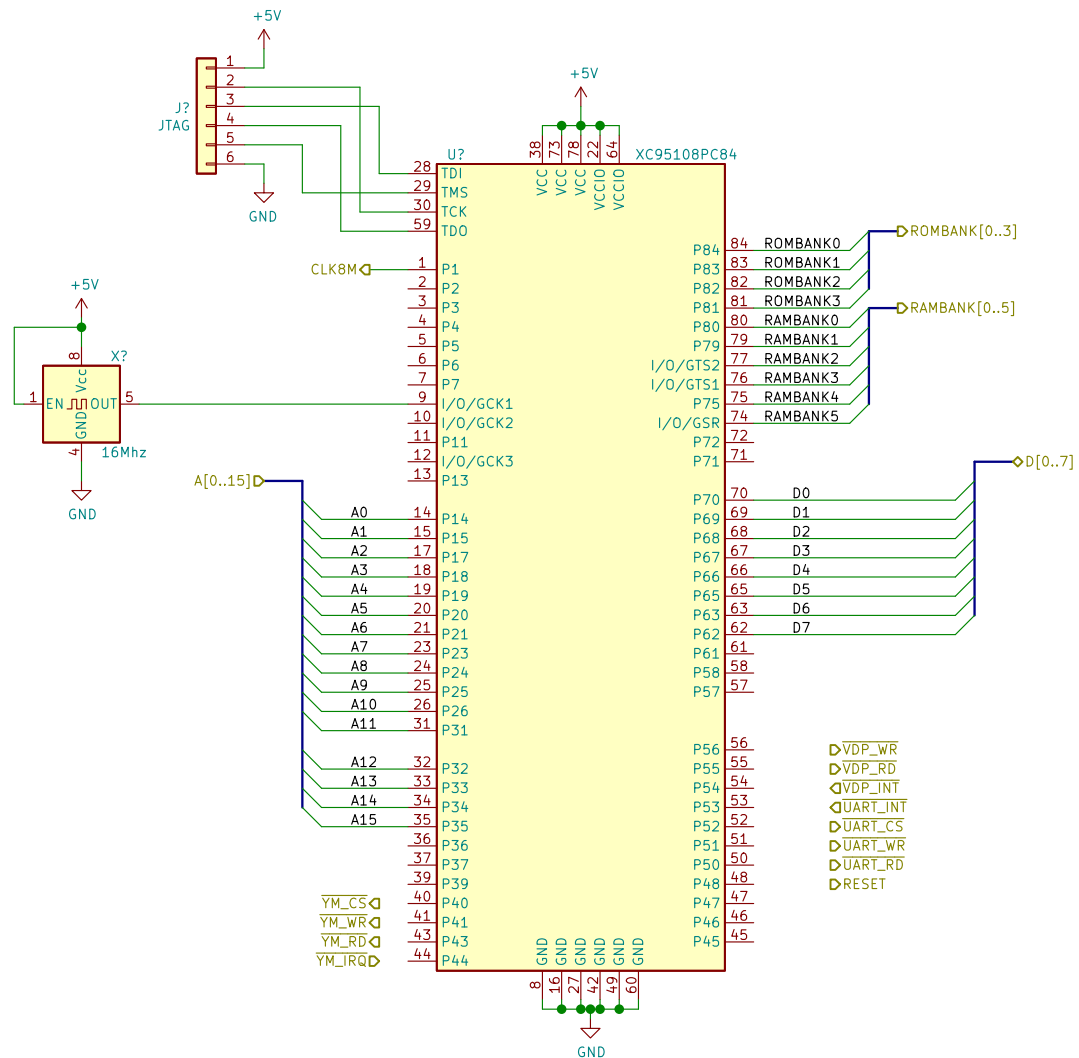
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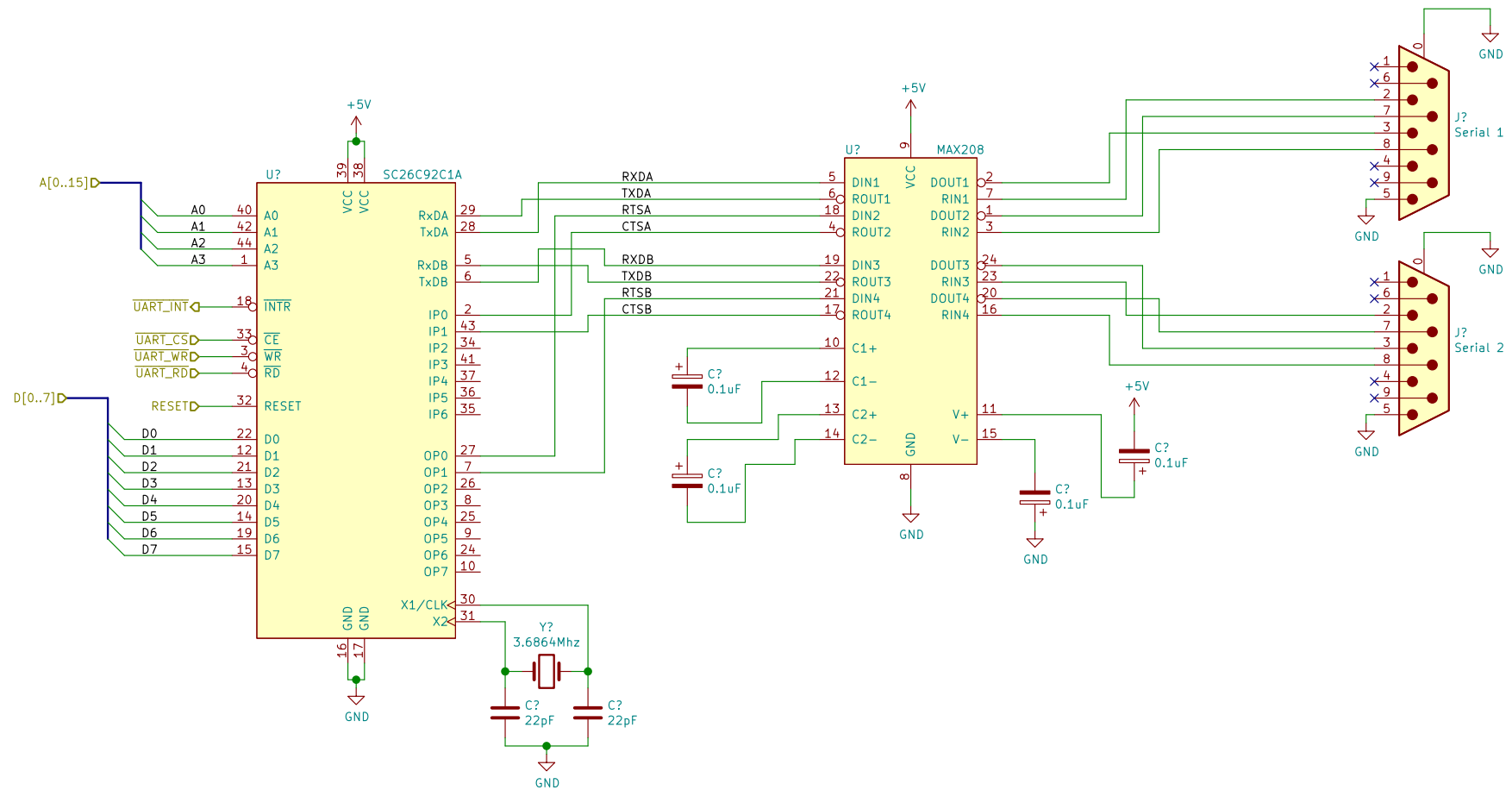
Date:

KiCad E.D.A. kicad (5.1.10)-1

Rev:

Id: 2/7





Sheet: /Serial Ports/
File: Serial.sch

Title:

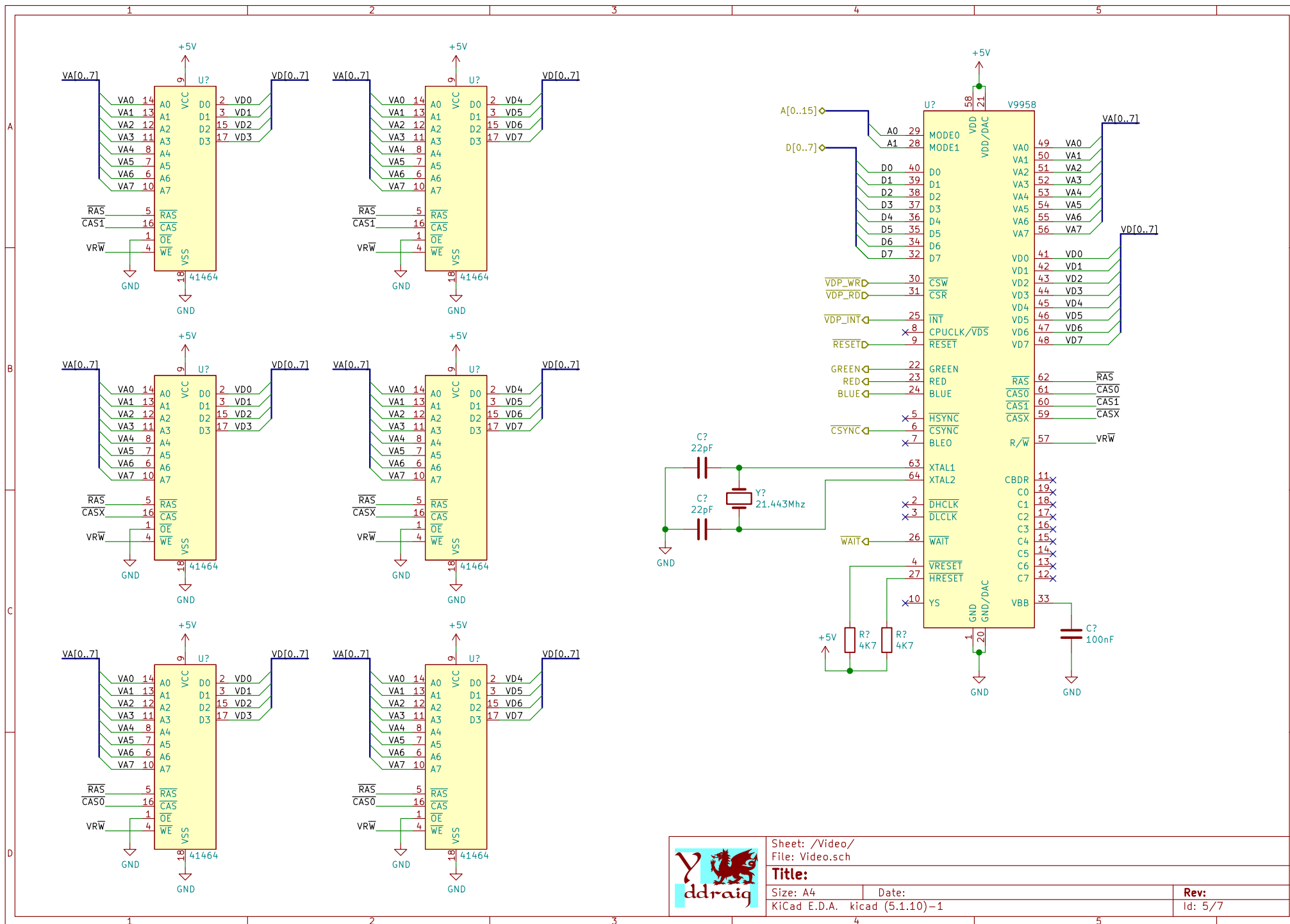
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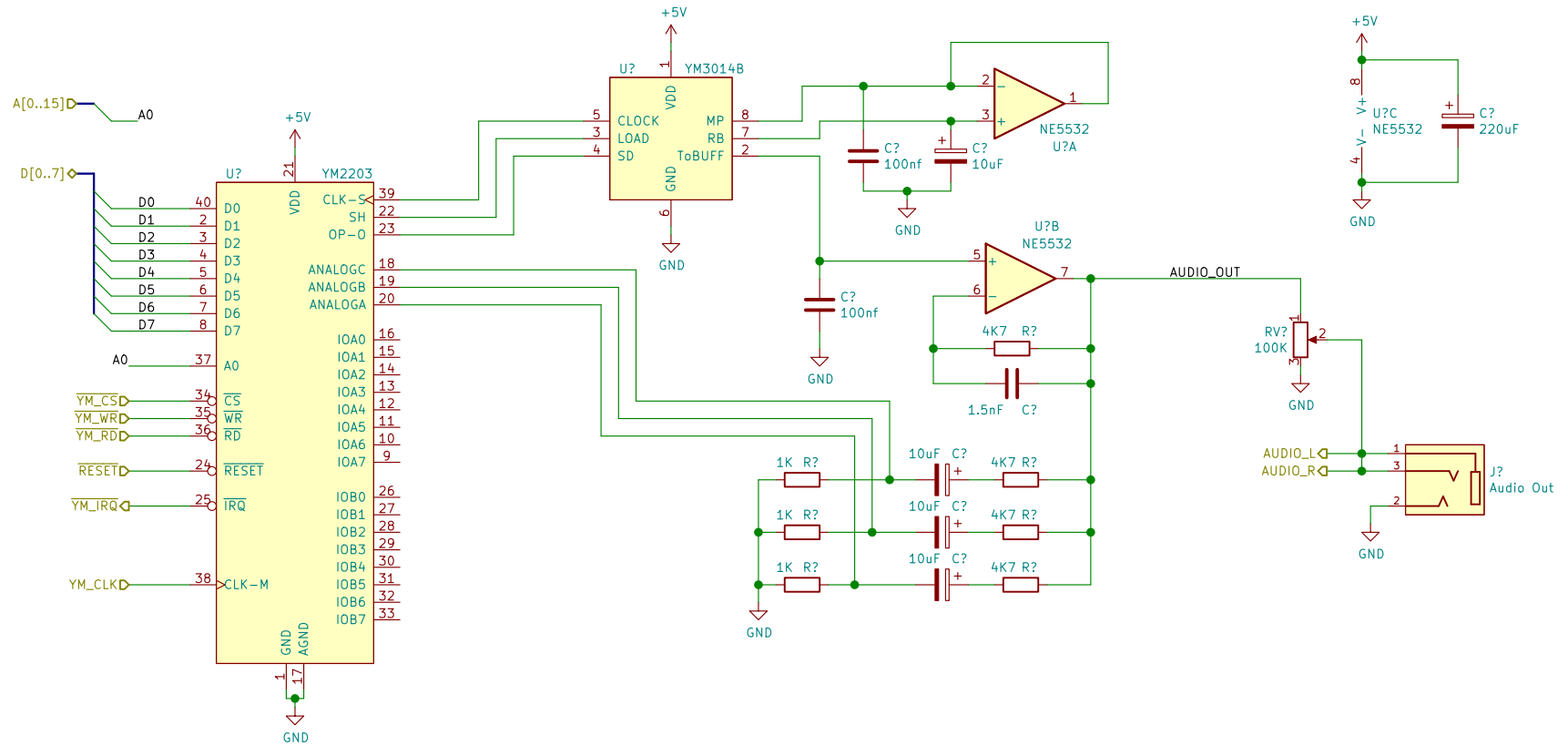
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Rev:

Id: 4/7





Sheet: /Audio/
File: Audio.sch

Title:

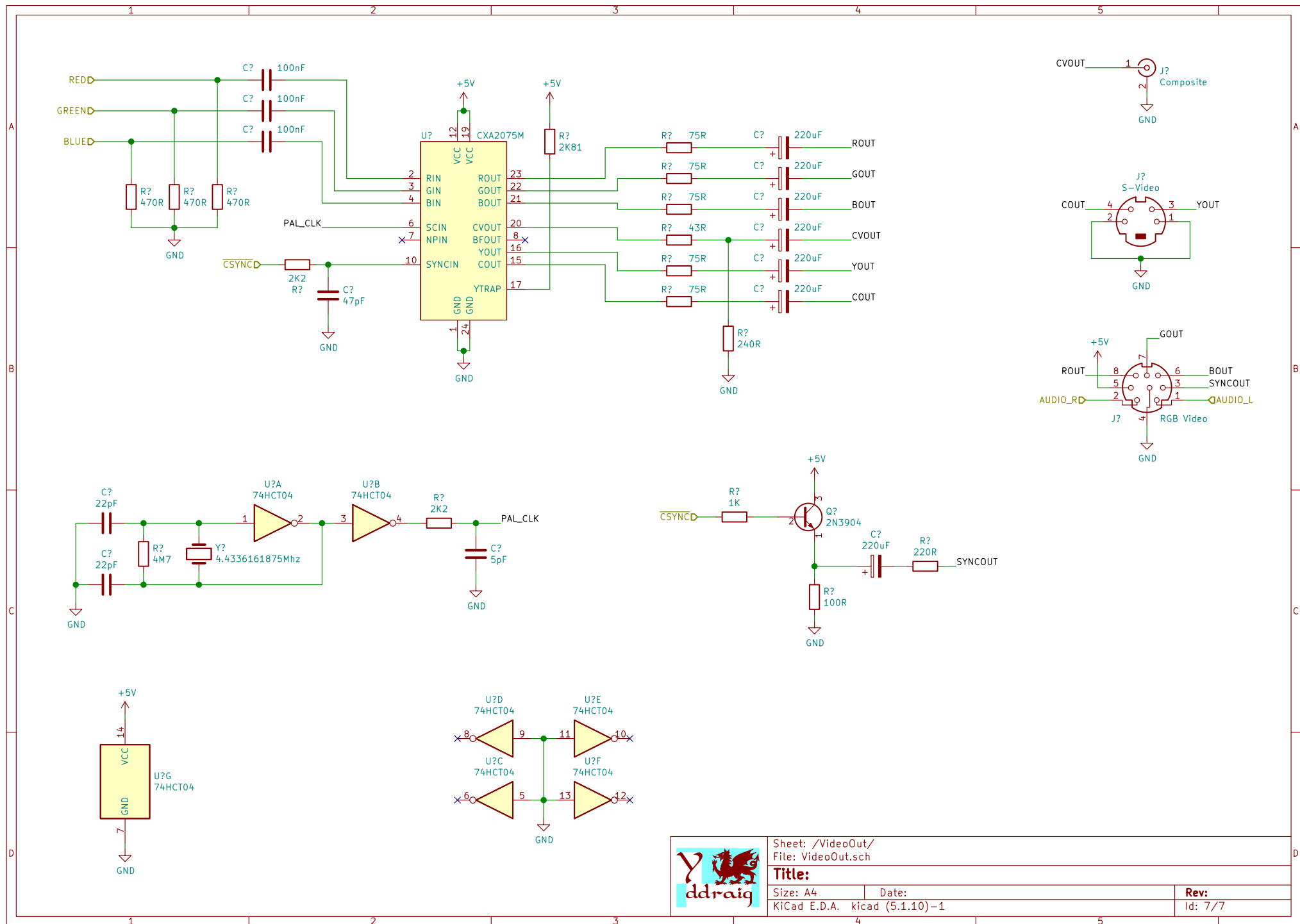
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Date:

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Rev:

Id: 6/7



		Sheet: /VideoOut/	
		File: VideoOut.sch	
Title:		Rev:	
Size: A4	Date:	Id: 7/7	
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