

Sheet: Reset and LEDs

RESET

HALT

RUN

RESET

File: Reset.sch

Sheet: CPU and FPU

D[0..31]

A[0..31]

FC[0..2]

IPL[0..2]

SIZ0

SIZ1

AS

DS

RW

DSACK0

DSACK1

BERR

RESET

CS_FPU

FPUSENSE

MMUDIS

CDIS

STERM

CBREQ

File: CPU.sch

Sheet: ROM and RAM

D[0..31]

A[0..31]

CS_SRAM

CS_ROM

RDWR

RDWR

File: ROMRAM.sch

Sheet: Serial

D[0..31]

A[0..31]

RESET

RDWR

CS_DUART

DTACK_DUART

INT_DUART

File: Serial.sch

Sheet: IDE

D[0..31]

A[0..31]

RESET

RDWR

RDWR

CS_IDE

INT_IDE

File: IDE Interface.sch

Sheet: Decode and Logic

D[0..31]

A[0..31]

RDWR

RDWR

LDSD

UDSD

ED

CLK100

CS_EXTREG[1..4]

CS_EXTDATA[1..4]

EXT_DBEND

EXT_DTACK

EXT_BERR

DSACK0_EXT

DSACK1_EXT

EXT_IRQ[1..4]

EXT_PRD0[1..4]

EXT_PRD1[1..4]

CS_DRAM

CS_DRAMCPLD

JTAG_TCK

JTAG_TMS

JTAG_TDI

JTAG_TDO

DRAM_DSACK0

DRAM_DSACK1

CS_KBDD

INT_KBD

CS_ETH

INT_ETH

CS_RTC

CS_USBD

INT_USBD

USB_ENABLED

USB_OVERCUR

File: Decode.sch

Sheet: Expansion Slots

D[0..31]

A[0..31]

RESET

RDWR

RDWR

LDSD

UDSD

ED

CLK10

CPUCCLK

VMA

CS_EXTREG[1..4]

CS_EXTDATA[1..4]

EXT_DBEN

EXT_DTACK

EXT_BERR

DSACK0_EXT

DSACK1_EXT

EXT_IRQ[1..4]

EXT_PRD0[1..4]

EXT_PRD1[1..4]

File: Expansion.sch

Sheet: DRAM

D[0..31]

A[0..31]

AS

DS

SIZ0

SIZ1

CBREQ

CS_DRAM

CS_DRAMCPLD

RESET

DRAM_CLK

JTAG_TCK

JTAG_TMS

JTAG_TDI

JTAG_TDO

DSACK0

DSACK1

STERM

File: DRAM.sch

Sheet: Keyboard

D[0..31]

A[0..31]

RESET

RDWR

RDWR

CS_KBD

INT_KBD

File: Keyboard.sch

Sheet: Ethernet

D[0..31]

A[0..31]

RESET

RDWR

RDWR

CS_ETH

INT_ETH

File: Ethernet.sch

Sheet: Real Time Clock

D[0..31]

A[0..31]

RESET

RDWR

RDWR

CS_RTC

File: RTC.sch

Sheet: USB

D[0..31]

A[0..31]

CS_USB

RDWR

RDWR

RESET

INT_USB

USB_ENABLE

USB_OVERCUR

File: USB.sch

Sheet: Power

File: Power.sch

Notes:

To be fixed...

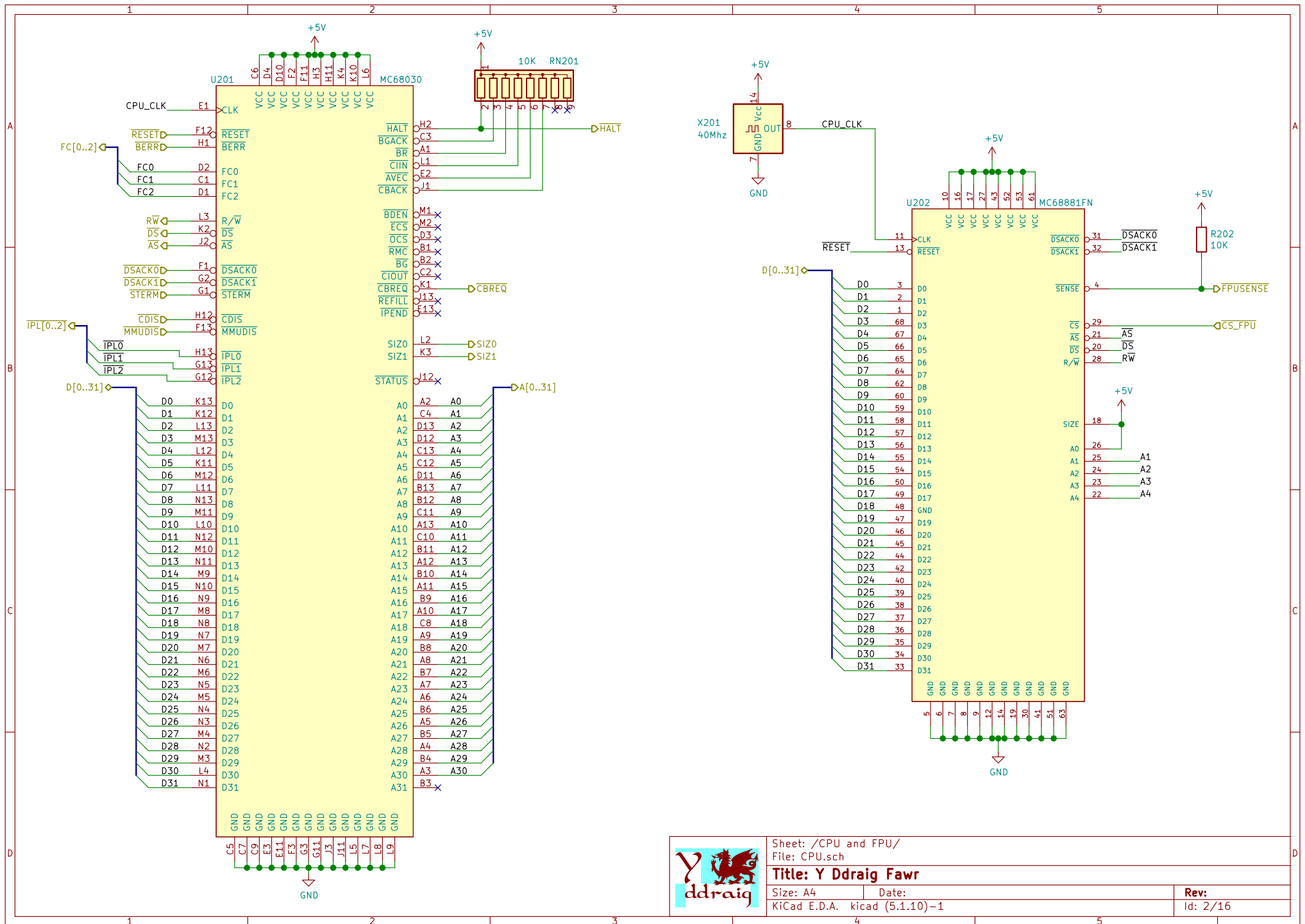
DSACKx from decode needs to be open collector

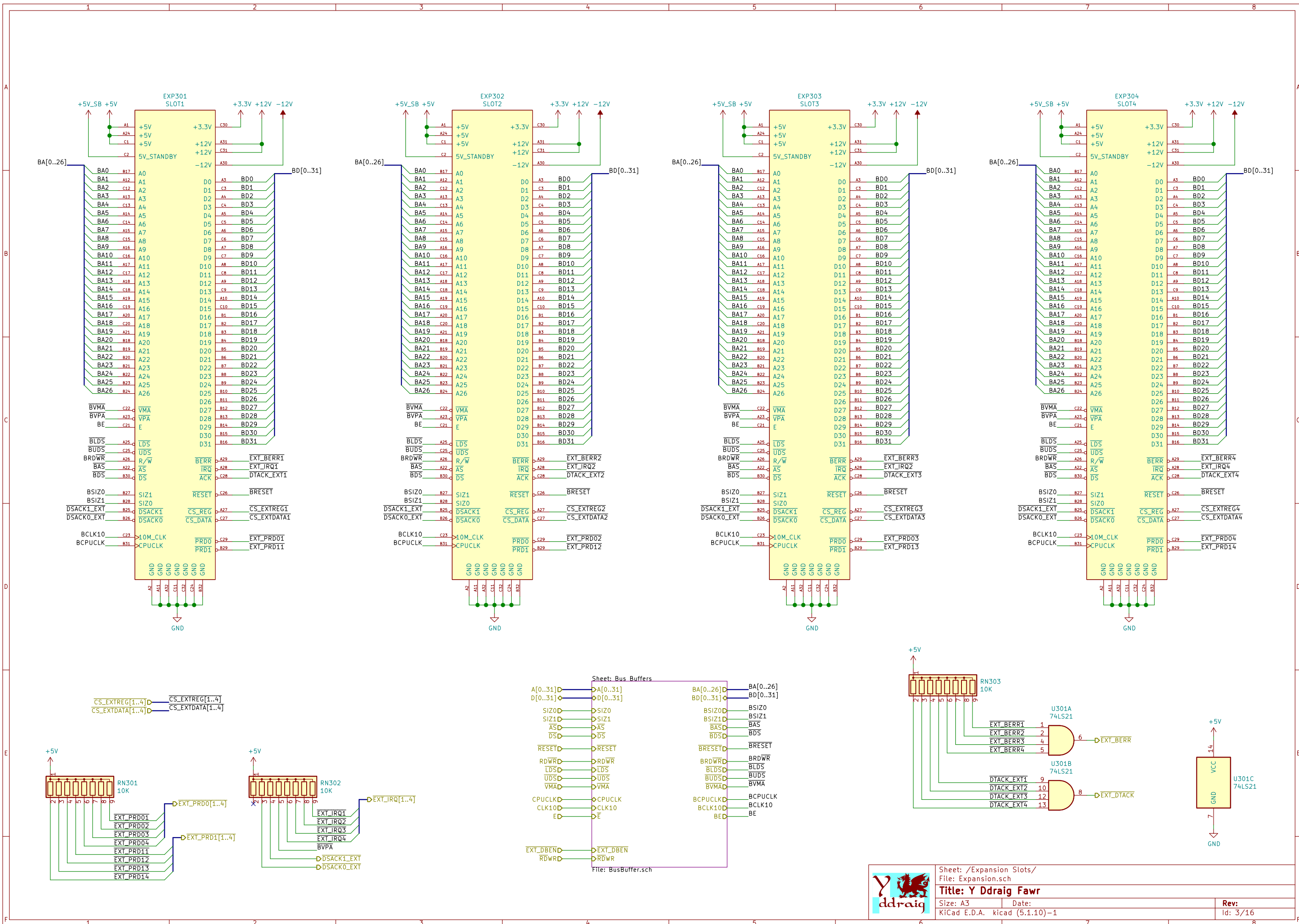
Pullups on CPU pins and DSACK

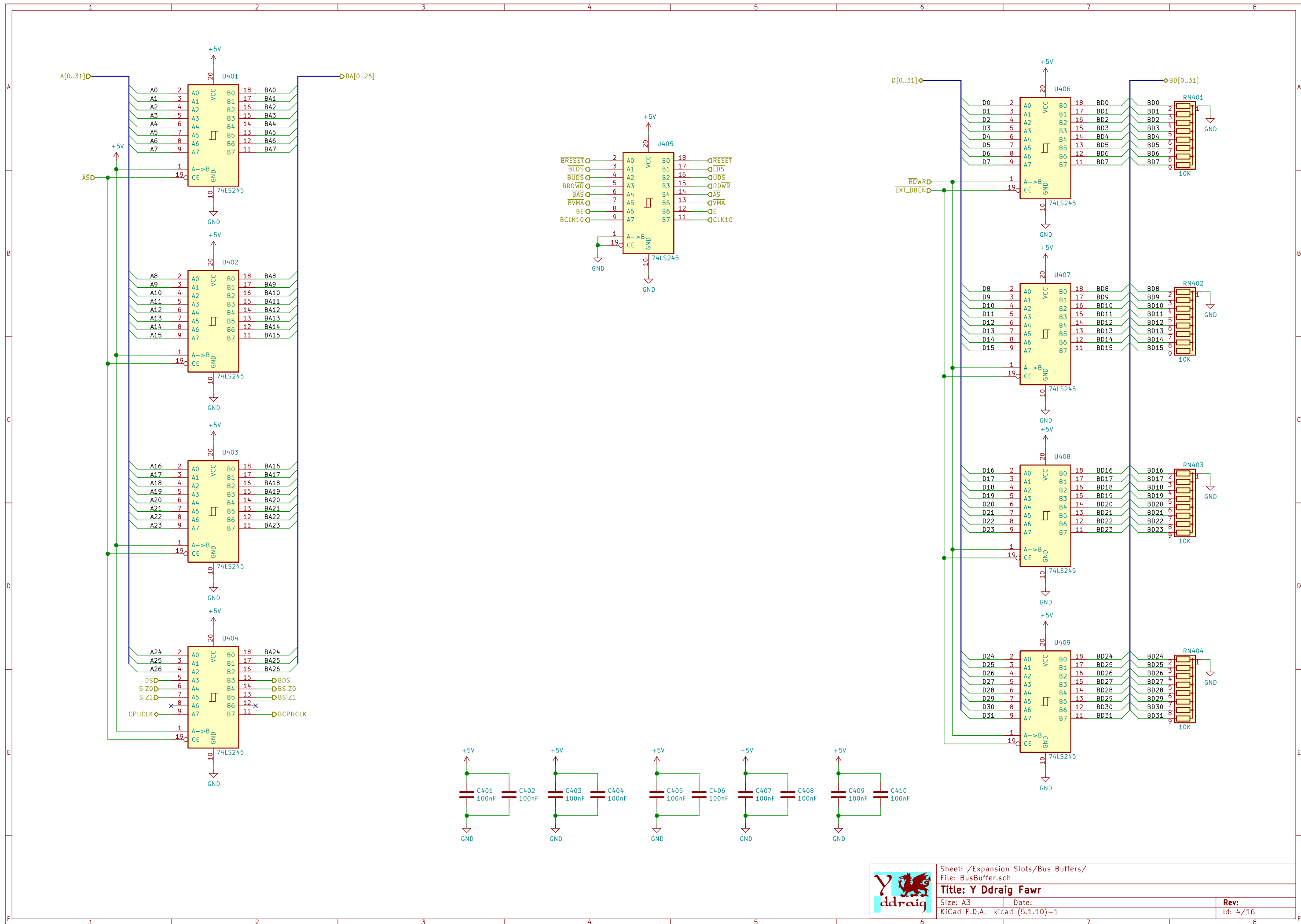
Check signals needed from CPU

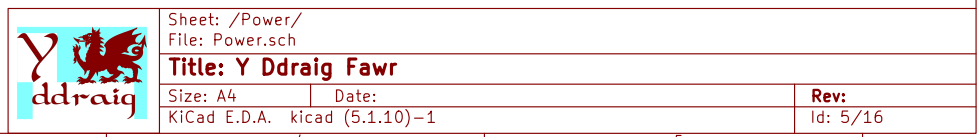
CPU clock and 10Mhz clock needed

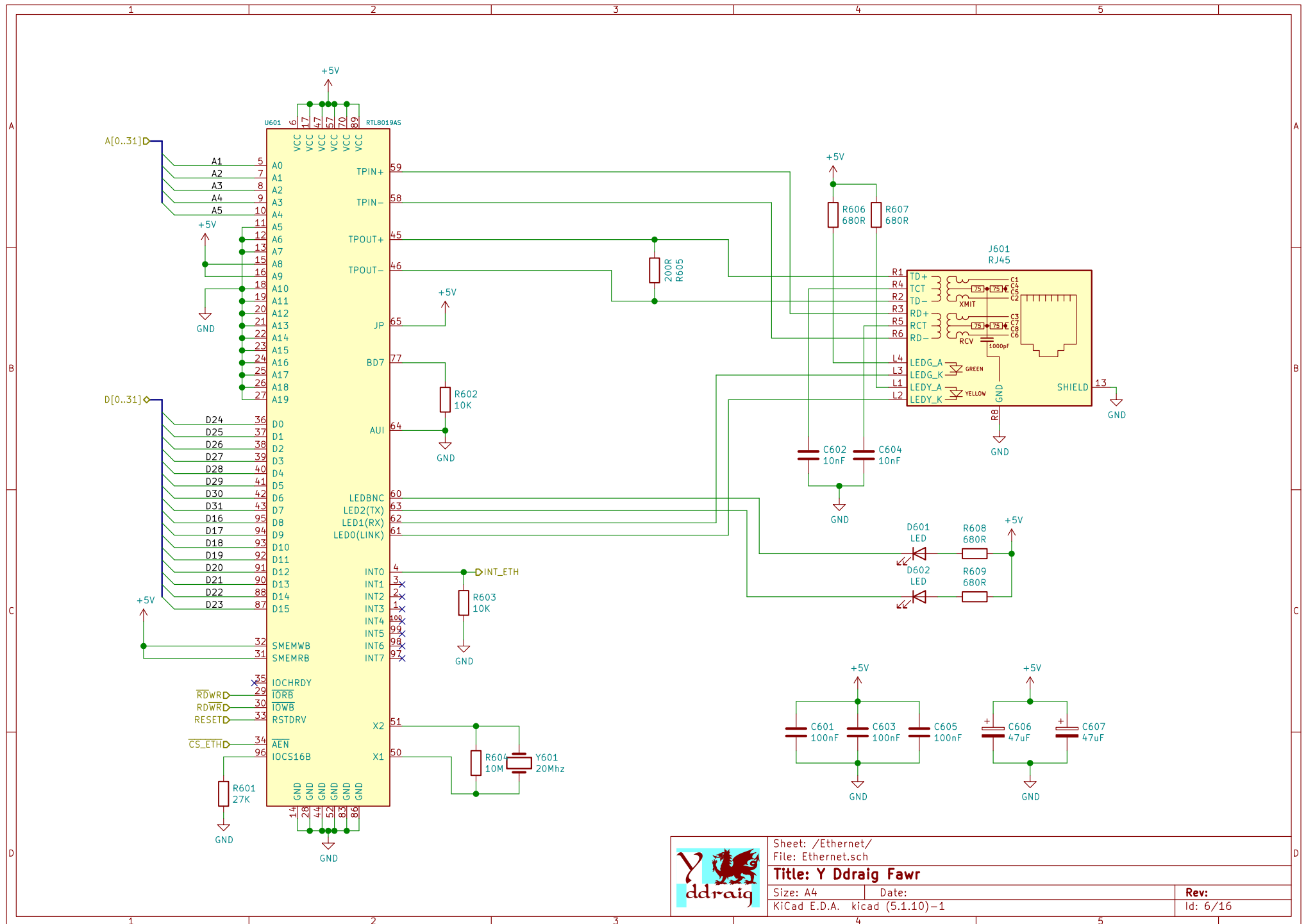




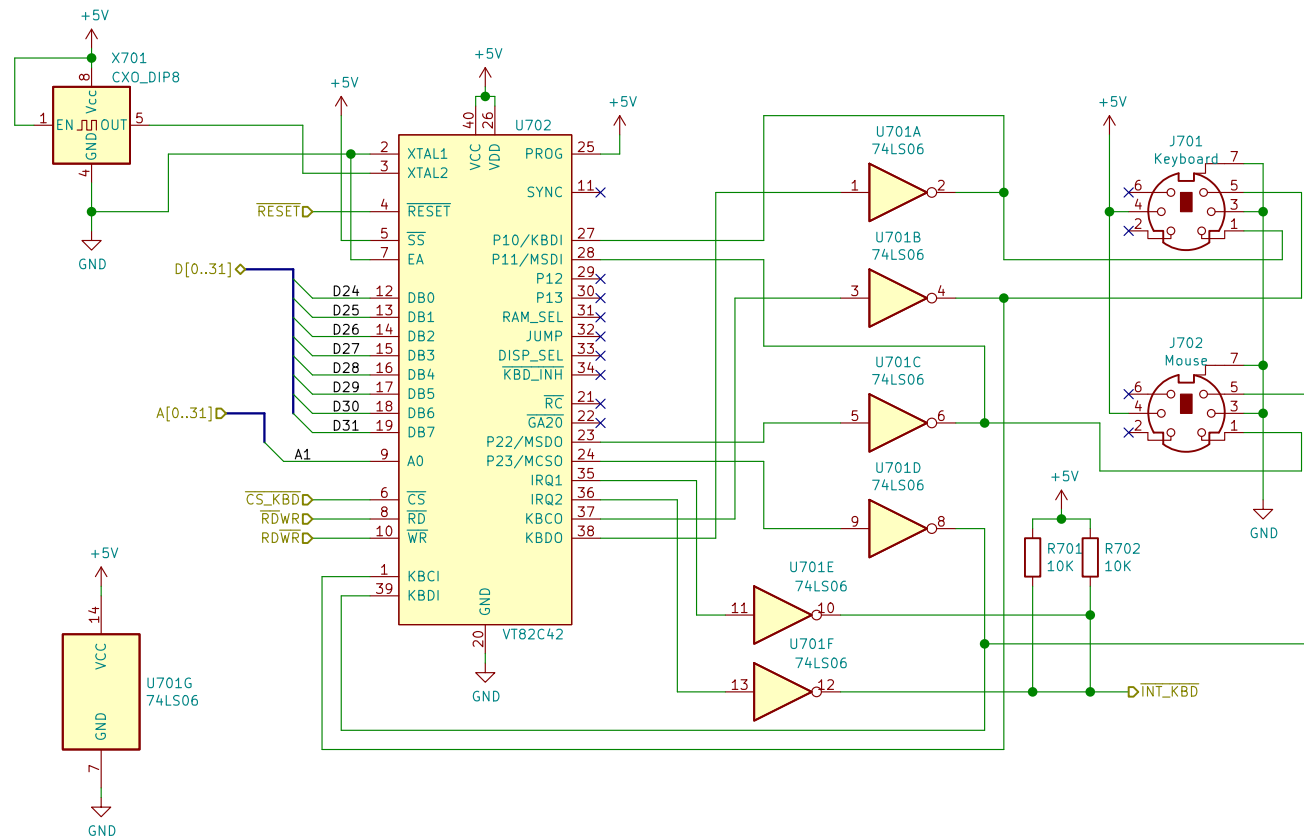








	Sheet: /Ethernet/		
	File: Ethernet.sch		
	Title: Y Ddraig Fawr		
	Size: A4	Date:	Rev:
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Sheet: /Keyboard/
File: Keyboard.sch

Title: Y Ddraig Fawr

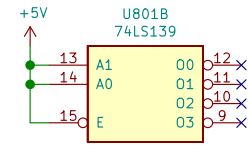
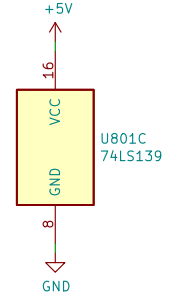
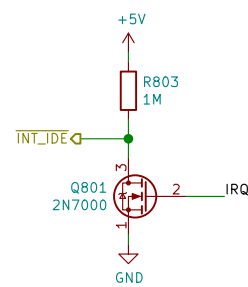
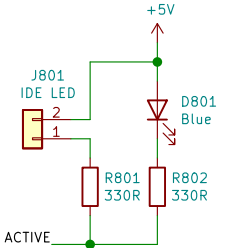
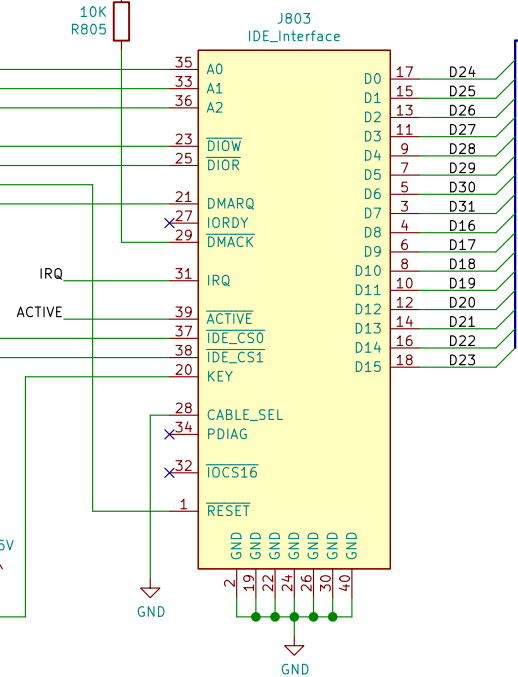
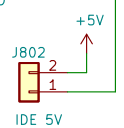
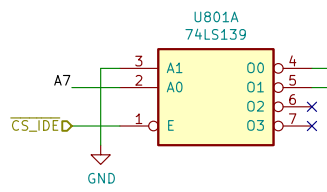
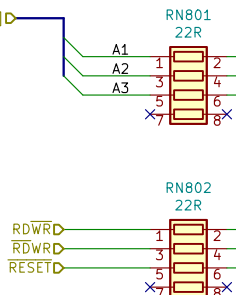
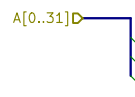
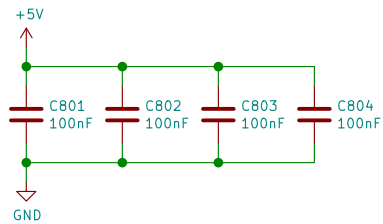
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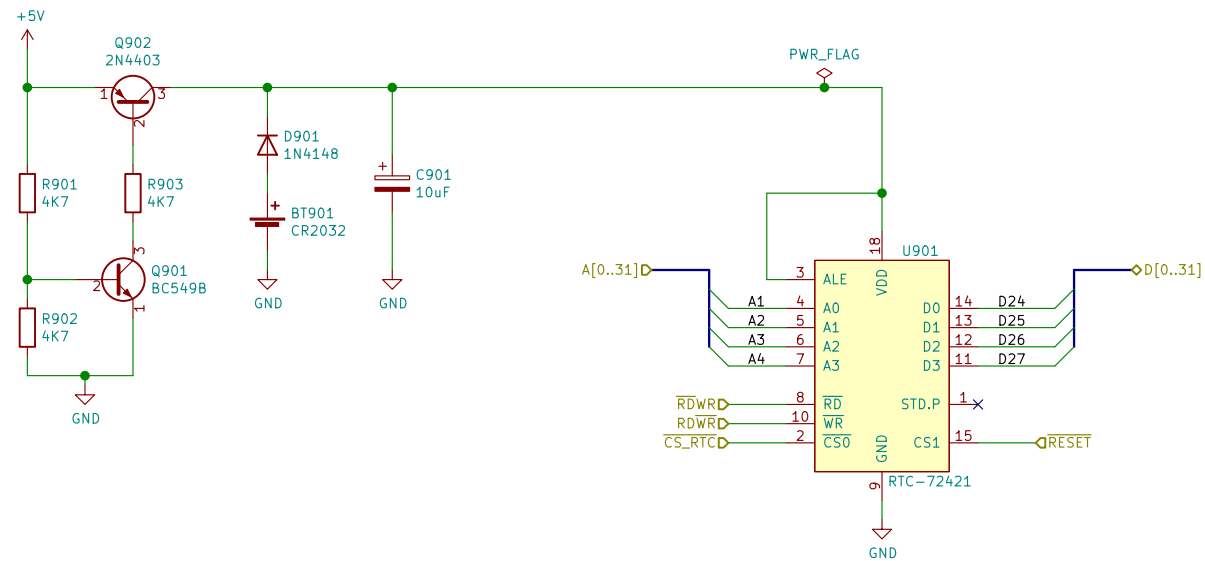
Date:

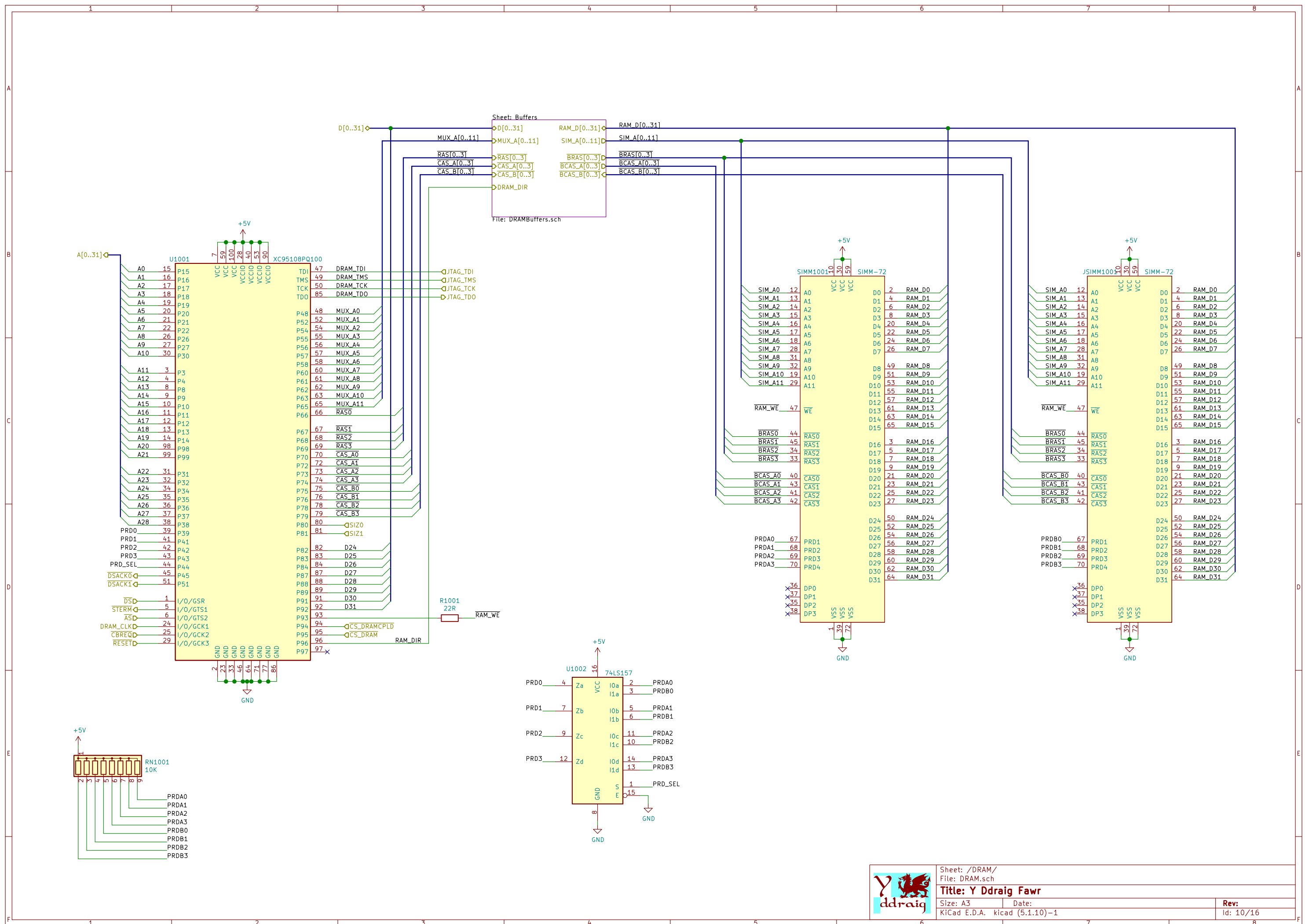
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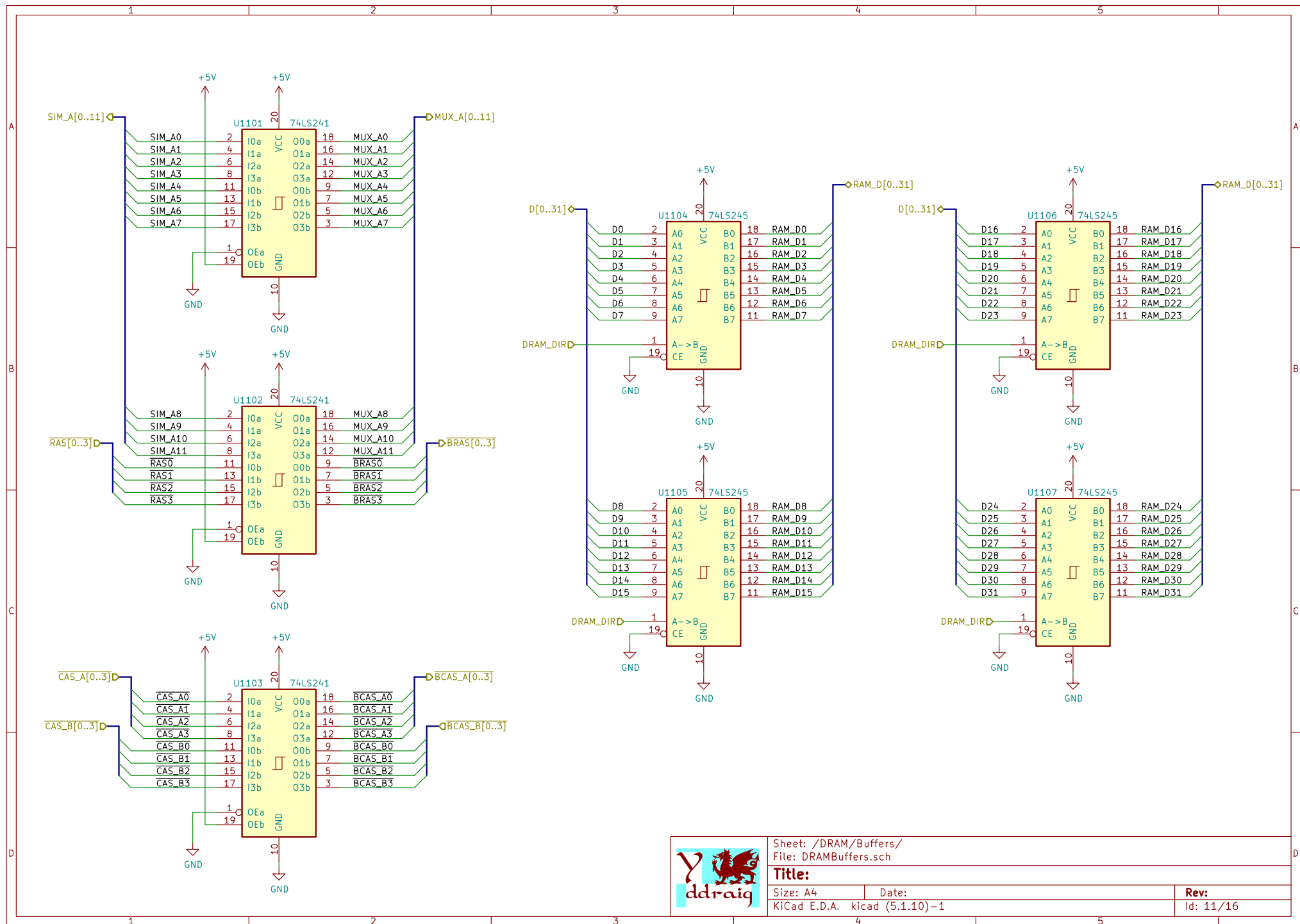
Rev:

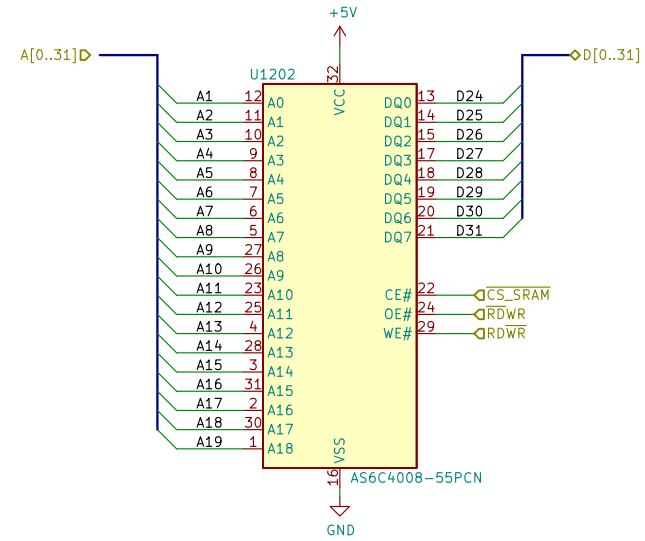
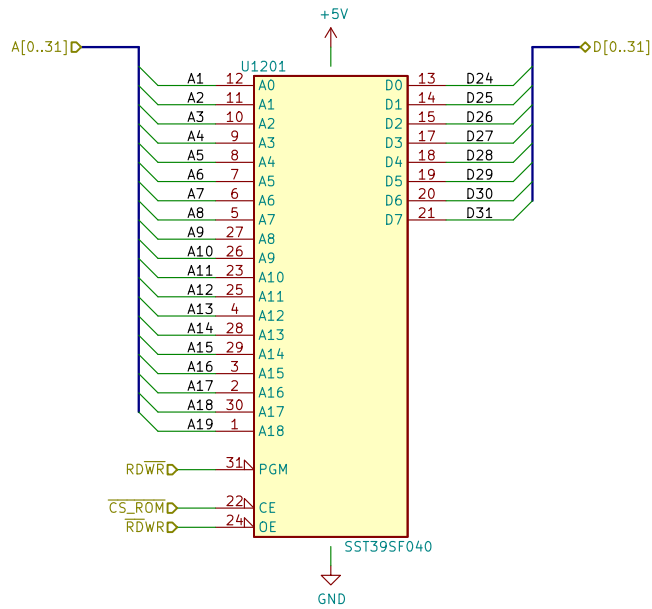
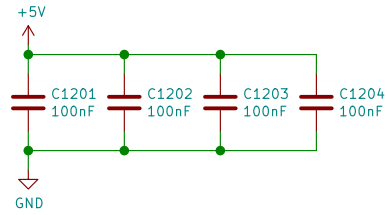
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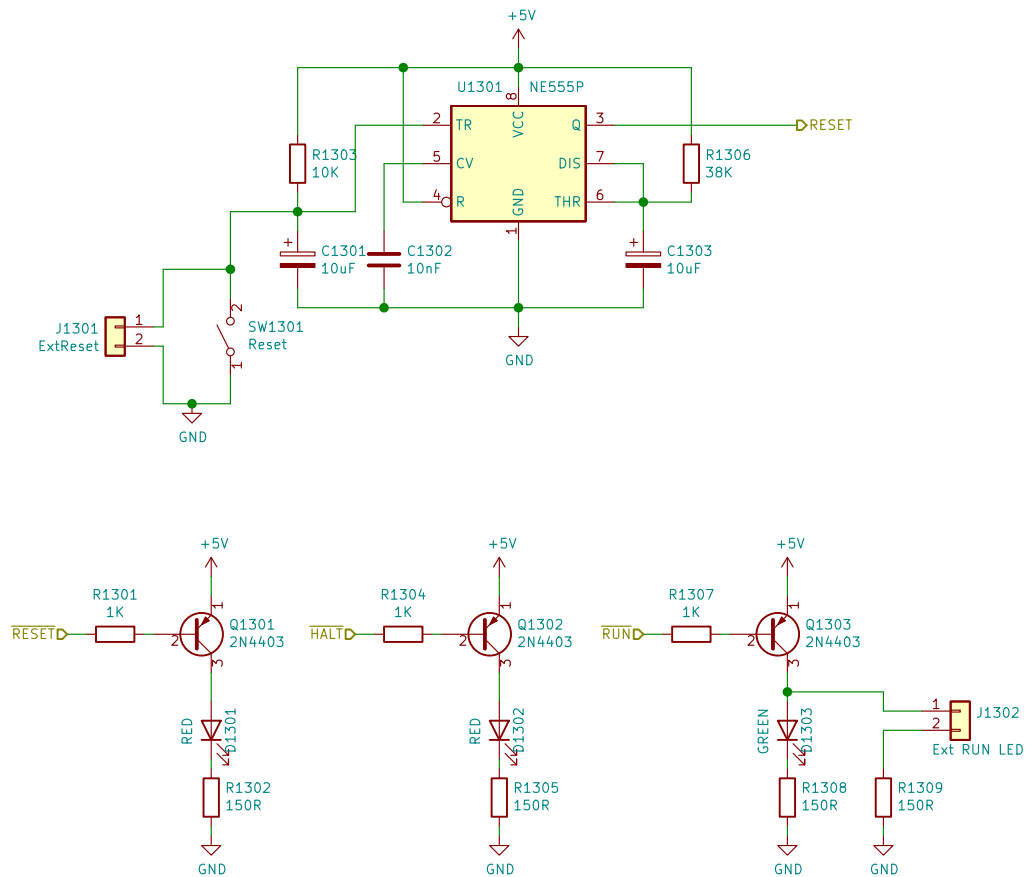





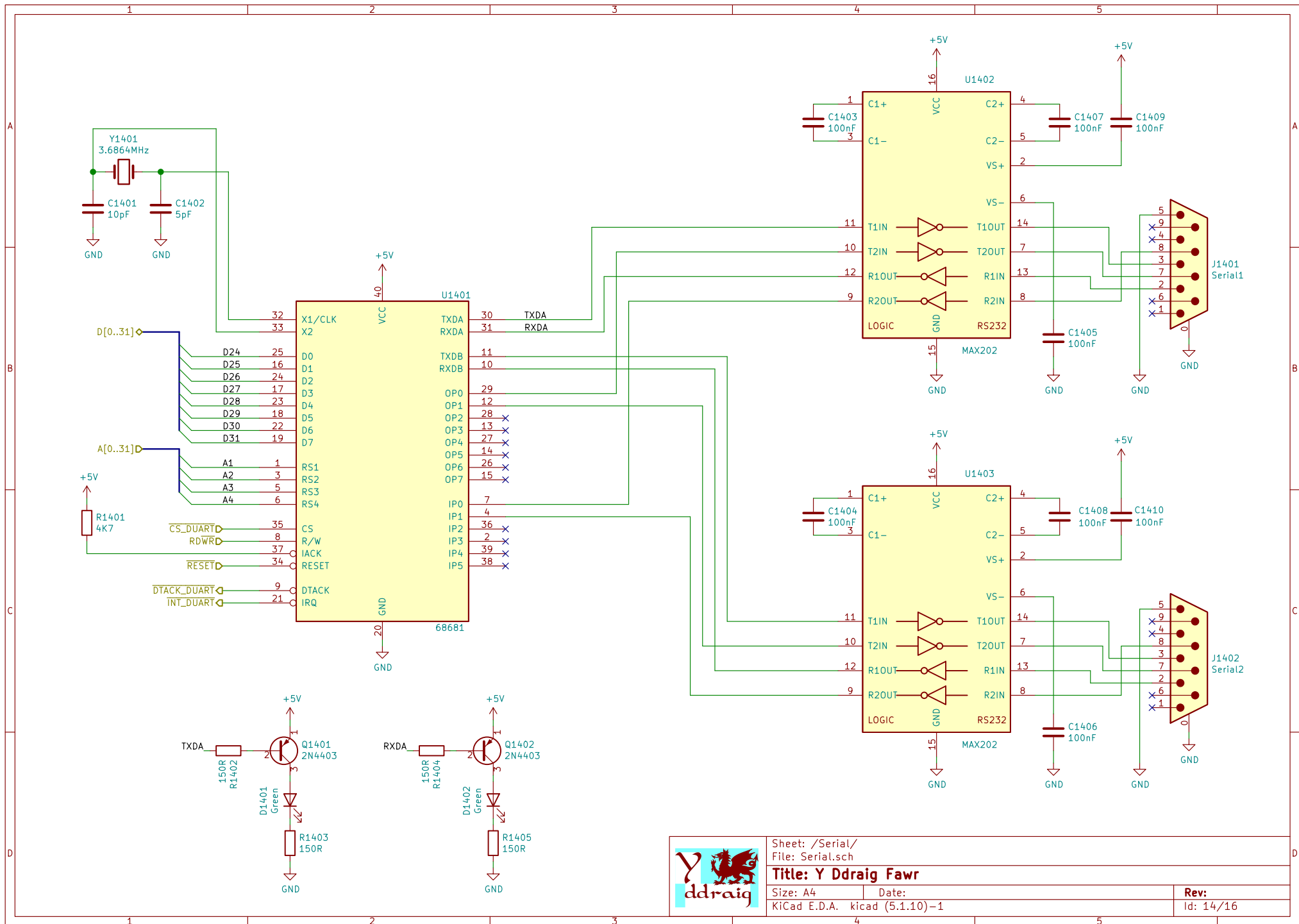








	Sheet: /Reset and LEDs/		
	File: Reset.sch		
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	Sheet: /Serial/ File: Serial.sch	
	Title: Y Ddraig Fawr	
Size: A4	Date:	Rev:
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