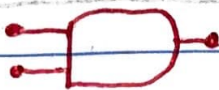






Practical 5

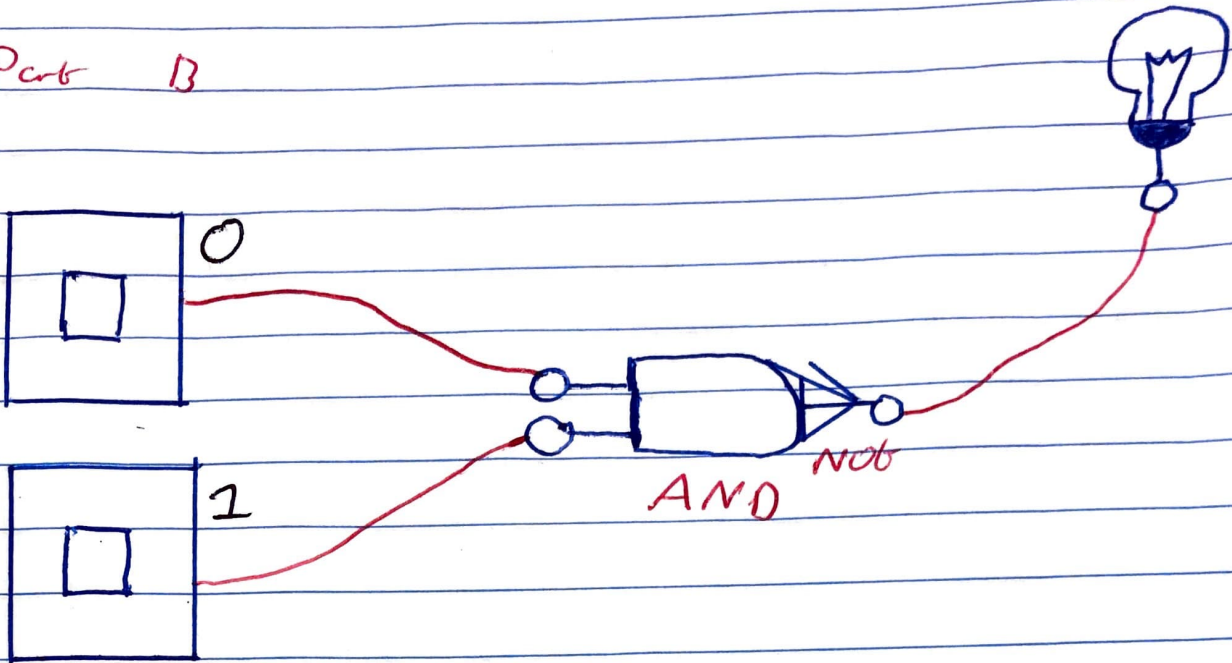
Part A

1.

Inputs		Output(s) Truth Table				
A	B	AND	OR	XOR	NAND	NOR
0	0	0	0	0	1	1
0	1	0	1	1	1	1
1	0	0	1	1	0	0
1	1	1	1	0	0	0
Gate Symbol						
Functional Notation		\wedge	\vee	\oplus	\uparrow	\downarrow

Part B

1.



b

