

取指令阶段

P0: $PC \rightarrow MAR, MAR \rightarrow BUS$
P1: Read $MEM, BUS \rightarrow MDR$
P2: $MDR \rightarrow IR, PC + 1 \rightarrow PC$

指令译码和取操作数阶段

P0: $Opcode(IR) \rightarrow CU$
 $DR(IR) \rightarrow Reg_addr1$
 $SR(IR) \rightarrow Reg_addr2$
P1: $CU_ena, reag_reg$
P2: Send data to ALU

执行回写阶段

P0: $ALU_ena \rightarrow ALU, Add$
P1: $ALU_out \rightarrow REG$
 $flag_out \rightarrow FLAG$
P2: $write_Reg$