

```
risc_cpu
MyCPU(.clkkin(clkin), .reset(reset), .addr(addr), .indata(indata), .outdata(out
data),
.wr_m(wr), .rd_m(rd), .hlt(hlt), .io(io), .sw(sw), .clkcon(clkcon), .run(run),
.port(port), .hand(hand),

.pc(counter), .ir(instr), .cu_ena(cu_ena), .fetch(fetch), .pc_ena(pc_ena), .cl
k(clock4),
.mar_ena(mar_ena), .mdr_ena(mdr_ena), .ir_ena(ir_ena), .state(state));

addr_decode DECODER(.addr(addr), .rom_sel(rom_sel), .ram_sel(ram_sel));

rom ROM(.data(indata), .addr(addr), .read(rd), .ena(rom_sel));

ram
RAM(.indata(outdata), .outdata(indata), .addr(addr), .ena(ram_sel), .read(rd),
.write(wr));

display_control
DISCON(.clk(clkin), .dis(dis), .seg_sel(an), .pc(counter), .ir(instr));

display DISPLAY(.dis(dis), .seg(seg));

integralaudio
IAUD(.clk(clkin), .portin(port[4:0]), .AUD_PWM(AUD_PWM), .AUD_SD(AUD_SD), .sw(
sw));
```