

取指令阶段

P0: $PC \rightarrow MAR, MAR \rightarrow BUS$
P1: Read $MEM, BUS \rightarrow MDR$
P2: $MDR \rightarrow IR, PC + 1 \rightarrow PC$

指令译码和取操作数阶段

P0: $Opcode(IR) \rightarrow CU$
 $DR(IR) \rightarrow Reg_addr1$
 $SR(IR) \rightarrow Reg_addr2$
P1: CU_ena , Read Reg
P2: $data \rightarrow MDR, addr \rightarrow MAR$

执行回写阶段

P0: $MAR \rightarrow BUS, MDR \rightarrow BUS$
P1: Write MEM
P2: Wait