Micriμm

Empowering Embedded Systems

μC/OS-II
μC/Probe

and the

STMicroelectronics STM32F103ZE

(Using the IAR STM32F103ZE-SK Kickstart Kit)

Application Note

AN-1913

www.Micrium.com

About Micriµm

Micriµm provides high-quality embedded software components in the industry by way of engineer-friendly source code, unsurpassed documentation, and customer support. The company's world-renowned real-time operating system, the Micriµm **µC/OS-II**, features the highest-quality source code available for today's embedded market. Micriµm delivers to the embedded marketplace a full portfolio of embedded software components that complement **µC/OS-II**. A TCP/IP stack, USB stack, CAN stack, File System (FS), Graphical User Interface (GUI), as well as many other high quality embedded components. Micriµm's products consistently shorten time-to-market throughout all product development cycles. For additional information on Micriµm, please visit www.micrium.com.

About µC/OS-II

µC/OS-II is a preemptive, real-time, multitasking kernel. **µC/OS-II** has been ported to over 45 different CPU architectures.

µC/OS-II is small yet provides all the services you'd expect from an RTOS: task management, time and timer management, semaphore and mutex, message mailboxes and queues, event flags an much more.

You will find that **µC/OS-II** delivers on all your expectations and you will be pleased by its ease of use.

Licensing

µC/OS-II is provided in source form for **FREE** evaluation, for educational use or for peaceful research. If you plan on using **µC/OS-II** in a commercial product you need to contact Micriµm to properly license its use in your product. We provide ALL the source code with this application note for your convenience and to help you experience **µC/OS-II**. The fact that the source is provided **DOES NOT** mean that you can use it without paying a licensing fee. Please help us continue to provide the Embedded community with the finest software available. Your honesty is greatly appreciated.

About µC/Probe Demo Version

µC/Probe is a Windows application that allows a user to display and change the value (at run-time) of virtually any variable or memory location on a connected embedded target. The user simply populates **µC/Probe**'s graphical environment with gauges, tables, graphs, and other components, and associates each of these with a variable or memory location. Once the application is loaded onto the target, the user can begin **µC/Probe**'s data collection, which will update the screen with variable values fetched from the target.

µC/Probe retrieves the values of global variables from a connected embedded target and displays the values in an engineer-friendly format. The supported data-types are: booleans, integers, floats and ASCII strings.

µC/Probe can have any number of 'data screens' where these variables are displayed. This allows to logically grouping different 'views' into a product.

This **µC/Probe** demo version can only retrieve information from RS-232C or J-LINK interfaces and is limited up to 15 symbols.

The demo version of **µC/Probe** is available on the Micriµm website:

http://www.micrium.com/products/probe/probe.html

About µC/Probe Full Version

The full version of **µC/Probe** allows you to use a TCP/IP is a Windows application that allows a user to display and change the value (at run-time) of virtually any variable or memory location on a connected embedded target. The user simply populates **µC/Probe**'s graphical environment with gauges, tables, graphs, and other components, and associates each of these with a variable or memory location. Once the application is loaded onto the target, the user can begin **µC/Probe**'s data collection, which will update the screen with variable values fetched from the target.

Manual Version

If you find any errors in this document, please inform us and we will make the appropriate corrections for future releases.

Version	Date	By	Description
V 1.00	2008/09/30	FT	Initial version.

Software Versions

This document may or may not have been downloaded as part of an executable file, *uCOSII-ST-STM32F103ZE-SK.exe* containing the code and projects described here. If so, then the versions of the Micriμm software modules in the table below would be included. In either case, the software port described in this document uses the module versions in the table below

Module	Version	Comment
μC/OS-II	V2.86	
μC/Probe	V2.20	

Document Conventions

Numbers and Number Bases

- Hexadecimal numbers are preceded by the "0x" prefix and displayed in a monospaced font. Example: `0xFF886633`.
- Binary numbers are followed by the suffix "b"; for longer numbers, groups of four digits are separated with a space. These are also displayed in a monospaced font. Example: `0101 1010 0011 1100b`.
- Other numbers in the document are decimal. These are displayed in the proportional font prevailing where the number is used.

Typographical Conventions

- Hexadecimal and binary numbers are displayed in a monospaced font.
- Code excerpts, variable names, and function names are displayed in a monospaced font. Functions names are always followed by empty parentheses (e.g., `OS_Start()`). Array names are always followed by empty square brackets (e.g., `BSP_Vector_Array[]`).
- File and directory names are always displayed in an italicized serif font. Example: */Micrium/Sofware/uCOS-II/Source/*.
- A bold style may be layered on any of the preceding conventions—or in ordinary text—to more strongly emphasize a particular detail.
- Any other text is displayed in a sans-serif font.

Table of Contents

1. Introduction

This document, ***AN-1913***, explains example code for using **μC/OS-II** and **μC/Probe** with the STMicroelectronics STM32F103ZE (Cortex M3) processor on the IAR STM32F103ZE-SK kickstart kit shown in Figure 1.

The ST STM32F103ZE includes a 512-KB flash and 64-kB SRAM, and operates at clock speeds as high as 72-MHz. Peripherals for several communications busses are provided on-chip, including UARTs, SPI, SDIO, CAN and USB. Three 21-channels 12 bits ADC, four general-purpose timers and up to 122 GPIOs round out the features on the chip.

The IAR STM32F103ZE has two RS-232 ports, two USB device port, a SD/MMC card slot a 20-pin JTAG for debugging and loading the processor. The interface components include a Nokia color LCD display, two push buttons, 4 status LEDs and potentiometer and 2 push buttons.

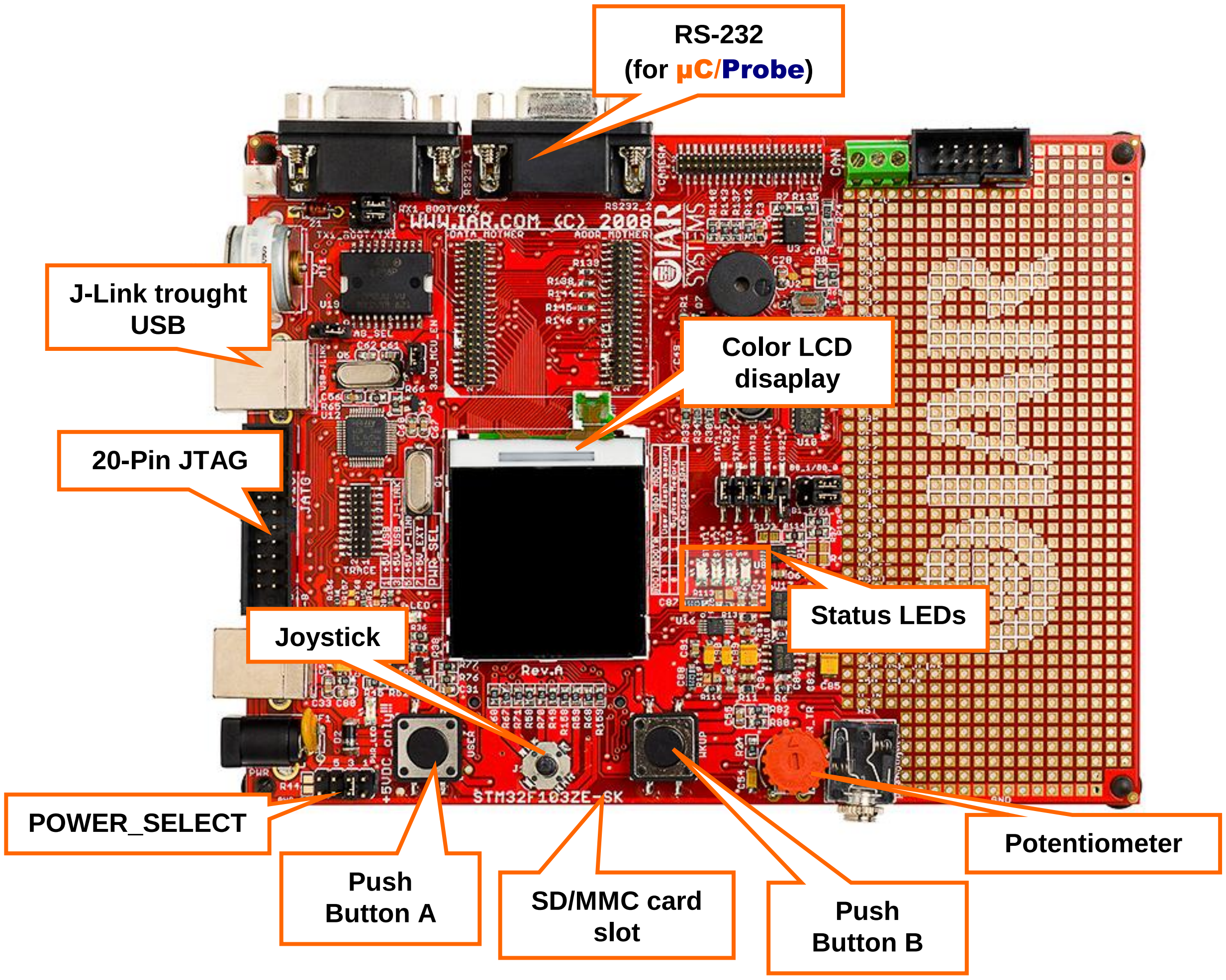
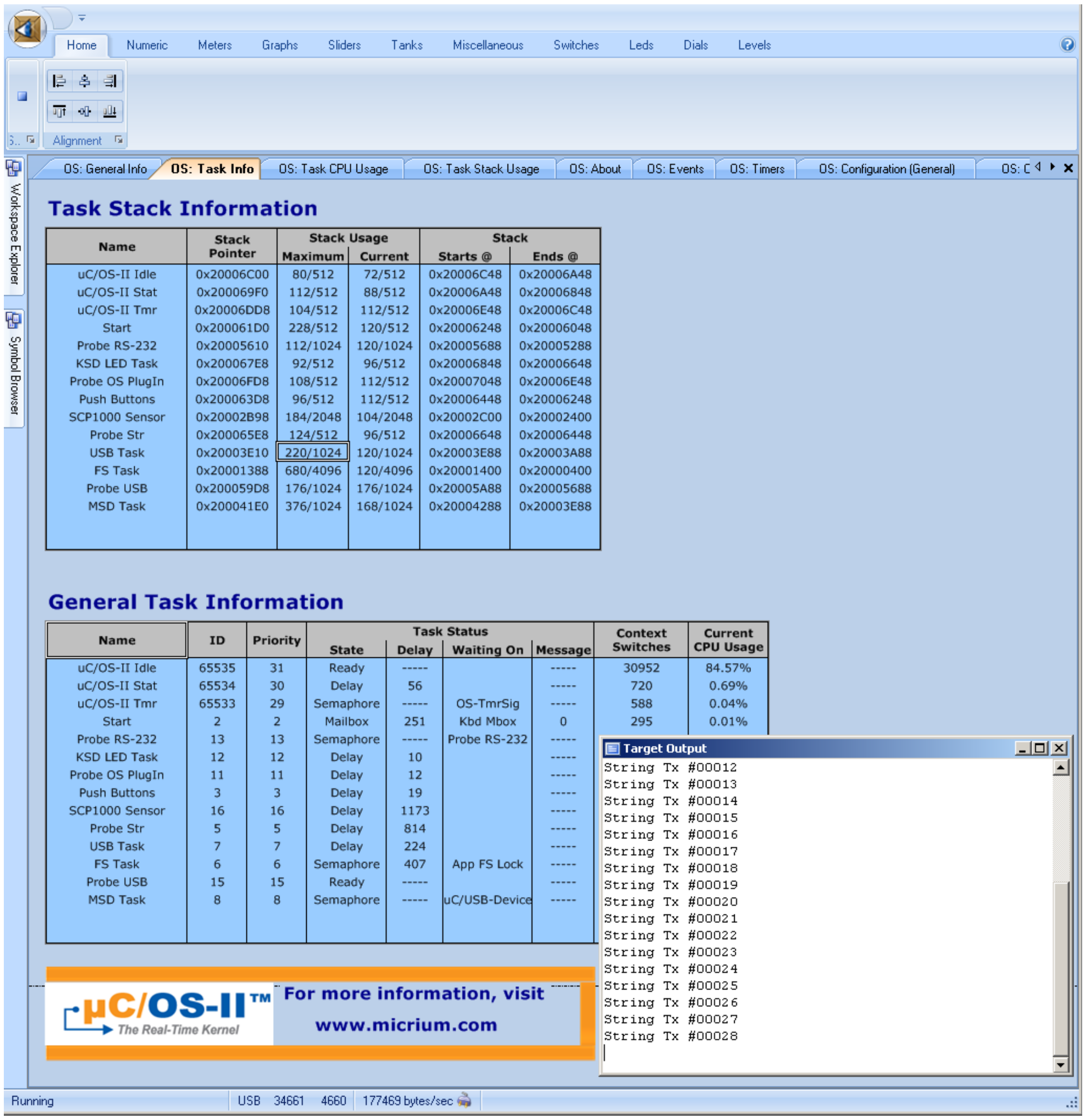


Figure 1-1. IAR STM32F103ZE Kickstart Kit.

If this appnote was downloaded in a packaged executable zip file, then it should have been found in the directory ***/Micrium/Appnotes/AN1xxx-RTOS/AN1913-uCOSII-ST-STM32F103ZE*** and the code files referred to herein are located in the directory structure displayed in Section 2.02; these files are described in Section 3.

The executable zip also includes example workspaces for **µC/Probe**. **µC/Probe** is a Windows program which retrieves the value of variables form a connected embedded target and displays the values in an engineer-friendly format. It interfaces with the IAR STM32F10ZE-SK via RS-232C. For more information, including instructions for downloading a trial and the demo version of the program, please refer to Section 6.



Task Stack Information

Name	Stack Pointer	Stack Usage		Stack	
		Maximum	Current	Starts @	Ends @
uC/OS-II Idle	0x20006C00	80/512	72/512	0x20006C48	0x20006A48
uC/OS-II Stat	0x200069F0	112/512	88/512	0x20006A48	0x20006848
uC/OS-II Tmr	0x20006DD8	104/512	112/512	0x20006E48	0x20006C48
Start	0x200061D0	228/512	120/512	0x20006248	0x20006048
Probe RS-232	0x20005610	112/1024	120/1024	0x20005688	0x20005288
KSD LED Task	0x200067E8	92/512	96/512	0x20006848	0x20006648
Probe OS PlugIn	0x20006FD8	108/512	112/512	0x20007048	0x20006E48
Push Buttons	0x200063D8	96/512	112/512	0x20006448	0x20006248
SCP1000 Sensor	0x20002B98	184/2048	104/2048	0x20002C00	0x20002400
Probe Str	0x200065E8	124/512	96/512	0x20006648	0x20006448
USB Task	0x20003E10	220/1024	120/1024	0x20003E88	0x20003A88
FS Task	0x20001388	680/4096	120/4096	0x20001400	0x20000400
Probe USB	0x200059D8	176/1024	176/1024	0x20005A88	0x20005688
MSD Task	0x200041E0	376/1024	168/1024	0x20004288	0x20003E88

General Task Information

Name	ID	Priority	Task Status				Context Switches	Current CPU Usage
			State	Delay	Waiting On	Message		
uC/OS-II Idle	65535	31	Ready	-----		-----	30952	84.57%
uC/OS-II Stat	65534	30	Delay	56		-----	720	0.69%
uC/OS-II Tmr	65533	29	Semaphore	-----	OS-TmrSig	-----	588	0.04%
Start	2	2	Mailbox	251	Kbd Mbox	0	295	0.01%
Probe RS-232	13	13	Semaphore	-----	Probe RS-232	-----		
KSD LED Task	12	12	Delay	10		-----		
Probe OS PlugIn	11	11	Delay	12		-----		
Push Buttons	3	3	Delay	19		-----		
SCP1000 Sensor	16	16	Delay	1173		-----		
Probe Str	5	5	Delay	814		-----		
USB Task	7	7	Delay	224		-----		
FS Task	6	6	Semaphore	407	App FS Lock	-----		
Probe USB	15	15	Ready	-----		-----		
MSD Task	8	8	Semaphore	-----	uC/USB-Device	-----		

Target Output

```
String Tx #00012
String Tx #00013
String Tx #00014
String Tx #00015
String Tx #00016
String Tx #00017
String Tx #00018
String Tx #00019
String Tx #00020
String Tx #00021
String Tx #00022
String Tx #00023
String Tx #00024
String Tx #00025
String Tx #00026
String Tx #00027
String Tx #00028
```

Figure 1-2. µC/Probe (with Target Output Window)

2. Getting Started

The following sections step through the prerequisites for using the demonstration application described in this document. First, the setup of the hardware will be outlined. Second, the use and setup of the IAR Embedded Workbench project will be described. Thirdly, the steps to build the projects and load the application onto the board through a JTAG will be described. Lastly, instructions will be provided for using the example application.

2.01 Setting up the Hardware

The IAR STM32F103ZE board can be power from 4 diferrenct sources:

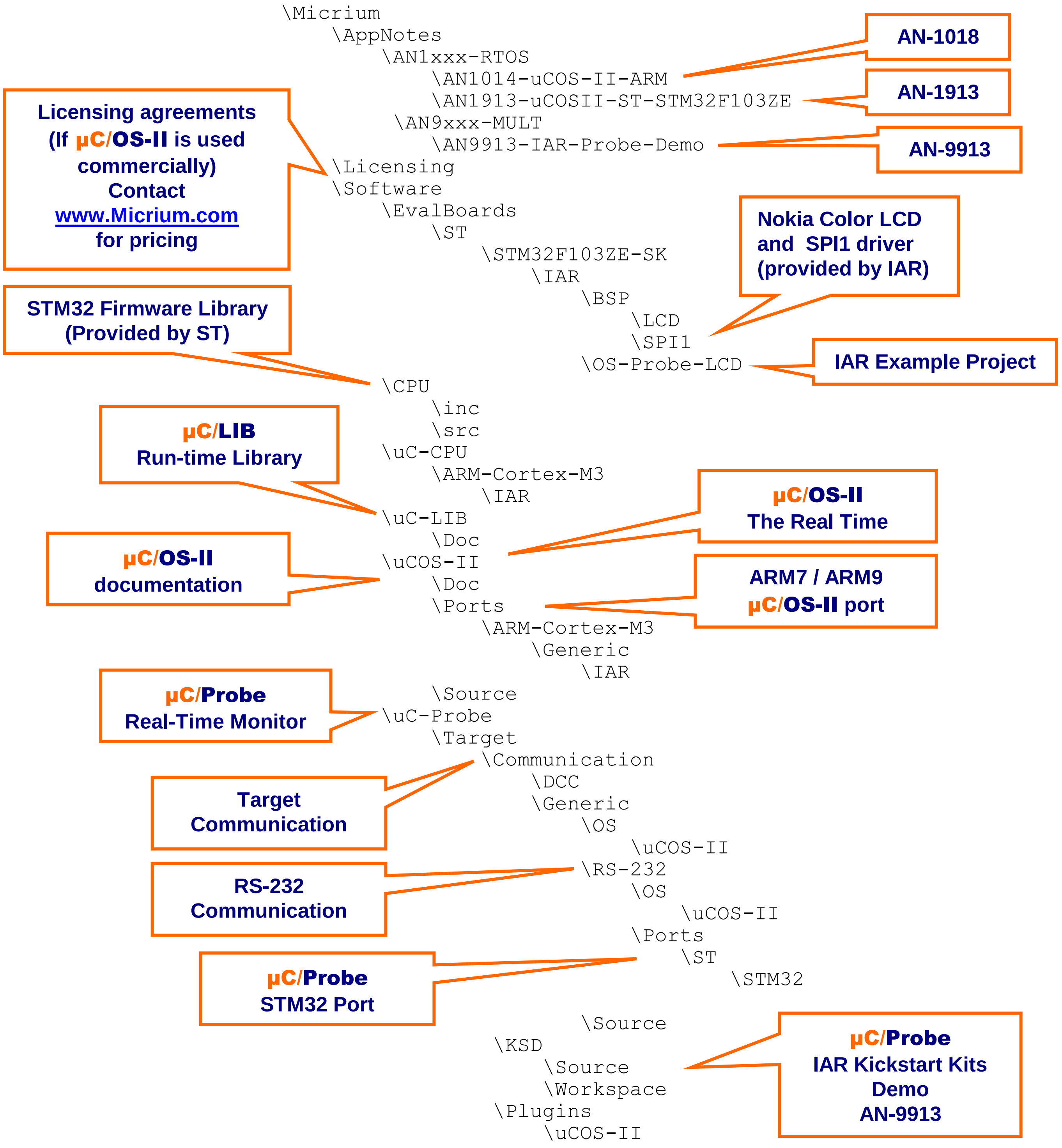
- External DC adapter.
- USB device port A,
- USB devuce port B
- J-link.

The power source can be selected change the jumper 'POWER_SELECT' as shown in the Figure 1-1.

To use **µC/Probe** with the IAR STM32F103ZE-SK, download and install the trial/demo version of the program from the Micriµm website as discussed in Section 6. After programming your target one of the included example projects, connect a RS-232 cable between your PC and the evaluation board, configure the RS-232 options (also covered in Section 6), and start running the program. The open data screens should update, as shown in Figure 1-2. The IAR STM32F10ZE-SK example application is configured to use UART2, the RS-232C connector labeled "RS-232 for **µC/Probe**" in Figure 1-1.

2.02 Directory Tree

If this file were downloaded as part of an executable zip file (which should have been named *uCOSII-ST-STM32F103ZE-SK.exe*, then the code files referred to herein are located in the directory structure shown in Figure 2-2.


AN-1018
AN-1913
AN-9913
Licensing agreements (If µC/OS-II is used commercially) Contact www.Micrium.com for pricing
Nokia Color LCD and SPI1 driver (provided by IAR)
STM32 Firmware Library (Provided by ST)
IAR Example Project
µC/LIB Run-time Library
µC/OS-II The Real Time
µC/OS-II documentation
ARM7 / ARM9 µC/OS-II port
µC/Probe Real-Time Monitor
Target Communication
RS-232 Communication
µC/Probe STM32 Port
µC/Probe IAR Kickstart Kits Demo AN-9913

```
\Micrium
    \AppNotes
        \AN1xxx-RTOS
            \AN1014-uCOS-II-ARM
            \AN1913-uCOSII-ST-STM32F103ZE
         \AN9xxx-MULT
            \AN9913-IAR-Probe-Demo
    \Licensing
    \Software
        \EvalBoards
            \ST
                \STM32F103ZE-SK
                    \IAR
                        \BSP
                            \LCD
                            \SPI1
                        \OS-Probe-LCD
        \CPU
            \inc
            \src
        \uC-CPU
            \ARM-Cortex-M3
                \IAR
        \uC-LIB
            \Doc
        \uCOS-II
            \Doc
            \Ports
                \ARM-Cortex-M3
                    \Generic
                        \IAR
            \Source
        \uC-Probe
            \Target
                \Communication
                    \DCC
                    \Generic
                        \OS
                            \uCOS-II
                        \RS-232
                            \OS
                                \uCOS-II
                            \Ports
                                \ST
                                    \STM32

                        \Source
                 \KSD
                     \Source
                     \Workspace
                 \Plugins
                     \uCOS-II
```

Figure 2-1. Directory Structure

2.03 Using the IAR Projects

One IAR projects is located in the directory marked “IAR Example Project ” in Figure 2-1:

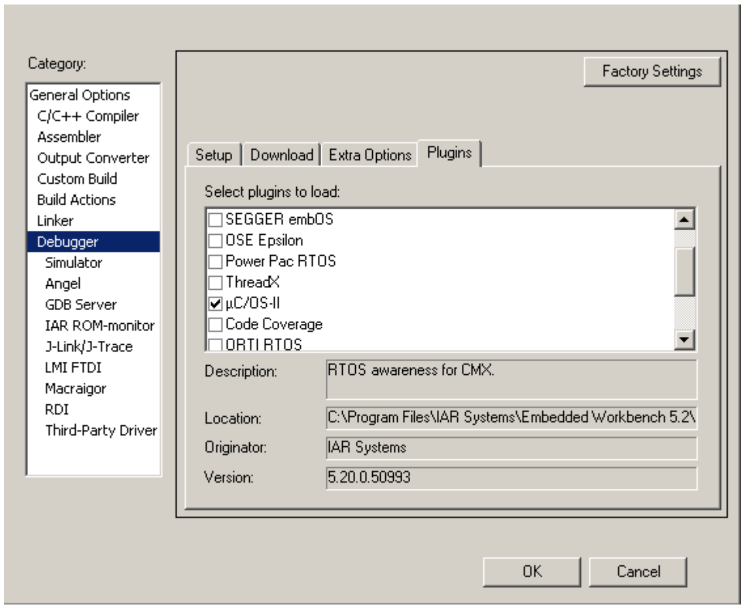
/Micrium/Software/EvalBoardsST/STM32F103ZE-SK/IAR/OS-Probe-LCD

The example project, *STM32F103ZE-SK-OS-Probe-LCD-v5-2.ewp*, is intended for EWARM v5.2x. To view this example, start an instance of IAR EWARM v5.2x, and open the workspace file *STM32F103ZE-SK-OS-Probe-LCD-v5-2.eww*. To do this, select the “Open” menu command under the “File” menu, select the “Workspace…” submenu command and select the workspace file after navigating to the project directory. The project tree shown in Figure 2-2 should appear. (In addition, the workspace should be openable by double-clicking on the file itself in a Windows Explorer window.)

2.03.01 μC/OS-II Kernel Awareness

When running the IAR C-Spy debugger, the **μC/OS-II** Kernel Awareness Plug-In can be used to provide useful information about the status of **μC/OS-II** objects and tasks. If the **μC/OS-II** Kernel Awareness Plug-In is currently enabled, then a “μC/OS-II” menu should be displayed while debugging. Otherwise, the plug-in can be enabled. Stop the debugger (if it is currently active) and select the “Options” menu item from the “Project” menu. Select the “Debugger” entry in the list box and then select the “Plugins” tab pane. Find the **μC/OS-II** entry in the list and select the check box beside the entry, as shown in Figure 2-4.

When the code is reloaded onto the evaluation board, the “μC/OS-II” menu should appear. Options are included to display lists of kernel objects such as semaphores, queues, and mailboxes, including for each entry the state of the object. Additionally, a list of the current tasks may be displayed, including for each task pertinent information such as used stack space, task status, and task priority, in addition to showing the actively executing task. An example task list for this project is shown in Figure 2-5.

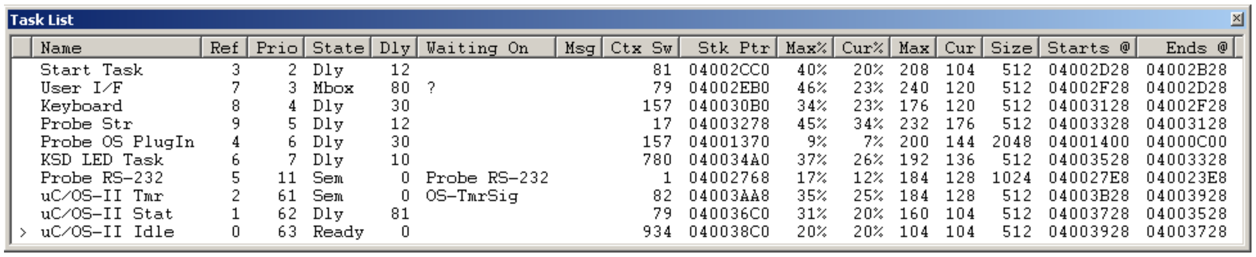


Select plugins to load:

☐ SEGGER embOS
☐ OSE Epsilon
☐ Power Pac RTOS
☐ ThreadX
☑ μC/OS-II
☐ Code Coverage
☐ ORTI RTOS

Description: RTOS awareness for CMX.
Location: C:\Program Files\IAR Systems\Embedded Workbench 5.2\
Originator: IAR Systems
Version: 5.20.0.50993

Figure 2-4. Enabling the μC/OS-II Kernel Awareness Plug-In


Task List

	Name	Ref	Prio	State	Dly	Waiting On	Msg	Ctx Sw	Stk Ptr	Max%	Cur%	Max	Cur	Size	Starts @	Ends @
	Start Task	3	2	Dly	12			81	04002CC0	40%	20%	208	104	512	04002D28	04002B28
	User I/F	7	3	Mbox	80	?		79	04002EB0	46%	23%	240	120	512	04002F28	04002D28
	Keyboard	8	4	Dly	30			157	040030B0	34%	23%	176	120	512	04003128	04002F28
	Probe Str	9	5	Dly	12			17	04003278	45%	34%	232	176	512	04003328	04003128
	Probe OS PlugIn	4	6	Dly	30			157	04001370	9%	7%	200	144	2048	04001400	04000C00
	KSD LED Task	6	7	Dly	10			780	040034A0	37%	26%	192	136	512	04003528	04003328
	Probe RS-232	5	11	Sem	0	Probe RS-232		1	04002768	17%	12%	184	128	1024	040027E8	040023E8
	uC/OS-II Tmr	2	61	Sem	0	OS-TmrSig		82	04003AA8	35%	25%	184	128	512	04003B28	04003928
	uC/OS-II Stat	1	62	Dly	81			79	040036C0	31%	20%	160	104	512	04003728	04003528
>	uC/OS-II Idle	0	63	Ready	0			934	040038C0	20%	20%	104	104	512	04003928	04003728

Figure 2-5. μC/OS-II Task List.

2.04 Example Applications

The example application will blink the status LEDs. The blink patter can be changed using the push buttons 1 and 2.

The system state will be output to the color LCD display, the joystick (toggle left/right) can be used to move the output to a new item.

Stack Out of Range Notification

While debugging this project (or any other **μC/OS-II** project), IAR may log a SVC stack pointer out-of-range notification in the "Debug Log" window. This is actually normal behavior and does **NOT** indicate an error. IAR EWARM does not understand that the SVC stack pointer points to the stack for the current task stack.

3. Directories and Files

Application Notes

\Micrium\AppNotes\AN1xxx-RTOS\AN1014-uCOS-II-ARM

This directory contains ***AN-1014.pdf***, the application note describing the ARM port for **μC/OS-II**, and ***AN-1014-PPT.pdf***, a supplement to *AN-1014.pdf*.

\Micrium\AppNotes\AN1xxx-RTOS\AN1913-uCOSII-ST-STM32F103ZE

This directory contains this application note, ***AN-1913.pdf***.

\Micrium\AppNotes\AN9xxx-MULT\AN-9913-IAR-Probe-Demo

This directory contains this application note, ***AN-9913.pdf***.

Licensing Information

\Micrium\Licensing

Licensing agreements are located in this directory. Any source code accompanying this appnote is provided for evaluation purposes only. If you choose to use **μC/OS-II** in a commercial product, you must contact Micriμm regarding the necessary licensing.

μC/OS-II Files

\Micrium\Software\uCOS-II\Doc

This directory contains documentation for **μC/OS-II**.

\Micrium\Software\uCOS-II\Ports\ARM\Generic\IAR

This directory contains the standard processor-specific files for the generic **μC/OS-II** ARM port assuming the IAR toolchain. These files could easily be modified to work with other toolchains (i.e., compiler/assembler/linker/locator/debugger); however, the modified files should be placed into a different directory. The following files are in this directory:

- *os_cpu.h*
- *os_cpu_a.asm*
- *os_cpu_c.c*
- *os_dcc.c*
- *os_dbg.c*

With this port, **μC/OS-II** can be used in either ARM or Thumb mode. Thumb mode, which drastically reduces the size of the code, was used in this example, but compiler settings may be switched (as discussed in Section 2.30) to generate ARM-mode code without needing to change either the port or the application code. The ARM/Thumb port is described in application note *AN-1014* which is available from the Micrium web site.

\Micrium\Software\uCOS-II\Source

This directory contains the processor-independent source code for **μC/OS-II**.

µC/Probe Files

\Micrium\Software\uC-Probe\Communication\Generic\

This directory contains the **µC/Probe** generic communication module, the target-side code responsible for responding to requests from the **µC/Probe** Windows application (including requests over RS-232).

\Micrium\Software\uC-Probe\Communication\Generic\Source

This directory contains ***probe_com.c*** and ***probe_com.h***, the source code for the generic communication module.

\Micrium\Software\uC-Probe\Communication\Generic\OS\uCOS-II

This directory contains ***probe_com_os.c***, which is the **µC/OS-II** port for the **µC/Probe** generic communication module.

\Micrium\Software\uC-Probe\Communication\Generic\Source\RS-232

This directory contains the RS-232 specific code for **µC/Probe** generic communication module, the target-side code responsible for responding to requests from the **µC/Probe** Windows application over RS-232

\Micrium\Software\uC-Probe\Communication\Generic\Source\RS-232\Source

This directory contains ***probe_rs232.c*** and ***probe_rs232.h***, the source code for the generic communication module RS-232 code.

\Micrium\Software\uC-Probe\Communication\Generic\Source\RS-232\Ports\ST\STM32

This directory contains ***probe_rs232c.c*** and ***probe_rs232c.h***, the STM32xx port for the RS-232 communications.

\Micrium\Software\uC-Probe\Communication\Generic\Source\RS-232\OS\uCOS-II

This directory contains ***probe_rs232_os.c***, which is the **µC/OS-II** port for the **µC/Probe** RS-232 communication module.

\Micrium\Software\uC-Probe\Target\Demo\KSD\Source

This directory contains **ksd.c** and **ksd.h,** the source code for the IAR Kickstart kits demo example for the demo version of **µC/Probe**.

\Micrium\Software\uC-Probe\Target\Demo\KSD\Workspace

This directory contains ***OS-Probe-Kickstart-Demo-Workspace.wsp*** which is the generic **µC/Probe** workspace the IAR Kickstart kits demo example for the demo version of **µC/Probe**.

µC/CPU Files

\Micrium\Software\uC-CPU

This directory contains ***cpu_def.h***, which declares `#define` constants for CPU alignment, endianness, and other generic CPU properties.

\Micrium\Software\uC-CPU\ARM\IAR

This directory contains ***cpu.h*** and ***cpu_a.s***. *cpu.h* defines the Micriµm portable data types for 8-, 16-, and 32-bit signed and unsigned integers (such as `CPU_INT16U`, a 16-bit unsigned integer). These allow code to be independent of processor and compiler word size definitions. *cpu_a.s* contains generic assembly code for ARM7 and ARM9 processors which is used to enable and disable interrupts within the operating system. This code is called from C with `OS_ENTER_CRITICAL()` and `OS_EXIT_CRITICAL()`.

µC/LIB Files

\Micrium\Software\uC-LIB

This directory contains ***lib_def.h***, which provides `#defines` for useful constants (like `DEF_TRUE` and `DEF_DISABLED`) and macros.

The files *lib_mem.c* and *lib_mem.h* contain code to replace the standard library functions `memclr()`, `memset()`, `memcpy()` and `memcmp()`. These functions are replaced by `Mem_Clr()`, `Mem_Set()`, `Mem_Copy()` and `Mem_Cmp()`, respectively. The reason we declared our own functions is for third party certification purposes.

The files *lib_str.c* and *lib_str.h* contain code to replace the standard library functions `str???()` with their equivalent `Str_???()` functions. Again, this is to simplify third party certification foravionics and medical use.

\Micrium\Software\uC-LIB\Doc

This directory contains the documentation for **µC/LIB**.

ST STM32 Firmware library.

\Micrium\Software\CPU\ST\STM32\inc

This directory contains the STM32xx firmware library includes files.

\Micrium\Software\CPU\ST\STM32\src

This directory contains the STM32xx firmware library source code files.

Application Code

\Micrium\Software\EvalBoards\ST\STM32F103ZE \IAR\OS-Probe

This directory contains the soruce code the **µC/OS-II** and **µC/Probe** example application:

- ***app.c*** contains the test code for the example application including calls to the functions that start multitasking within **µC/OS-II**, register tasks with the kernel, and update the user interface (the LEDs , the push buttons, the LCD and the Joystick)
- ***app_cfg.h*** is a configuration file specifying stack sizes and priorities for all user tasks and `#define`s for important global application constants.
- ***includes.h*** is the master include file used by the application.
- ***os_cfg.h*** is the **µC/OS-II** configuration file.
- ***STM32F103ZE-OS-Probe-Workspace.wsp*** is an example **µC/Probe** workspace.
- ***STM32F103ZE-OS-Probe-Workspace -v5-2.**** are the IAR EWARM v5.2x project files.

\Micrium\Software\EvalBoards\ST\STM32F103ZE\IAR\BSP

This directory contains the Board Support Package for the IAR STM32F103ZE-SK evaluation board:

- ***bsp.c*** contains the board support package functions which initialize critical processor functions (e.g., the PLL) and provide support for peripherals such as the push buttons and LEDs.
- ***bsp.h*** contains prototypes for functions that may be called by the user.
- ***cstartup.s*** is the IAR EWARM v5.xx startup file. This file performs critical processor initialization (such as the initialization of task stacks), readying the platform to enter `main()`.
- ***STM32F103ZE_Flash.icf*** is a IAR EWARM v5.xx linker file which contains information about the placement of data and code segments in the processor's memory map.

4. Application Code

The example application described in this appnote, *AN-1913* is a simple demonstration of **μC/OS-II** and **μC/Probe** for the STMicroelectronics STM32F103ZE processor on the IAR STM32F103ZE evaluation board. The basic procedure for setting up and using each of these can be gleaned from an inspection of the application code contained in *app.c*, which should serve as a beginning template for further use of these software modules. Being but a basic demonstration of software and hardware functionality, this code will make evident the power and convenience of **μC/OS-II** "The Real-Time Kernel" used on the STM32F103ZE processor without the clutter or confusion of a more complex example.

4.01 *app.c*

Five functions of interest are located in *app.c*:

1. **`main()`** is the entry point for the application, as it is with most C programs. This function initializes the operating system, creates the primary application task, `App_TaskStart()`, begins multitasking, and exits.

2. **`App_TaskStart()`**, after creating the user interface tasks, enters an infinite loop in which it blinks the LEDs on the board. The blinking pattern is determined by the messages passed by the `App_TaskKbd()` task and the frequency is determined by the value of the potentiometer.

3. **`App_TaskKbd()`** monitors the state of the keyboard and passes messages to the `App_TaskStart` () task.

4. **`App_TaskJoy()`** monitors the state of the Joystick and passes messages to the `App_TaskLCD`() task.

5. **`App_TaskLCD()`**, Outputs the state of the system based on the display state passed to it by `App_TaskJoy()`.

```
void  main (void)                                                   /* Note 1 */
{
    CPU_INT08U  err;


    BSP_IntDisAll();                                                /* Note 2 */

    OSInit();                                                       /* Note 3 */

    OSTaskCreateExt((void (*)(void *)) App_TaskStart,              /* Note 4 */
                    (void           *) 0,
                    (OS_STK         *)&AppTaskStartStk[APP_CFG_TASK_START_STK_SIZE - 1],
                    (INT8U           ) APP_CFG_TASK_START_PRIO,
                    (INT16U          ) APP_CFG_TASK_START_PRIO,
                    (OS_STK         *)&AppTaskStartStk[0],
                    (INT32U          ) APP_CFG_TASK_START_STK_SIZE,
                    (void           *) 0,
                    (INT16U          )(OS_TASK_OPT_STK_CHK | OS_TASK_OPT_STK_CLR));
#if OS_TASK_NAME_SIZE > 13                                          /* Note 5 */
    OSTaskNameSet(APP_CFG_TASK_START_PRIO, "Start Task", &err);
#endif

    OSStart();                                                      /* Note 6 */
}
```

Listing 4-1, `main()`

Listing 4-1, Note 1: As with most C applications, the code starts in `main()`.

Listing 4-1, Note 2: All interrupts are disabled to make sure the application does not get interrupted until it is fully initialized.

Listing 4-1, Note 3: `OSInit()` must be called before creating a task or any other kernel object, as must be done with all **μC/OS-II** applications.

Listing 4-1, Note 4: At least one task must be created (in this case, using `OSTaskCreateExt()` to obtain additional information about the task). In addition, **μC/OS-II** creates either one or two internal tasks in `OSInit()`. **μC/OS-II** always creates an idle task, `OS_TaskIdle()`, and will create a statistic task, `OS_TaskStat()` if you set `OS_TASK_STAT_EN` to 1 in *os_cfg.h*.

Listing 4-1, Note 5: As of V2.6x, you can now name **μC/OS-II** tasks (and other kernel objects) and display task names at run-time or with a debugger. In this case, the `App_TaskStart()` is given the name “Start Task”. Because C-Spy can work with the Kernel Awareness Plug-In available from Micriμm, task names can be displayed during debugging.

Listing 4-1, Note 6: Finally multitasking under **μC/OS-II** is started by calling `OSSTart()`. **μC/OS-II** will then begin executing `App_TaskStart()` since that is the highest-priority task created (both `OS_TaskStat()` and `OS_TaskIdle()` having lower priorities).

```
static  void  App_TaskStart (void *p_arg)
{
    CPU_INT08U   os_err;
    CPU_INT08U   pb_state;
    CPU_INT08U   i;
    CPU_INT16U   dly;
    CPU_INT08U  *p_msg;



    (void)p_arg;

    BSP_Init();                                                     /* Note 1 */
    OS_CPU_SysTickInit();                                           /* Note 2 */

#if (OS_TASK_STAT_EN > 0)
    OSStatInit();                                                   /* Note 3 */
#endif

#if (APP_CFG_PROBE_COM_EN       == DEF_ENABLED) || \
    (APP_CFG_PROBE_OS_PLUGIN_EN == DEF_ENABLED)
    App_ProbeInit();                                                /* Note 4 */
#endif

    App_EventCreate();                                              /* Note 5 */
    App_TaskCreate();

    BSP_LED_Off(0);

    while (DEF_TRUE) {                                              /* Note 6 */

        dly = (BSP_ADC_GetStatus(1) >> 3) + 10;
        p_msg = (CPU_INT08U *)(OSMboxPend(AppStartMbox,
                                          (OS_TICKS_PER_SEC / 3),
                                           &os_err));

        if (os_err == OS_NO_ERR) {
            pb_state = (CPU_INT32U)p_msg;
            BSP_LED_Off(0);
        }

        switch (pb_state) {
            default:
            case APP_PB_1_PRESSED:
                 for (i = 1; i <= 4; i++) {
                     BSP_LED_On(i);
                     OSTimeDlyHMSM(0, 0, 0, dly);
                     BSP_LED_Off(i);
                 }
                 break;

            case APP_PB_2_PRESSED:
                 BSP_LED_Toggle(0);
                 OSTimeDlyHMSM(0, 0, 0, dly);
                 break;
        }
    }
}
```

Listing 4-2, `App_TaskStart()`

Listing 4-2, Note 1: `BSP_Init()` initializes the Board Support Package—the I/Os, tick interrupt, etc. See Section 5 for details.

Listing 4-2, Note 2: `OS_CPU_SysTcikInit()` Initializes Cortex-M3 Systick Tiemer, which generates the **µC/OS-II** time tick.

Listing 4-2, Note 3: `OSStatInit()` initializes **μC/OS-II**'s statistic task. This only occurs if you enable the statistic task by setting `OS_TASK_STAT_EN` to 1 in *os_cfg.h*. The statistic task measures overall CPU usage (expressed as a percentage) and performs stack checking for all the tasks that have been created with `OSTaskCreateExt()` with the stack checking option set.

Listing 4-2, Note 4: `App_ProbeInit()` initialize **μC/Probe**. This function calls `OSProbe_Init()` which initializes the **μC/Probe** plug-in for **μC/OS-II**, which maintains CPU usage statistics for each task. `ProbeCom_Init()` which initializes the **μC/Probe** generic communication module, `ProbeRS232_Init()` which initializes the RS-232 communication module and `KSD_Init`() which initializes the IAR Kickstart kit demo (KSD) for the demo version of **μC/Probe.** (see AN-9913). After these have been initialized, the **μC/Probe** Windows program will be able to download data from the processor. For more information, see Section 6.

Listing 4-2, Note 5 `App_TaskCreate()` Creates all the application task. `App_EventCreate()` creates all the application events, in there, a three mailbox are created. When the push button 1 or 2 are pressed the `App_TaskKbd()` will send a message to `App_TaskStart()` indicating wich push button has been pressed changing the LED blinking pattern. The other two mailboxes are used to communicate events between `App_TaskJoy()` and `App_TaskLCD()`.

Listing 4-2, Note 6: Any task managed by **μC/OS-II** must either enter an infinite loop 'waiting' for some event to occur or terminate itself. This task enters an infinite loop in which the LEDs are toggled.

4.02 *os_cfg.h*

The file ***os_cfg.h*** is used to configure **μC/OS-II** and defines the maximum number of tasks that your application can have, which services will be enabled (semaphores, mailboxes, queues, etc.), the size of the idle and statistic task and more. In all, there are about 60 or so `#define` that you can set in this file. Each entry is commented and additional information about the purpose of each `#define` can be found in Jean Labrosse's book, ***μC/OS-II, The Real-Time Kernel, 2nd Edition***. *os_cfg.h* assumes you have **μC/OS-II** V2.83 or higher but also works with previous versions of **μC/OS-II**.

- **`OS_APP_HOOKS_EN`** is set to 1 so that the cycle counters in the `OS_TCB`s will be maintained.

- Task sizes for the Idle (**`OS_TASK_IDLE_STK_SIZE`**), statistics **`OS_TASK_STAT_STK_SIZE`**) and timer (**`OS_TASK_TMR_STK_SIZE`**) task are set to 128 `OS_STK` elements (each is 4 bytes) and thus each task stack is 512 bytes. If you add code to the examples make sure you account for additional stack usage.

- **`OS_DEBUG_EN`** is set to 1 to provide valuable information about **μC/OS-II** objects to IAR's C-Spy through the Kernel Awareness plug-in. Setting `OS_DEBUG_EN` to 0 should some code space (though it will not save much).

- **`OS_LOWEST_PRIO`** is set to 31, allowing up to 32 total tasks.

- **`OS_MAX_TASKS`** determines the number of "application" tasks and is currently set to 16.

- **`OS_TICKS_PER_SEC`** is set to 1000 Hz. This value can be changed as needed and the proper tick rate will be adjusted in *bsp.c* if you change this value.

5. Board Support Package (BSP)

The Board Support Package (BSP) provides functions to encapsulate common I/O access functions and make porting your application code easier. Essentially, these files are the interface between the application and the IAR STM32F103ZE-SK board. Though one file, *bsp.c*, contains some functions which are intended to be called directly by the user (all of which are prototyped in *bsp.h*), the other files serve the compiler (as with *cstartup.)*.

5.01 BSP, *bsp.c* and *bsp.h*

The file *bsp.c* implements several global functions, each providing some important service, be that the initialization of processor functions for **μC/OS-II** to operate or the toggling of an LED. Several local functions are defined as well to perform some atomic duty, initializing the I/O for the LED or initialize the **μC/OS-II** tick timer. The discussion of the BSP will be limited to the discussion of the global functions that might be called from user code (and may be called from the example application).

The global functions defined in *bsp.c* (and prototyped in *bsp.h*) may be roughly divided into two categories: critical processor initialization and user interface services. Three functions constitute the former:

- **`BSP_Init()`** is called by the application code to initialize critical processor features (particularly the **μC/OS-II** tick interrupt) after multitasking has started (i.e., `OS_Start()` has been called). This function should be called before any other BSP functions are used. See Listing 5-1 for more details.
- **`BSP_IntDisAll()`** is called to disable all interrupts, thereby preventing any interrupts until the processor is ready to handle them.
- **`BSP_CPU_ClkFreq()`** returns the clock frequency in Hz.

Several functions provide access to user interface components:

- **`BSP_LED_Toggle()`**, **`BSP_LED_On()`** and **`BSP_LED_Off()`** will toggle, turn on, and turn off (respectively) the LED corresponding to the ID passed as the argument If an argument of 0 is provided, the appropriate action will be performed on all LEDs. Valid IDs are 1, 2, 3 and 4 (inclusive).
- **`BSP_PB_GetStatus ()`** returs the status of the board's push buttons corresponding the the ID passed as the argument.
- **`BSP_ADC_GetStatus ()`** returns the status of the board's potentiometer using the ADC channels 1, 2 and 3.
- **`BSP_PB_GetPosition()`** and **`BSP_PB_GetStatus()`** returns the position and the status of the joystick.

5.02 Processor Initialization Functions

```
void  BSP_Init (void)
{
    BSP_RCC_Init();                                                   /* Note 1 */

    BSP_ADC_Init();                                                   /* Note 2 */

    BSP_LED_Init();

    BSP_PB_Init();

    BSP_Joy_Init();
}
```

Listing 5-1, `BSP_Init()`

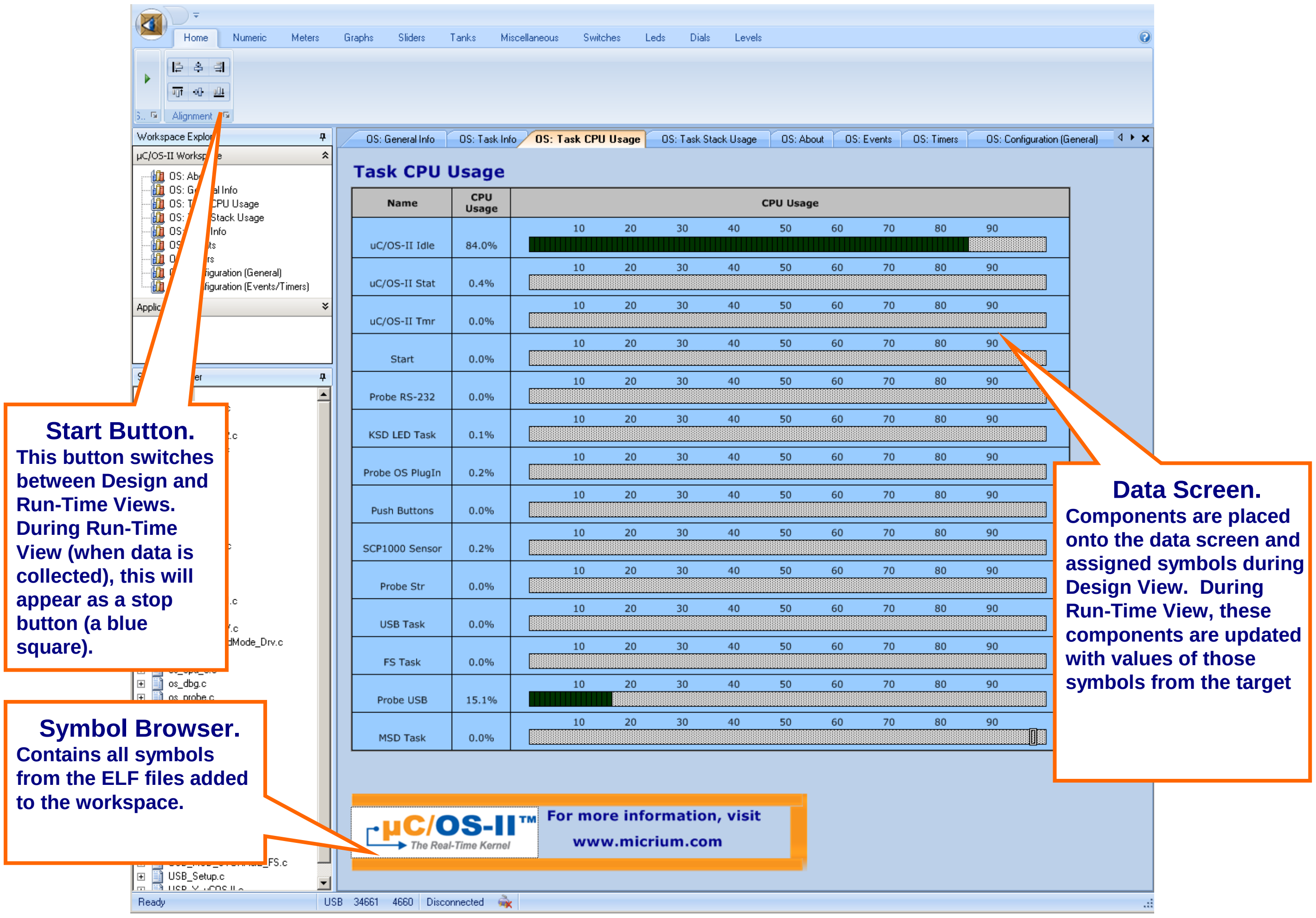
Listing 5-1, Note 1: The RCC (Reset and clock controll) module is enabled setting the CPU at 72 Mhz

Listing 5-1, Note 2: The board peripherals (ADC, LEDs, PBs and the Joystick) are initlized.

6. µC/Probe

µC/Probe is a Windows program which retrieves the values of global variables from a connected embedded target and displays the values in a engineer-friendly format. To accomplish this, an ELF file, created by the user's compiler and containing the names and addresses of all the global symbols on the target, is monitored by **µC/Probe**. The user places components (such as gauges, labels, and charts) into a Data Screen in a **µC/Probe** workspace and assigns each one of these a variable from the Symbol Browser, which lists all symbols from the ELF file. The symbols associated with components placed on an open Data Screen will be updated after the user presses the start button (assuming the user's PC is connected to the target).

A small section of code resident on the target receives commands from the Windows application and responds to those commands. The commands ask for a certain number of bytes located at a certain address, for example, "Send 16 bytes beginning at 0x0040102C". The Windows application, upon receiving the response, updates the appropriate component(s) on the screens with the new values.



Task CPU Usage

Name	CPU Usage
uC/OS-II Idle	84.0%
uC/OS-II Stat	0.4%
uC/OS-II Tmr	0.0%
Start	0.0%
Probe RS-232	0.0%
KSD LED Task	0.1%
Probe OS PlugIn	0.2%
Push Buttons	0.0%
SCP1000 Sensor	0.2%
Probe Str	0.0%
USB Task	0.0%
FS Task	0.0%
Probe USB	15.1%
MSD Task	0.0%

Start Button. This button switches between Design and Run-Time Views. During Run-Time View (when data is collected), this will appear as a stop button (a blue square).

Symbol Browser. Contains all symbols from the ELF files added to the workspace.

Data Screen. Components are placed onto the data screen and assigned symbols during Design View. During Run-Time View, these components are updated with values of those symbols from the target

Figure 6-1. **µC/Probe** Windows Program

To use **µC/Probe** with the example project (or your application), do the following:

1. **Download and Install µC/Probe.** A trial version of **µC/Probe** can be downloaded from the Micriµm website at

   http://www.micrium.com/products/probe/probe.html

IAR Kickstart Kits Users

If this development board is part of the IAR Kickstart Kit a demo version of **µC/Probe** is already included in the installation CD. Please refer to the application note **AN-9913** for more details in how to use the demo version of **µC/Probe** with the IAR Kickstart kits.

2. **Open µC/Probe.** After downloading and installing this program, open the example **µC/Probe** workspace for **µC/OS-II**, named ***OS-Probe-Workspace.wsp***, which should be located in your installation directory at

   /Program Files//Micrium/uC-Probe/Target/Plugins/uCOS-II/Workspace

3. **Connect Target to PC**. Currently, **µC/Probe** can use RS-232 to retrieve information from the target. You should connect a RS-232 cable between your target and computer.

4. **Load Your ELF File**. The example projects included with this application note are already configured to output an ELF file. (If you are using your own project, please refer to Appendix A of the **µC/Probe** user manual for directions for generating an ELF file with your compiler.) This file should be in

   /<Project Directory>/<Configuration Name>/exe/

   where <***Project Directory***> is the directory in which the IAR EWARM project is located (extension *.ewp) and <***Configuration Name***> is the name of the configuration in that project which was built to generate the ELF file and which will be loaded onto the target. The ELF file will be named

   <Project Name>.elf

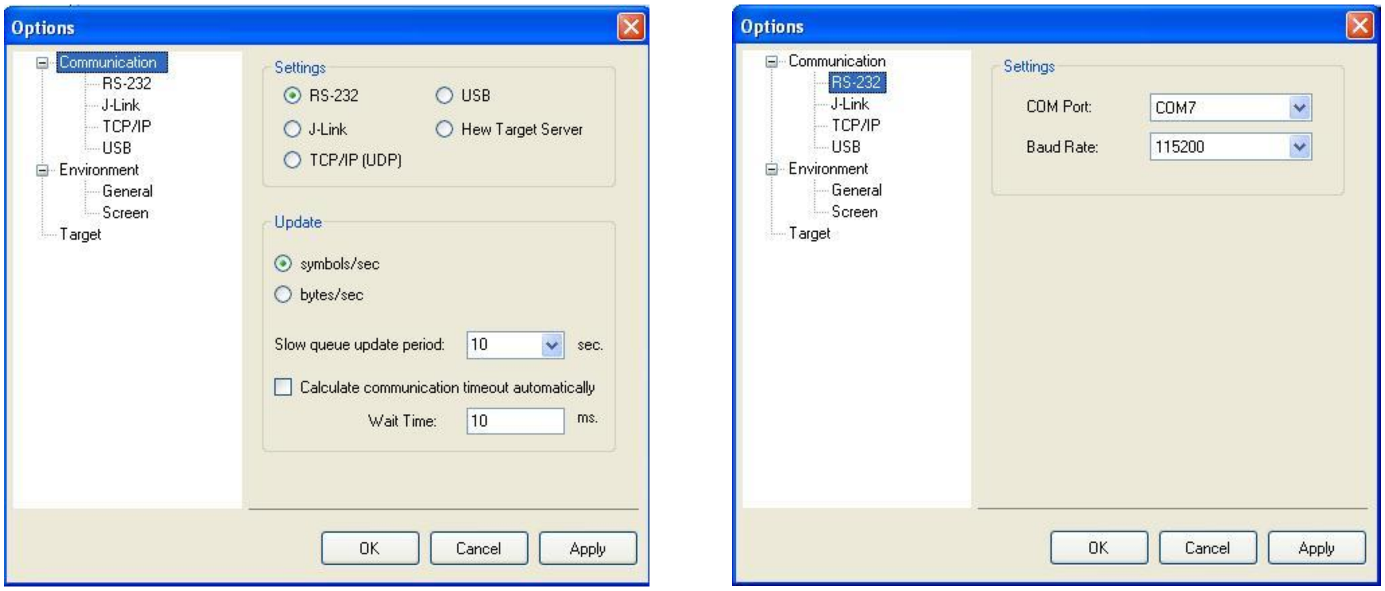
   in EWARM v4.4x and

   <Project Name>.out

   in EWARM v5.xx unless you specify otherwise. To load this ELF file, right-click on the symbol browser and choose "Add Symbols".

5. **Configure the RS-232 Options**. In **µC/Probe**, choose the "Options" menu item on the "Tools" menu. A dialog box as shown in Figure 6-2 (left) should appear. Choose the "RS-232" radio button. Next, select the "RS-232" item in the options tree, and choose the appropriate COM port and baud rate. The baud rate for the projects accompanying this appnote is 115200.

6. **Start Running**. You should now be ready to run **µC/Probe**. Just press the run button (▶) to see the variables in the open data screens update. Figure 6-3 displays the **µC/OS-II** workspace which display detailed information about each task's state.



Settings

[✓ RS-232] USB
J-Link Hew Target Server
TCP/IP (UDP)

Update

[✓ symbols/sec]
bytes/sec

Slow queue update period: 10 sec.

[ ] Calculate communication timeout automatically

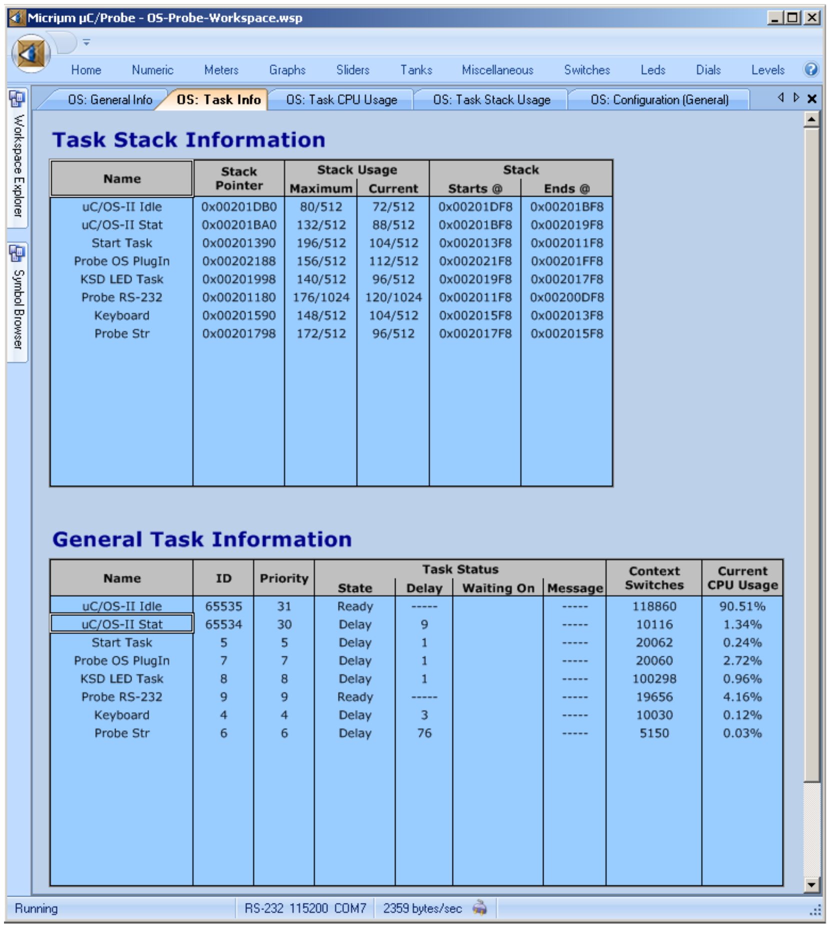
Wait Time: 10 ms.

Settings

COM Port: COM7

Baud Rate: 115200

Figure 6.2. µC/Probe Options



Task Stack Information

Name	Stack Pointer	Stack Usage		Stack	
		Maximum	Current	Starts @	Ends @
uC/OS-II Idle	0x00201DB0	80/512	72/512	0x00201DF8	0x00201BF8
uC/OS-II Stat	0x00201BA0	132/512	88/512	0x00201BF8	0x002019F8
Start Task	0x00201390	196/512	104/512	0x002013F8	0x002011F8
Probe OS PlugIn	0x00202188	156/512	112/512	0x002021F8	0x00201FF8
KSD LED Task	0x00201998	140/512	96/512	0x002019F8	0x002017F8
Probe RS-232	0x00201180	176/1024	120/1024	0x002011F8	0x00200DF8
Keyboard	0x00201590	148/512	104/512	0x002015F8	0x002013F8
Probe Str	0x00201798	172/512	96/512	0x002017F8	0x002015F8

General Task Information

Name	ID	Priority	Task Status				Context Switches	Current CPU Usage
			State	Delay	Waiting On	Message		
uC/OS-II Idle	65535	31	Ready	-----		-----	118860	90.51%
uC/OS-II Stat	65534	30	Delay	9		-----	10116	1.34%
Start Task	5	5	Delay	1		-----	20062	0.24%
Probe OS PlugIn	7	7	Delay	1		-----	20060	2.72%
KSD LED Task	8	8	Delay	1		-----	100298	0.96%
Probe RS-232	9	9	Ready	-----		-----	19656	4.16%
Keyboard	4	4	Delay	3		-----	10030	0.12%
Probe Str	6	6	Delay	76		-----	5150	0.03%

Running RS-232 115200 COM7 2359 bytes/sec

Figure 6-3. µC/Probe Run-Time: µC/OS-II Task Information

Licensing

µC/OS-II is provided in source form for **FREE** evaluation, for educational use or for peaceful research. If you plan on using **µC/OS-II** in a commercial product you need to contact Micriµm to properly license its use in your product. We provide **ALL** the source code with this application note for your convenience and to help you experience **µC/OS-II**. The fact that the source is provided does **NOT** mean that you can use it without paying a licensing fee. Please help us continue to provide the Embedded community with the finest software available. Your honesty is greatly appreciated.

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