

Q5. By definition of the IEEE754 standard, 32-bit floating point numbers are represented as follows:

S (1 bit)	E (8 bits)	M (23 bits)
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S: Sign bit

E: Exponent

M: Mantissa

Which of the following is the correct “mask bits” in hexadecimal to be used for extracting only the exponent part of the above format? Here, “mask bits” means a bit pattern which is logically ANDed with the 32-bit floating point value.

a) 107FFFFFFF

b) 7F800000

c) FF100000

d) FF800000