

Q22. Which of the following appropriately describes cache memory?

- a) An interrupt occurs if a cache miss is detected when the main memory is accessed, and the program transfers data from the main memory to the cache memory.
- b) The cache memory is used to make up the difference of capacity between the real memory and the virtual memory.
- c) The demand for cache memory is decreasing due to the rapid increase in the access speed of semiconductor memory.
- d) Two methods exist: one is to rewrite both the cache and main memory when a write instruction is executed; the other is to rewrite only the cache memory while the main memory is not rewritten until the data is removed from the cache memory.