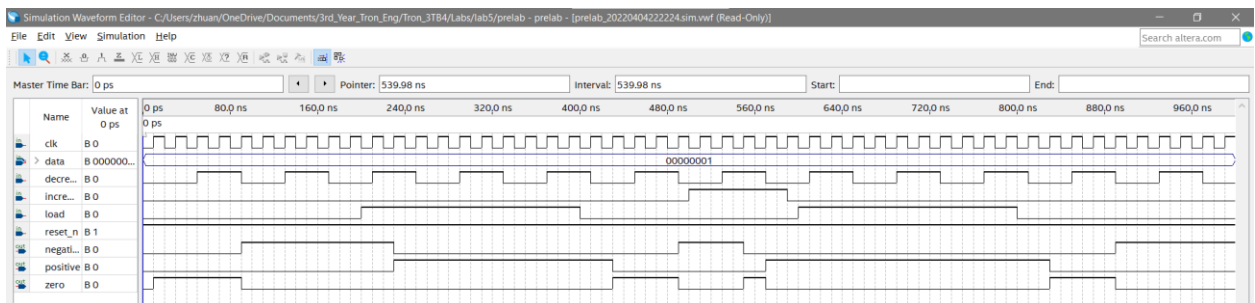
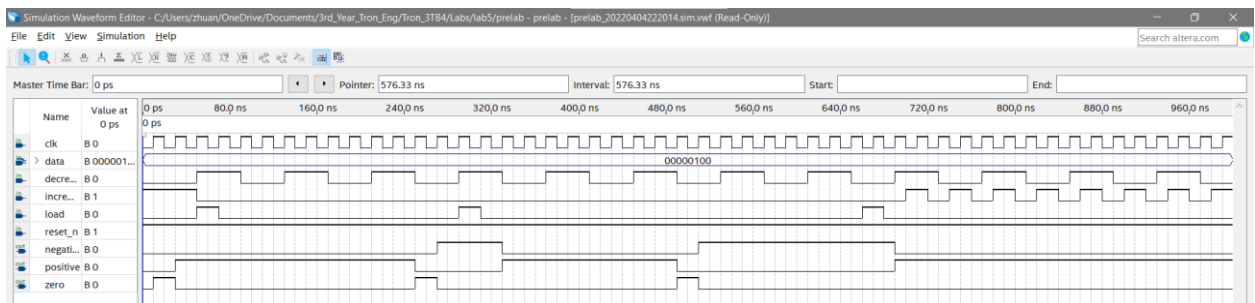
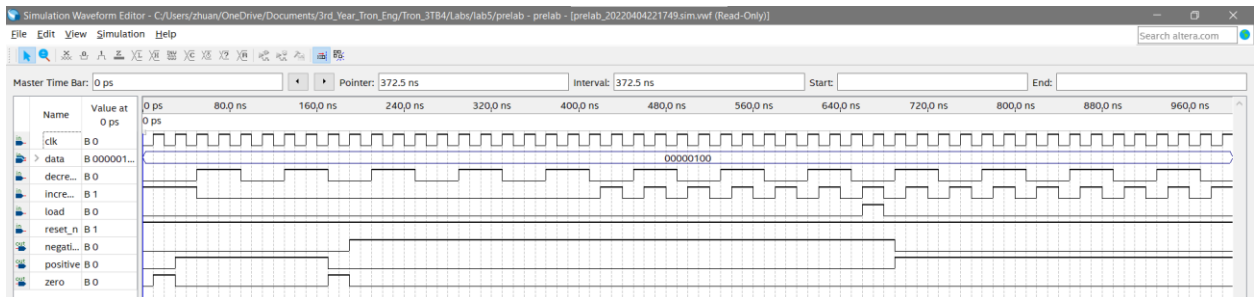
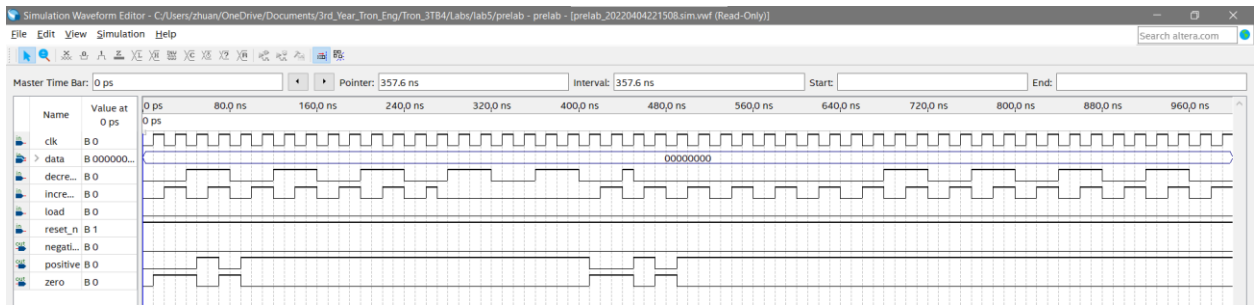
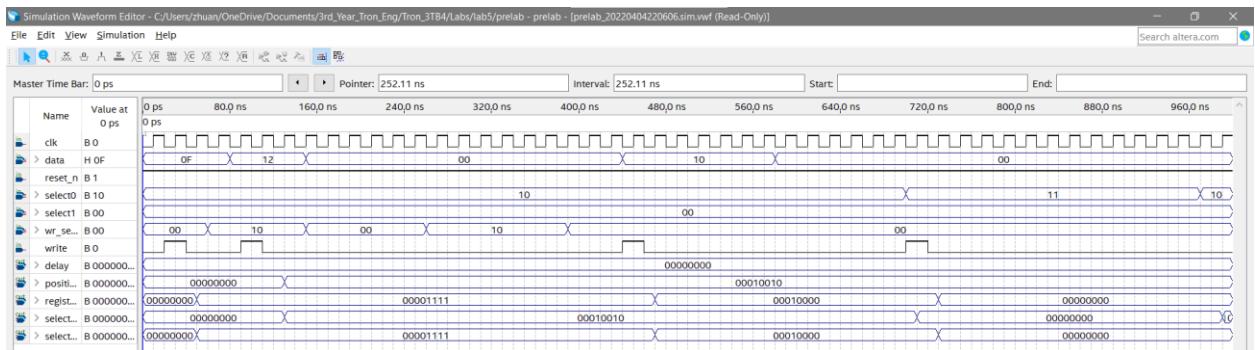
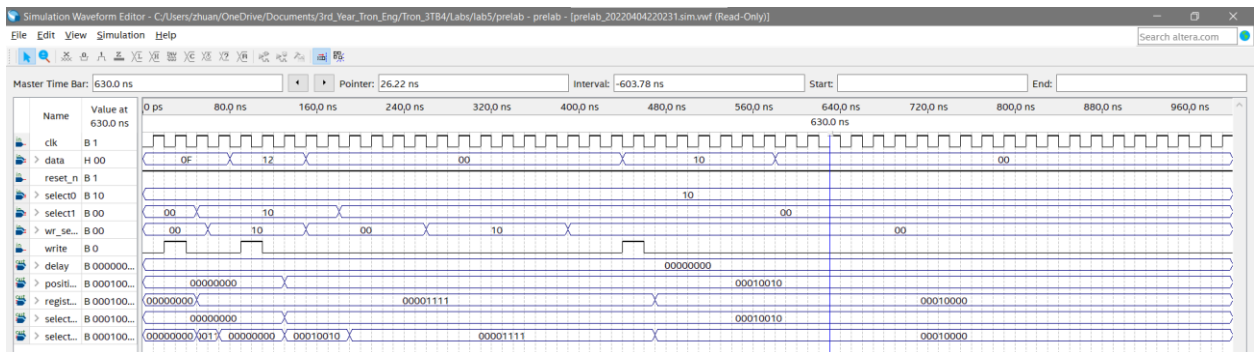
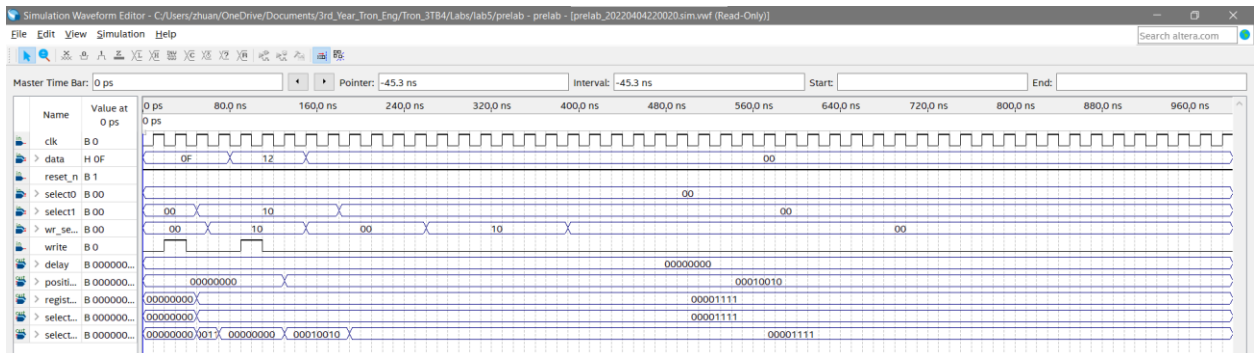
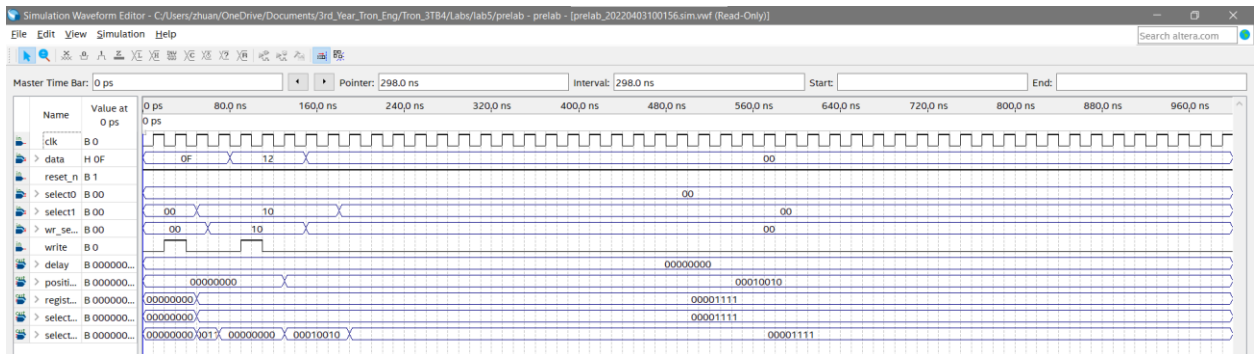


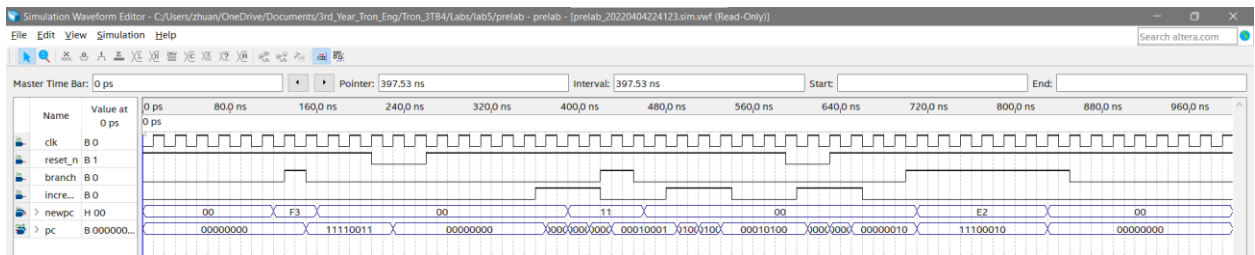
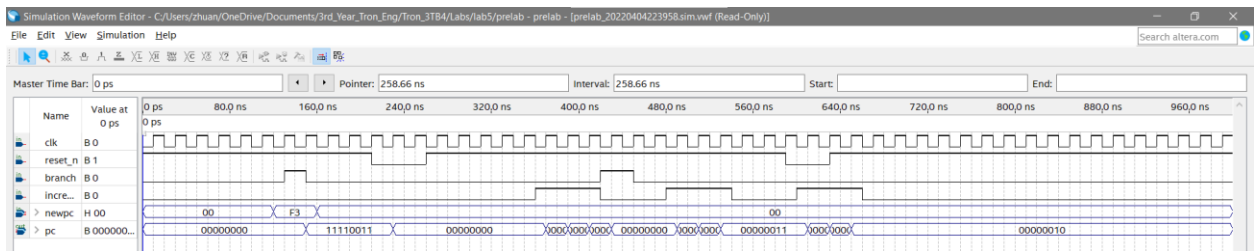
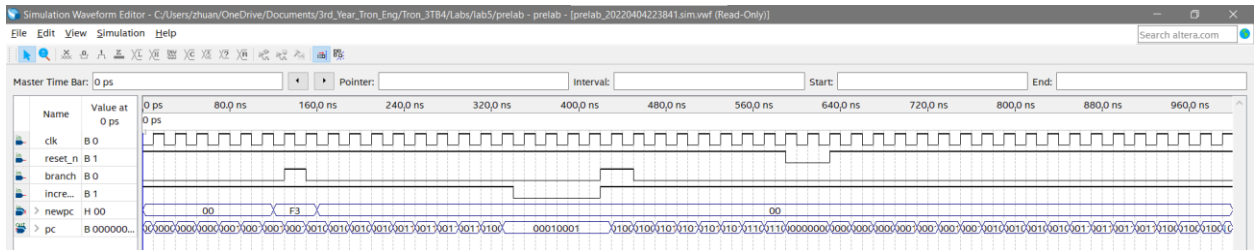
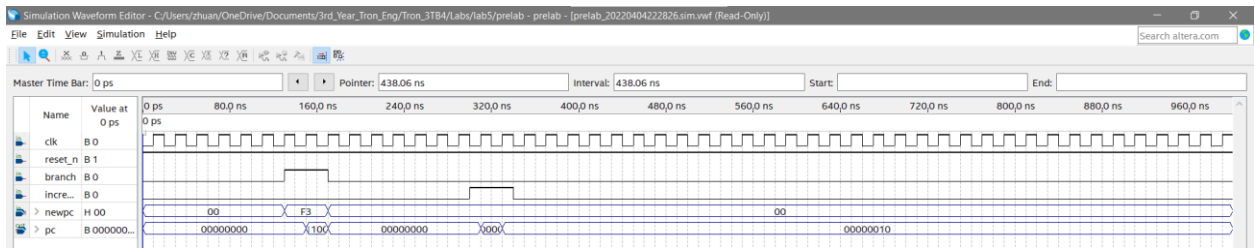
## 1. Temp Register



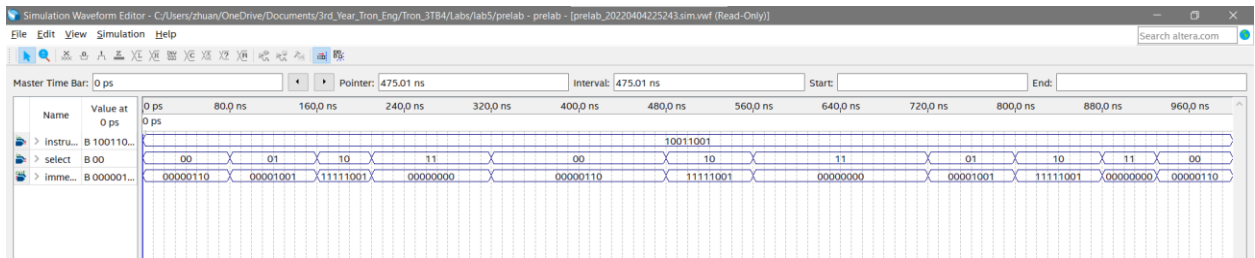
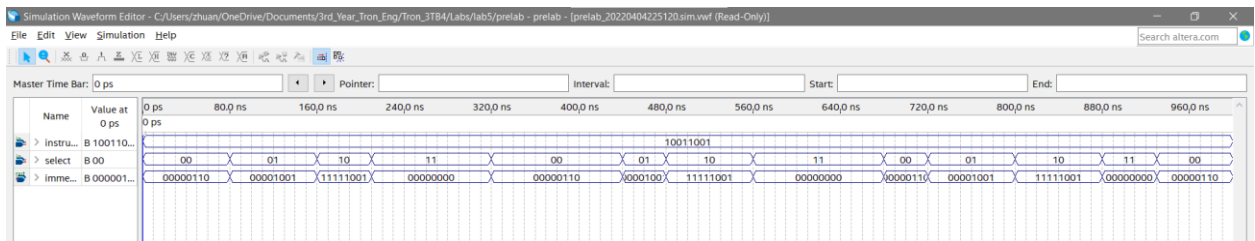
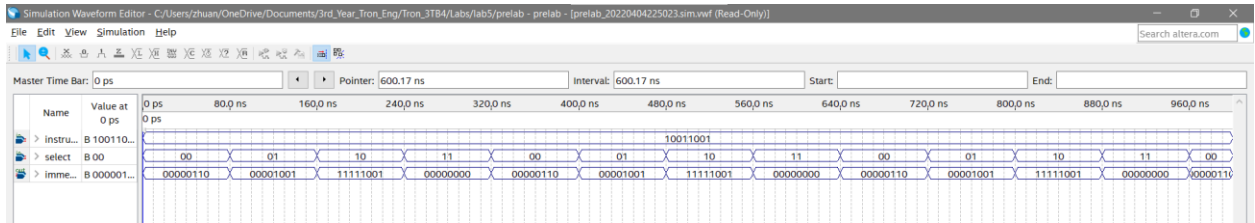
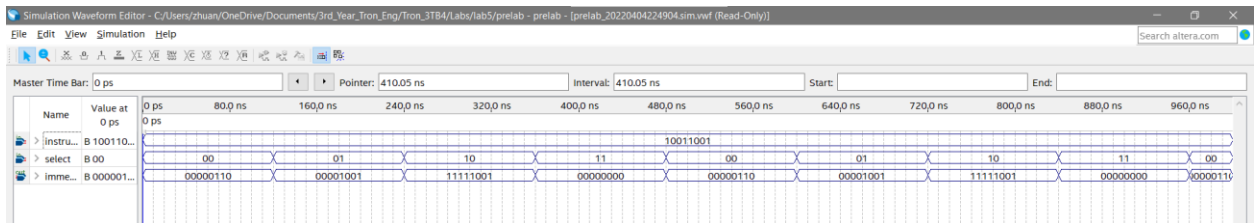
## 2. Register File



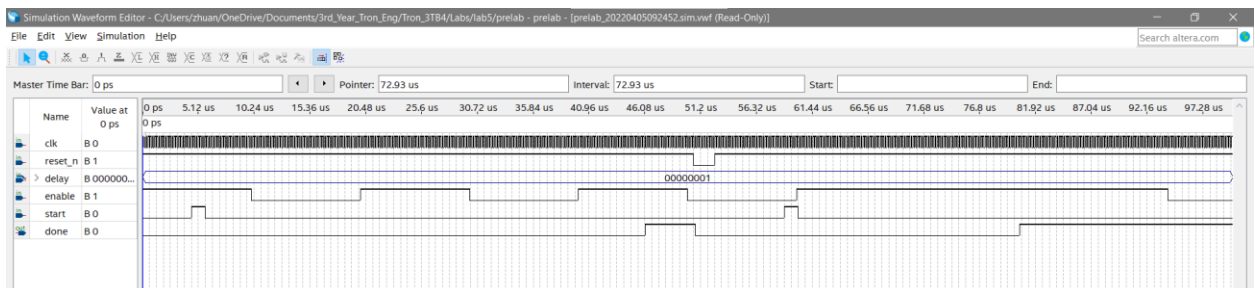
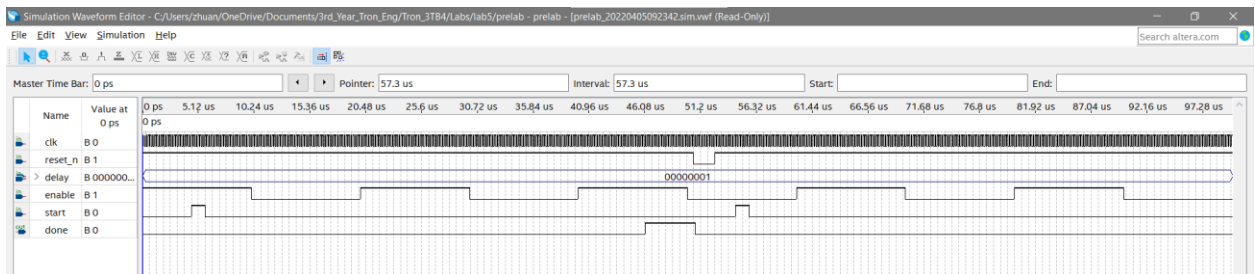
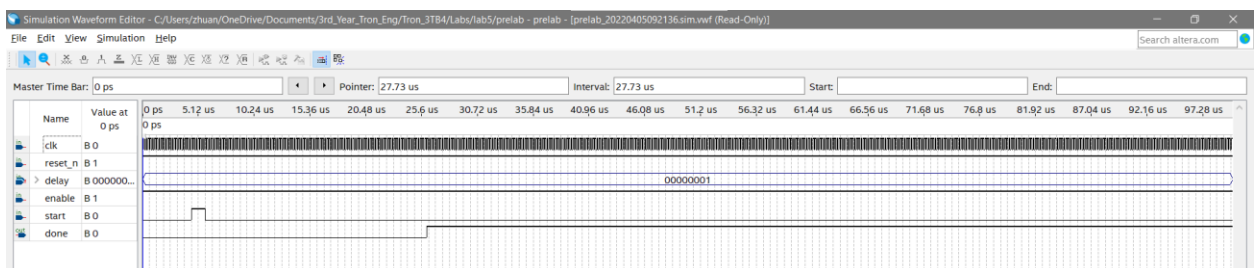
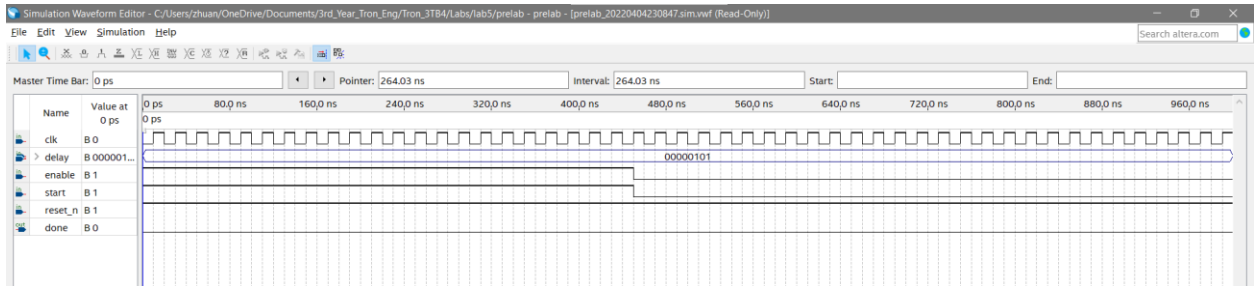
### 3. Program Counter



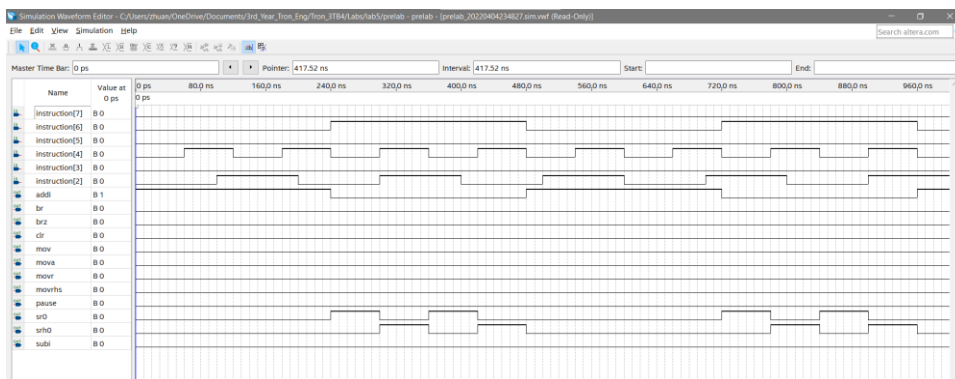
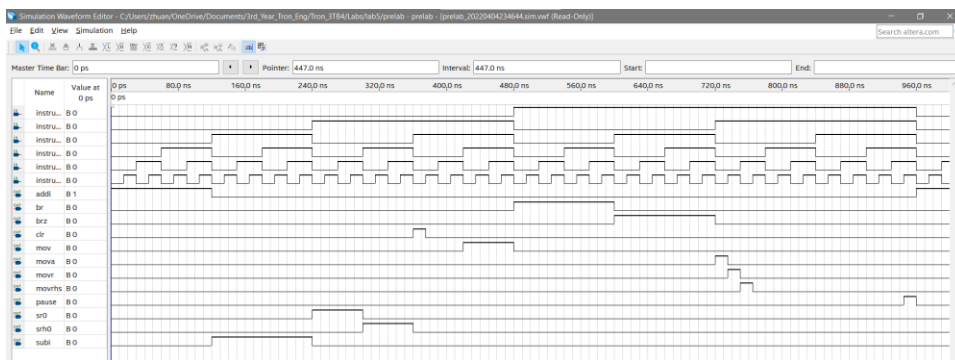
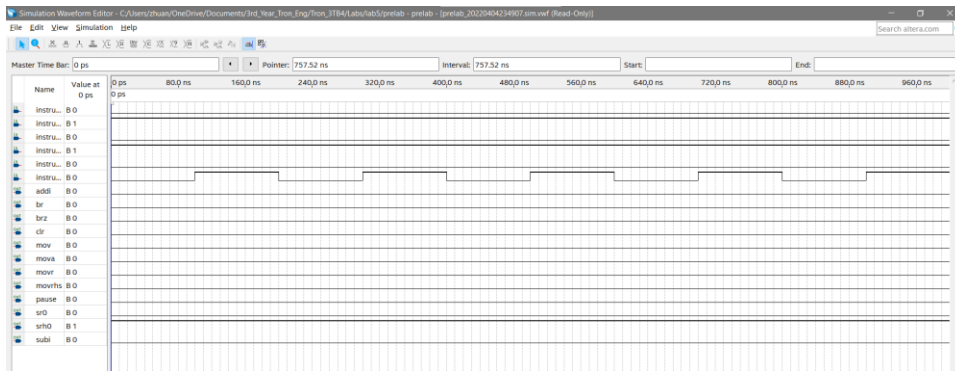
## 4. Immediate Extractor



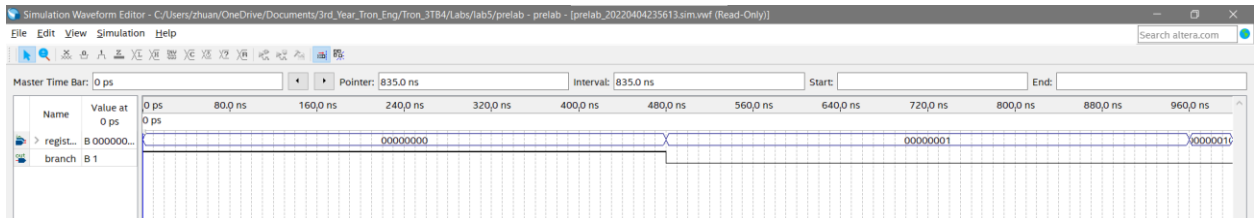
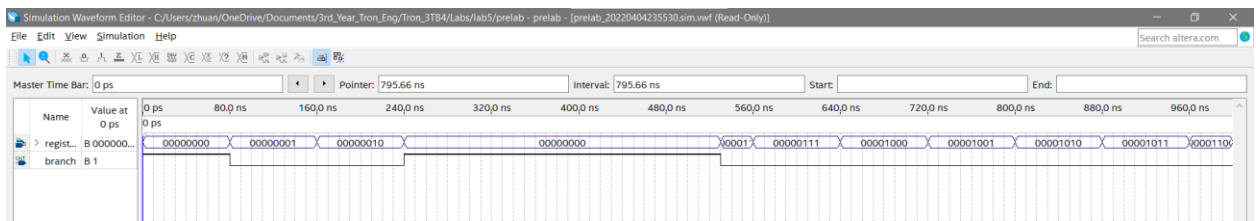
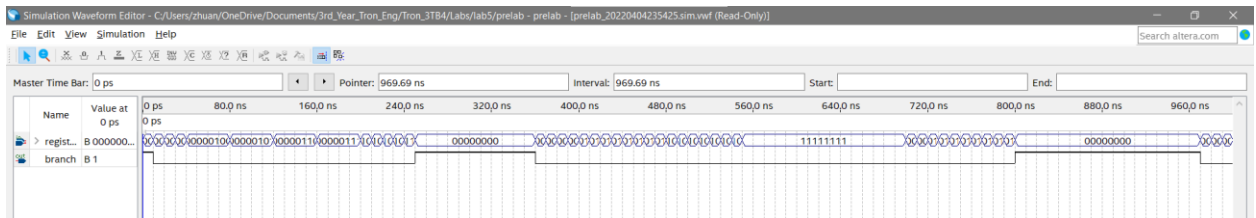
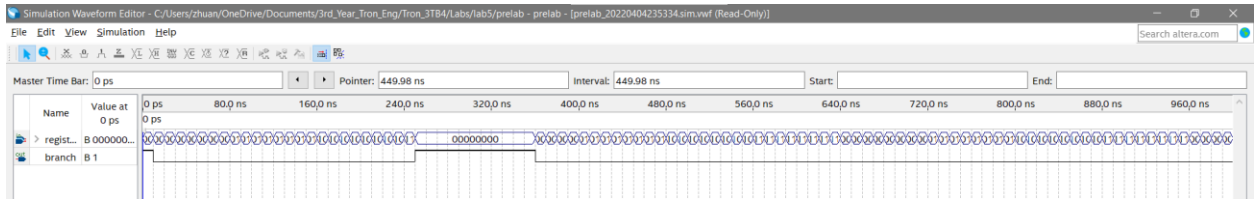
## 5. Delay Counter



## 6. Decoder



## 7. Branch Logic





## 8. ALU

