For the coding section of the lab, outside the overall experience isn't exactly smooth, because it is a language that doesn't have a of information. I find the SystemVerilog PDF on the blackboard to be very helpful. However, there are some things in the PDF that aren't explained well or aren't explained at all. So that is when I started to get help from GPT, where I was able to find out things such as how to do bitwise shifts, make signed bits, and also shorthand to pad a series of bits with 0s.

Besides writing the code, I also noticed that the negative integers are displayed as hexadecimals in the waveforms, and I was having a bit of trouble validating my testbench results.

Here are some screenshots of the waveforms :)