

<b>SUMMARY</b>	A passionate, well-rounded, and experienced computer systems engineering senior with a multitude of different expertise and experiences <b>seeking a full-time opportunity</b>		
<b>EDUCATION</b>	<b>University of Massachusetts Amherst</b> , Amherst, MA Bachelor of Science in Computer Systems Engineering Minor in Engineering Management		GPA 3.65/4.0 Anticipated May 2014
<b>SKILLS</b>	<b>Proficient Languages:</b> C   Java   VB.NET   C# <b>Software:</b> Visual Studio   Eclipse   Git   CLI   Quartus   Wireshark   PSpice <b>Platforms:</b> Microsoft Windows   Mac OS X   Linux <b>Others:</b> Multithreading   Verilog   SQL   LINQ   HTML   CSS		
<b>EXPERIENCE</b>	<b>TeraDiode</b> , Wilmington, MA Summer 2013 Software Engineer Intern Rapidly expanding Startup Company that specializes in high-powered laser cutting <ul style="list-style-type: none"> <li>Worked as a lead in developing <b>computer vision</b> software in <b>C#</b> using the AForge.NET framework</li> <li><b>Architected</b> a back-end framework using fundamental <b>object-oriented design</b> paradigms and a <b>XML</b> framework to easily allow developers to integrate different cameras into new and existing software</li> <li>Wrote multiple back-end camera <b>drivers</b> in this system using <b>multithreading</b> techniques</li> <li>Wrote a fast performing algorithm for creating a <b>nonlinear least squares fit</b> equation that is used to determine the quality of the laser beam for every laser that is shipped to customers</li> <li>Facilitated in <b>saving thousands of dollars</b> by allowing the engineers to switch over to an in-house solution for their cameras and tools from an expensive and poorly supported out-of-house camera and tools solution currently used in R&amp;D and production</li> <li><b>Reduced</b> server load and SQL query time by localizing queries into local memory</li> </ul> <b>Lutron Electronics, Inc.</b> , Coopersburg, PA Summer 2012 Engineering Intern (Windows Systems) Global leader in lighting control systems with an expanding market in shading control systems <ul style="list-style-type: none"> <li>Established a multitude of skills including, but not limited to <b>agile software development</b>, <b>version control</b>, <b>code review</b>, and <b>cross-team communication</b></li> <li>Remodeled software in <b>C</b> for RF Shades that allows multiple versions of the product to utilize common software <b>significantly reducing overhead costs</b> from manufacturing error</li> <li>Added features to existing PC tools to greatly simplify and reduce on-site time for technicians to install/repair shades resulting in better <b>satisfied customers</b></li> <li><b>Directed</b> a PowerPoint presentation based off an Excel spreadsheet I created to correlate events related to shade data to upper-level management</li> </ul>		
<b>PROJECTS</b>	<b>DragonFire</b> Winter 2013 – Spring 2013 <ul style="list-style-type: none"> <li>Designed and implemented an embedded system capable of <b>logging x, y, and z-axis motion</b> requiring extensive knowledge in low-level C, ARM core processors, and SD card technical specs</li> <li>Developed software in <b>Python</b> to analyze the log with a front-end GUI design using wxPython</li> </ul> <b>Network Interface</b> Spring 2013 <ul style="list-style-type: none"> <li>Using <b>FPGAs</b>, <b>Verilog</b>, and <b>C</b>, developed capabilities to allow data packets representing image data to be sent over a Ethernet line using the OSI Model abstraction</li> </ul> <b>Royal Crown Casino Gambling</b> Fall 2012 <ul style="list-style-type: none"> <li>Designed a gambling program in <b>C++</b> with a team of 4 developers while coordinating logistics, mediating discussions, and reinforcing positive feedback</li> <li>Developed portions of the <b>core code</b> and <b>GUI</b> for the game Gin Rummy</li> <li>Documented software using <b>UML</b> diagrams and commenting code</li> </ul> <b>Pipeline Simulator</b> Fall 2012 <ul style="list-style-type: none"> <li>Implemented a <b>C</b> program in a team environment to <b>simulate a 5-stage MIPS pipelined processor</b> with different configurations to see where bottlenecks can occur</li> </ul>		
<b>RELEVANT COURSES</b>	Embedded Systems I, II Software Intensive Engineering Digital Logic Design	Computer Architecture & Design Computer Networks Microprocessors	Electronics I Circuits Digital Systems