List of Deliverables

# #45 Ultra-Fast Failure Test Unit
## Capstone Project

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# Deliverables

## Documentation

1. **Key Documents**

   This includes the Requirements, Constraints and Goals, Proposal, Design and Verification and Validation documents.

2. **User Guide**

   A step-by-step guide to setting up, running, and completing testing using our design. This includes guides on:

   - Hardware interconnections
   - Software downloads: our terminal program and our GUI program
   - List of commands available for configuring project parameters in our terminal program
   - Functionality of our GUI responsible for post-analysis of data

   This document will be included in the document submission for the client.

3. **Project Files and Code**

   All code written, and any other relevant project files. Guides on how to use these will be provided as described above.

4. **Hardware**

   Any hardware necessary for implementing our system will be returned to the client. This includes:

   - Xilinx Zynq Ultrascale+ RFSoC ZCU111 evaluation board including connecting ADC/DAC board