

|                             |   |   |                   |             |            |                        |
|-----------------------------|---|---|-------------------|-------------|------------|------------------------|
| <b>Test Writer:</b>         |   | Steven Zehner   |                   |             |            |                        |
| <b>Test Case Name:</b>      |   | LCD Clear Top Logic   | <b>Test ID #:</b> |             |            | Clear Top Logic Test 1 |
| <b>Description:</b>         |   | Verify operation of the vLCD_CLEAR_TOP function with Logic Analyzer | <b>Type:</b>      |             |            | White Box              |
|                             |   |   |                   |             | X          | Black Box              |
| <b>Tester Information</b>   |   |   |                   |             |            |                        |
| <b>Name of Tester:</b>      |   |   | Date:             |             |            |                        |
| <b>Hardware Ver:</b>        |   | 1.0   | Time:             |             |            |                        |
| <b>Setup :</b>              | Connect STK600 Port_K_ to the Logic Analyzer. A task is created that calls the vLCD_CLEAR_TOP() function. |   |                   |             |            |                        |
| <b>Test</b>                 | <b>(Test Variable)</b>  | <b>Expected Result</b>  | <b>Pass</b>       | <b>Fail</b> | <b>N/A</b> | <b>Comments</b>        |
| 1                           | Return Home   | Logic Analyzer Shows 0b00000010                                     |                   |             |            |                        |
| 2                           | Write Spaces  | Logic Analyzer Shows 0b00100000                                     |                   |             |            |                        |
| 3                           | Return Home   | Logic Analyzer Shows 0b00000010                                     |                   |             |            |                        |
| <b>Overall Test Result:</b> |   |   |                   |             |            |                        |