

Test Writer:	Steven Zehner					
Test Case Name:	LCD Clear Bottom Logic		Test ID #:			Clear Bottom Logic Test 1
Description:	Verify operation of the vLCD_CLEAR_BOTTOM function with Logic Analyzer	Type:				White Box
			X			Black Box
Tester Information						
Name of Tester:		Date:				
Hardware Ver:	1.0	Time:				
Setup :	Connect STK600 Port_K_ to the Logic Analyzer. A task is created that calls the vLCD_CLEAR_BOTTOM() function.					
Test	(Test Variable)	Expected Result	Pass	Fail	N/A	Comments
1	Return Home on Bottom	Logic Analyzer Shows 0b11000000				
2	Write Spaces	Logic Analyzer Shows 0b00100000				
3	Return Home on Bottom	Logic Analyzer Shows 0b11000000				
Overall Test Result:						