WEJŚCIE					GPU						WYJŚCIE			
BUF_1	p_1	[...]	p_n	→		w_1_1	[...]	w_1_n	→		BUF_1	p_1	[...]	p_n
BUF_2	p_1	[...]	p_n	→		w_2_1	[...]	w_2_n	→		BUF_2	p_1	[...]	p_n
BUF_3	p_1	[...]	p_n	→	GRID	w_3_1	[...]	w_3_n	→		BUF_3	p_1	[...]	p_n
BUF_4	p_1	[...]	p_n	→		w_4_1	[...]	w_4_n	→		BUF_4	p_1	[...]	p_n
BUF_5	p_1	[...]	p_n	→		w_5_1	[...]	w_5_n	→		BUF_5	p_1	[...]	p_n
[...]				→		[...]			→		[...]			
BUF_m	p_1	[...]	p_n	→		w_m_1	[...]	w_m_n	→		BUF_m	p_1	[...]	p_n